ProofPower

Compliance Tool — Proving VCs
Information on the current status of ProofPower is available on the World-Wide Web, at URL:

http://www.lemma-one.demon.co.uk/ProofPower/index.html

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ABOUT THIS PUBLICATION

0.1 Purpose

This document gives guidance on the use of the Compliance Tool proof facilities supplied with ProofPower.

0.2 Readership

This document is intended to be read by users of the Compliance Tool who wish to produce machine-checked proofs of some or all of the VCs generated by the tool. These users are expected to have experience of using ProofPower for proofs in Z; in particular they should be familiar with the material in the ProofPower Z Tutorial [1].

0.3 Related Publications

A bibliography is given towards the end of this document.

- How to use the Compliance Tool is described in:
  Compliance Tool — User Guide [4].

- The syntax and semantics of the Compliance Notation as supported by the Compliance Tool is described in:
  Compliance Notation — Language Description [6]

- A description of ProofPower may be found in:
  ProofPower Software and Services [3],
  which also contains a full list of other ProofPower documentation.

- How to use ProofPower for formal reasoning about Z specifications may be found in:
  ProofPower Z Tutorial [1].

0.4 Area Covered

The user might typically have already undertaken the following tasks:

1. Installed the Compliance Tool on his workstation, by following the procedure described in the Compliance Tool — Installation and Operation [5]
2. Loaded a sequence of Compliance Notation scripts into the tool and generated the Z documents
   from the scripts, by following the procedure described in the *Compliance Tool — User Guide*
   [4]

This tutorial should then assist the user in working with the VCs, i.e. attempting to prove them,
using ProofPower and Compliance Tool facilities. A Compliance Notation example concerning the
computational aspects of a simple calculator is included in this tutorial, see chapter 4. Proofs are
provided for some of the VCs generated by this example to illustrate the techniques advocated in
chapter 3.

0.5 Prerequisites

This Tutorial is designed to assist a Compliance Tool user in the production of machine-checked
proofs of the VCs generated by the tool. It is *not* intended to be an introduction to the Z language,
or to the Compliance Notation, or indeed to the Compliance Tool itself.

Familiarity with the Compliance Notation is very desirable, although not essential since VC proofs
may be conducted independently without prior knowledge of the specification from which the VCs
have been generated. Familiarity with the Compliance Tool and with the use of ProofPower for
proofs in Z is essential. It is assumed that a user intent on using the Compliance Tool for proving
VCs will be familiar with the material in both the *ProofPower Z Tutorial* [1] and the *Compliance
Tool — User Guide* [4].

The *Compliance Notation — Language Description* [6] describes the syntax and semantics of the
Compliance Notation. The *Compliance Tool — User Guide* [4] gives an introduction to the use of
the Compliance Tool for loading a Compliance Notation Script and generating the Z document which
contains the VCs. The *ProofPower Z Tutorial* [1] gives an introduction to the use of ProofPower for
specification and proof in Z.

The ProofPower user documentation is supplied as part of the ProofPower release included with the
Compliance Tool and is available for on-line reference.

0.6 Acknowledgements

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bution tape is copyright the \(\LaTeX\) 3 project and its individual authors.

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This tutorial has been designed to assist in the proof of VCs generated by the Compliance Tool. It is divided into three main areas:

- Chapter 2 provides a review of how to do proof in Z. Familiarity with the ProofPower Z Tutorial [1] is considered to be essential before embarking on VC proofs, see section 0.5, but the ProofPower Z Tutorial [1] covers a lot more than proof in Z. The material presented in chapter 2 concentrates on the proof aspects of ProofPower-Z. There are also some explicit examples in Z of material covered fully in the ProofPower HOL Tutorial Notes [2] but only briefly mentioned in the ProofPower Z Tutorial [1]. For example, linear arithmetic proofs in ProofPower-Z are very similar to those in ProofPower-HOL and as such are not covered in detail in the ProofPower Z Tutorial [1].

- Chapter 3 overviews the extra proof support available in the Compliance Tool and describes how to use these facilities to tackle VC proofs.

- Chapter 4 provides an example sequence of literate scripts concerning the computational aspects of a simple calculator. Selected VCs generated from these scripts are then proven, illustrating the use of techniques described in section 3.

For reference purposes, a complete listing of the calculator example theories is in appendix A. The SPARK program generated by the calculator example literate scripts is in appendix B.

As with many mathematical activities, proving VCs is best learnt by doing rather than reading. The recommended way of using this document is to work through the examples in an interactive session with the Compliance Tool. The source of this document is provided as part of the Compliance Tool release (as $PPINSTALLDIR/docs/usr503.doc) to help you do this.
This chapter provides an overview of the material in the **ProofPower Z Tutorial** [1] which describes how to use **ProofPower-Z** for doing proofs. It is assumed, in particular, that you have some familiarity with:

- how to do backwards proofs, using `set_goal` and work with the theorems you have proved using `pop_thm` and `save_pop_thm`
- proof contexts. The following have been used in the examples:
  - `set_pc`, `push_pc`, `pop_pc`
  - `z_library1`, `z_library1_ext`
  - `z_lin_arith`
  - `z_predicates`

The example proofs in this chapter are all conducted in the proof context `z_library1`.

- tactics and tacticals, etc. The following have been used in the examples:
  - **tactics**:
    - `strip_tac`, `z_∀_tac`
    - `asm_rewrite_tac`
    - `lemma_tac`, `cases_tac`
    - `z_spec_asm_tac`
    - `ante_tac`, `discard_tac`
    - `fc_tac`, `all_fc_tac`, `asm_fc_tac`, `all_asm_fc_tac`
    - `eq_sym_asm_tac`, `eq_sym_nth_asm_tac`
    - `var_elim_asm_tac`, `var_elim_nth_asm_tac`
    - `all_var_elim_asm_tac`, `all_var_elim_asm_tac1`
    - `z_app_eq_tac`
    - `z_≤_induction_tac`
  - **tacticals**:
    - `REPEAT`
    - `ALL.FC.T`, `ALL.FC.T1`
    - `⇒_T`
    - `THEN`, `THEN1`
    - `PC.T1`
    - `LEMMA.T`
    - `DROP.NTH.ASM.T`, `LIST_DROP.NTH.ASM.T`

- **canonicalisation functions**:
  - `fc⇔canon`

- about forward inference: `rewrite_rule` and `∧_right_elim` have been used in the examples
- how to access the specification with `z_get_spec`
You will find detailed information on all the above in the ProofPower Reference Manual [8].

A full account of using ProofPower-Z for proofs may be found in the ProofPower Z Tutorial [1], which, in turn, refers to the ProofPower HOL Tutorial Notes [2]. There are some topics, for example linear arithmetic, which are fully covered in the ProofPower HOL Tutorial Notes [2] but to which no special treatment coverage is given in the ProofPower Z Tutorial [1]. This is because there is nothing extra to add about working in Z as opposed to HOL.

While it is assumed that you are familiar with the material presented in these tutorials, for convenience, a summary of the main methods advocated for dealing with Z proofs is given here. The recommended way of revising the material in this chapter is to read and try out the examples as you go along. You may also find it instructive to attempt the Z exercises from chapter 7 of the ProofPower Z Tutorial [1], the solutions to which are in chapter 8 of that tutorial.

The revision material is divided into the following sections:

2.1 Proof by the “two tactic” method.
2.2 Stripping.
2.3 Automatic Proof.
2.4 Forward Chaining.
2.5 Predicate Calculus with Equality.
2.6 Rewriting.
2.7 Function Application.
2.8 Proving Lemmas “on the fly”.
2.9 Case Analysis.
2.10 Induction.

2.1 The Two Tactic Method

This method is given a high priority in the ProofPower Z Tutorial [1], although more for pedagogical reasons than because it is a particularly natural way to tackle a proof. Proof by stripping, see section 2.2, is effective in discharging a goal only where the reasoning is mainly propositional. Where the proof will depend either on appropriate specialisation of universally quantified assumptions, or on the choice of a suitable witness for proving an existential conclusion, stripping will not suffice.

The two tactic method injects into the proof process based on stripping, user directed specialisation of universal assumptions. In the context of a proof by contradiction (in which existential conclusions will not arise) this is sufficient to discharge any goals which are reduced to reasoning in the first order predicate calculus. The method is sometimes unnatural because it destroys much of the logical structure of the original goal. Schematically the method is:

```
sml
| set_goal([], conjecture);
| a contr_tac; (* once suffices *)
| a (z_spec_asm_tac \& assumption \& \& value); (* as many times as necessary *)
```
The choice of universal assumptions and of the values to specialise them to depends on the user identifying one or more specialisations which will result in the derivation of a contradiction from the assumptions. For example, this method transforms a goal with an existentially quantified conclusion into one with a universally quantified assumption:

```
SML

set_goal([],\(\exists X [\forall x,y:X (\exists x:X \bullet x = y)]\));
a contr_tac;
a (z_spec_asm_tac \(\forall x:X \bullet \neg x = y \downarrow\neg y\));
pop_thm();
```

ProofPower output

```
Tactic produced 0 subgoals:
Current and main goal achieved
```

2.2 Stripping

This method is complete for propositional logic. The tactic `strip_tac` performs a variety of simplifications, and is often usefully applied at the outset of embarking on a proof. The simplifications achieved by `strip_tac` include the following:

- moving the antecedent of an implication from the conclusion to the assumptions of the goal
- proving tautologies
- removing leading universal quantifiers
- using, where possible, relevant assumptions in the assumption-list.

This is often a more natural way to start a proof because it retains some of the structure of the original goal. For example, the proof above in section 2.1 could have been achieved by stripping then applying `z_∃_tac` with a suitable witness then stripping the trivial result:

```
SML

set_goal([],\(\exists X [\forall x,y:X (\exists x:X \bullet x = y)]\));
a (REPEAT strip_tac);
a (z_∃_tac ∋ y ∋);
a (REPEAT strip_tac);
pop_thm();
```

ProofPower output

```
Tactic produced 0 subgoals:
Current and main goal achieved
```
2.3 Automatic Proof

An automatic proof procedure, in the form of `prove_tac`, is provided for each proof context. In most proof contexts this is capable of solving results which are reducible to simple theorems of the predicate calculus. For example, the proof context `z_library1_ext` reduces the subset relation to a universally quantified membership statement:

```
SML
set_goal([], Z A ⊆ B ∧ B ⊆ C ⇒ A ⊆ C);
a(PC_T1"z_library1_ext"prove_tac[]);
```

ProofPower output

*Tactic produced 0 subgoals:
Current and main goal achieved

```
SML
set_goal([], {x,y:Z | x<y} ⊆ {x,y:Z | x<y ∨ x>y+99});
a(PC_T1"z_library1_ext"prove_tac[]);
```

ProofPower output

*Tactic produced 0 subgoals:
Current and main goal achieved

Even when the application of `prove_tac` fails to prove a goal, it may have resulted in more simplification than would be obtained by other methods. It is also conceivable that if `prove_tac` fails to achieve a proof it may unnecessarily split the goal into subgoals. In this case it would probably be better to undo the application of `prove_tac` and try another approach to solving the goal.

2.3.1 Linear Arithmetic Proof Context

The proof context `z_lin_arith` contains an automatic proof procedure for linear arithmetic. This means terms built up from:

- “Atoms” (numeric literals, variables of type `Z`, etc.)
- Multiplication by numeric literals
- Addition
- `=, ≤, ≥, <, >`
- Logical operators

So, for example, the following are all proved by an application of `prove_tac` in the proof context `z_lin_arith`:

```
SML
set_goal([], ∀x,y,z:Z | x ≤ y ∧ x + y < z + x • x < z);
a(PC_T1"z_lin_arith"prove_tac[]);
pop_thm();
```
2.4 Forward Chaining

Forward chaining facilities often provide an easier way of achieving proofs requiring instantiation of universal assumptions.

all_asm_fc_tac will attempt to instantiate universally quantified assumptions which are effectively implications to values which will enable forward inference to take place. This is achieved by matching the antecedent of the implication against other assumptions.

Consider the example in section 2.1. After the application of contr_tac, you can derive the required contradiction using all_asm_fc_tac with less effort than having to specialise the universally quantified assumption:

```
SML
set_goal([], \(\forall x,y:Z \mid x + 2*y < 2*x * y + y < x\));
a(contr_tac);
a(all_asm_fc_tac);
pop_thm();
```

ProofPower output

```
Tactic produced 0 subgoals;
Current and main goal achieved
```

If forward chaining fails to solve a goal, it may generate irrelevant new assumptions, and so it should be used judiciously.

A related tactic suitable for use with Z is all_fc_tac, which chains forward using implications derived from a list of theorems supplied as an argument, matching these against the assumptions, using the assumptions to match the antecedents of the implications.

fc_tac and asm_fc_tac are also useful (see ProofPower Reference Manual [8]), but these are liable to introduce HOL universal quantifiers, leaving a mixed language subgoal.
Chapter 2. REVIEW OF PROOF IN Z

2.5 Predicate Calculus with Equality

A variety of additional proof facilities are available to make use of equations.

1. *asm_rewrite_tac*
   
   may be used to cause equations in the assumptions to rewrite the conclusion of a subgoal. This may sometimes prove sufficient to complete a proof.

2. *eq_sym_asm_tac* or *eq_sym_nth_asm_tac*
   
   may be used to turn round an equation in an assumption which is the wrong way round to achieve the required rewrite.

3. *var_elim_asm_tac* or *var_elim_nth_asm_tac*
   
   may be used to completely eliminate from the subgoal occurrences of a variable which appears on one side of an equation in the specified assumption. This causes the conclusion and all the other assumptions to be rewritten with the equation, eliminating occurrences of it. The assumption will then be discarded. These tactics will work whichever way round the equation appears in the assumption.

4. *all_var_elim_asm_tac*, *all_var_elim_asm_tac1*
   
   automatically eliminate from the assumptions all equations of a sufficiently simple kind, by rewriting every term in the subgoal with them and then discarding the equations. They avoid eliminating equations where this might cause a looping rewrite. The first variant only eliminates equations where both sides are either variables or constants, the second variant will eliminate any equation of which one side is a variable which does not appear on the other side.

2.6 Rewriting

Rewriting using any collection of theorems from which equations are derivable is supported by the standard HOL rewriting facilities (*rewrite_tac* etc.), see the *ProofPower Reference Manual* [8] for details, using Z specific preprocessing of the rewrite theorems (supplied in the Z proof contexts).

Many Z paragraphs give rise to predicates which can be used without further preparation by these standard rewriting facilities. This applies to given sets, abbreviation definitions and schema definitions.

Axiomatic descriptions, and generic axiomatic descriptions will result in equations which are likely to be effectively conditional. In such cases it is necessary to establish the applicability of the rewrite before it can be undertaken.

One way of achieving this is by forward chaining using the conditional equation after establishing the relevant condition. The relevant conditions are usually the membership assertions corresponding to the declaration part of the outer universal quantifier on the theorem to be used for rewriting.

For example, to prove the goal:

```sml
|set_goal([(], $ \forall i : \mathbb{N} \bullet abs i = abs \sim i$));
```

using theorem *z_abs_thm* (which is $\vdash \forall i : \mathbb{N} \bullet abs i = i \land abs \sim i = i$). First strip the goal:
In more complicated cases the proof of the required conditions may be non-trivial, often because reasoning about membership of expressions formed with function application is involved. The next section describes the proof support available for use in such cases.

2.7 Function Application

Reasoning at a low level, \( z_{\text{app}_\text{eq}_\text{tac}} \) may be used to reduce an equation involving an application to sufficient conditions for its truth, in terms of the membership of the function, e.g.:

SML

\[
\text{set_goal}(\forall f \; a : U \mid (a, f \cdot a) \in f \land (a, v) \in f) \wedge (a, v) \in f); \]
\[
\text{a z_{app_eq_tac};} \]

The first conjunct of this result is needed to ensure that \( f \) is functional at \( a \) (i.e. maps \( a \) to only one value). In the case that \( f \) is known to be a function, the theorem \( z_{\text{fun}_\text{app}_\text{clauses}} \) may be used with forward chaining, avoiding the need to prove that \( f \) is functional at \( a \).
In this case the result \((a, v) \in f\) would have to be proven and added to the assumptions before undertaking the forward chaining, e.g.:

\[
\begin{align*}
\text{SML} & & \text{drop_main_goal();} \\
& & \text{set_goal([], \(\forall X, Y \in f \Rightarrow f x = y\));} \\
& & \text{a (REPEAT z_strip_tac);} \\
\end{align*}
\]

ProofPower output

\[
\begin{align*}
\text{...} \\
\text{(* 4 *)} & \quad \forall f \in X \rightarrow Y \\
\text{(* 3 *)} & \quad \forall x \in X \\
\text{(* 2 *)} & \quad \forall y \in Y \\
\text{(* 1 *)} & \quad \forall (x, y) \in f \\
\text{(* ?? *)} & \quad \exists f x = y \\
\end{align*}
\]

\[
\text{SML} & & \text{a (allfc_tac [z_fun_app_clauses]);} \\
& & \text{pop_thm();} \\
\]

ProofPower output

\[
\text{Tactic produced 0 subgoals:} \\
\text{Current and main goal achieved}
\]

A common problem is to have to establish that the value of some expression formed by application falls within some particular set. This is often needed to establish the conditions necessary for use of a rewriting equation on the expression.

In these circumstances the theorem \(z\_fun\_app\_clauses\) may be used:

\[
\begin{align*}
\text{val z_fun_app_clauses =} & \quad \forall f : U; x : U; y : U; X : U; Y : U \\
\text{  \bullet (} & \quad (f \in X \rightarrow Y \lor f \in X \rightarrow Y \lor f \in X \rightarrow Y \lor f \in X \rightarrow Y) \land x \in X \\
\text{  \Rightarrow} & \quad f x \in Y) \\
\text{  \land (} & \quad (f \in X \rightarrow Y \lor f \in X \rightarrow Y \lor f \in X \rightarrow Y) \land x \in \text{dom } f \\
\text{  \Rightarrow} & \quad f x \in Y): \text{THM}
\end{align*}
\]
The claim that a global variable is a member of a function space will often be obtained from the specification of the constant (as part of the predicate implicit in the declaration part of the specification). Where the function is an expression the result is likely to have been established by forward inference using similar methods.

For example, consider the following specifications:

\[
\begin{align*}
\text{Z} &::= \text{CONSTSPEC : } \mathbb{P}_1 \ T \rightarrow \mathbb{N} \\
\text{true}
\end{align*}
\]

where we are required to demonstrate that \( \text{CONSTSPEC} \) applied to something in \( T \) is in the set \( \mathbb{N} \). From \( z\_fun \in \_ \_ \_ \_ \_ \_ \_ clauses \) we have that given an \( f \in X \rightarrow Y \) and an \( x \in X \), then we can conclude that \( f \ x \in Y \). The current proof context, \( z\_library1 \), will be too aggressive for our purposes because it will expand the definition of \( P_1 \), the \( X \) in this case. So, we will use the proof context \( z\_predicates \) for this example:

```
SML
push_pc"z\_predicates";
set_goal([], \( \forall \ x: P_1 \ T \bullet \text{CONSTSPEC} \ x \in \mathbb{N} \) );
a(REPEAT strip_tac);
```

ProofPower output

```
\[
\begin{align*}
(\ast \ 1 \ \ast ) & \ \ \ x \in \mathbb{P}_1 \ T^{-1} \\
(\ast \ ?\!- \ \ast ) & \ \ \ \text{CONSTSPEC} \ x \in \mathbb{N}^{-1}
\end{align*}
\]
```

We need the fact about \( \text{CONSTSPEC} \) which is found in its defining declaration to make the required inference.

This is added to the assumptions as follows:

```
SML
a (strip_asm_tac (z\_get\_spec \( \forall \text{CONSTSPEC} \) ));
```

ProofPower output

```
\[
\begin{align*}
(\ast \ 2 \ \ast ) & \ \ \ x \in \mathbb{P}_1 \ T^{-1} \\
(\ast \ 1 \ \ast ) & \ \ \ \text{CONSTSPEC} \in \mathbb{P}_1 \ T \rightarrow \mathbb{N}^{-1} \\
(\ast \ ?\!- \ \ast ) & \ \ \ \text{CONSTSPEC} \ x \in \mathbb{N}^{-1}
\end{align*}
\]
```

Next we forward chain using the theorem \( z\_fun \in \_ \_ \_ \_ \_ \_ \_ clauses \), which suffices to discharge the goal.
2.8 Using Lemmas

Use of the tactic lemma_tac may give a more natural feel to a proof. It allows you to state and prove a lemma “on the fly”. This will generate at least two subgoals - one the statement of the lemma and the rest the result of stripping the lemma into the assumptions. For example:

SML
\[
\text{set_goal(}([], \forall x, y : \mathbb{Z} \mid x \leq y \land x = y \Rightarrow P(x,y))];
\]
\[
a(\text{REPEAT} \text{ strip_tac});
\]

ProofPower output

\[
\text{Tactic produced 2 subgoals:}
\]
\[
(* \ 3 \ *) \ \exists x, y : \mathbb{Z} \mid x \leq y \land P(x,y)
\]
\[
(* \ 2 \ *) \ \exists x = y
\]
\[
(* \ 1 \ *) \ \exists x \leq y
\]
\[
(* ?! \ *) \ \exists P(x,y)
\]

SML
\[
a(\text{lemma_tac} \exists x \leq y);
\]

ProofPower output

\[
\text{Tactic produced 2 subgoals:}
\]
\[
(* \ *** \ \text{Goal } "2" \ *** \ *)
\]
\[
(* \ 3 \ *) \ \exists x, y : \mathbb{Z} \mid x \leq y \land P(x,y)
\]
\[
(* \ 2 \ *) \ \exists x = y
\]
\[
(* \ 1 \ *) \ \exists x \leq y
\]
\[
(* \ ?! \ *) \ \exists P(x,y)
\]
\[
(* \ *** \ \text{Goal } "1" \ *** \ *)
\]
2.8. Using Lemmas

\[ \forall x, y : \mathbb{Z} \mid x \leq y \implies P(x, y) \]
\[ \forall x = y \]
\[ \forall x \leq y \]
\[ \forall P(x, y) \]

Rewriting with the assumptions will solve the first goal:

SML

\[ a(\text{asm_rewrite_tac[]}[]); \]

ProofPower output

Tactic produced 0 subgoals:
Current goal achieved, next goal is:

(* *** Goal "2" *** *)
...

and forward chaining will solve the second:

SML

\[ a(\text{all_asm_fc_tac[]}[]); \]
\[ \text{pop_thm}(); \]

ProofPower output

Tactic produced 0 subgoals:
Current and main goal achieved
...

In this example, one tactic solved the lemma. If you apply this with THEN1, you can avoid the subgoal split:

SML

\[ \text{set_goal}([], \forall x, y : \mathbb{Z} \mid x \leq y \implies P(x, y)) \land x = y \implies P(x, y))); \]
\[ a(\text{REPEAT strip_tac}); \]
\[ a(\text{lemma_tac} x \leq y \text{ THEN1 asm_rewrite_tac[]}[]); \]

ProofPower output

Tactic produced 1 subgoal:

(* *** Goal "" *** *)

(* 3 *) \[ \forall x, y : \mathbb{Z} \mid x \leq y \implies P(x, y) \]
(* 2 *) \[ \forall x = y \]
(* 1 *) \[ \forall x \leq y \]
(* ?! *) \[ \forall P(x, y) \]
...

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Applying `lemma_tac` is equivalent to applying `LEMMA_T strip_asm_tac`. `LEMMA_T` has an argument of type `THM -> TACTIC` telling you what to do with the new assumption. Sometimes, you really want to put the lemma into the assumptions in exactly the way you have formulated it: `LEMMA_T asm_tac` will achieve this. Another common thing you might want to do is to rewrite your goal with the new assumption: `LEMMA_T rewrite_thm_tac` will achieve this.

### 2.9 Case Analysis

The tactic `cases_tac` condition lets you reason by cases according as a chosen condition is true or false. Consider the example from section 2.8 that was progressed with `lemma_tac`. This time, we will use `cases_tac` which will generate a contradiction in the case where the condition is false:

SML

```sml
|a(all_asm_fc_tac[]);
pop_thm();
```

ProofPower output

```
... 
(* 2 *) \(\forall x : Z \mid x \leq y \bullet P(x, y)\) \land x = y \Rightarrow P(x, y) \land
(* 1 *) \(\exists x = y\)
(* ?! *) \(\exists P(x, y)\)
...
```

SML

```sml
|a(cases_tac\(\exists x \leq y\));
```

ProofPower output

```
Tactic produced 2 subgoals:
(* *** Goal "2" *** *)
(* 3 *) \(\forall x, y : Z \mid x \leq y \bullet P(x, y)\)
(* 2 *) \(\exists x = y\)
(* 1 *) \(\exists x \leq y\)
(* ?! *) \(\exists P(x, y)\)
...
```

```
(* *** Goal "1" *** *)
(* 3 *) \(\forall x, y : Z \mid x \leq y \bullet P(x, y)\)
(* 2 *) \(\exists x = y\)
```

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2.10. Induction

\[ (* 1 *) \quad \exists x \leq y \]
\[ (* \neg 1 *) \quad \exists P(x, y) \]

This time, forward chaining proves the first subgoal:

SML
\[ a(all\_asm\_fc\_tac[]) ; \]

ProofPower output

Tactic produced 0 subgoals:
Current goal achieved, next goal is:

\[ (* \quad \text{Goal "2"} \quad \) \]
...

Eliminating \( x \) should derive the expected contradiction to solve subgoal 2:

SML
\[ a(all\_var\_elim\_asm\_tac1) ; \]

ProofPower output

Tactic produced 0 subgoals:
Current and main goal achieved

2.10 Induction

Induction tactics for integers are available. The easiest one to use is \( \leq\_induction\_tac \). Consider the following goal, for example:

SML
\[ \text{set}\_goal([\exists i,j: \mathbb{Z} \mid 0 \leq i \land 0 \leq j \cdot 0 \leq i*j]) ; \]
\[ a(REPEAT \text{ strip}\_tac) ; \]

ProofPower output

\[ (* 2 *) \quad \exists 0 \leq i \]
\[ (* 1 *) \quad \exists 0 \leq j \]
\[ (* \neg 1 *) \quad \exists 0 \leq i * j \]

Now apply the induction tactic:

SML
\[ a(\leq\_induction\_tac\_2 i) ; \]
ProofPower output

Tactic produced 2 subgoals:

\( (* \text{ *** Goal "2" *** *) } \)

\( (* 3 \text{ * } \exists x_0 \leq j \neg ) \)
\( (* 2 \text{ * } \exists x_0 \leq i \neg ) \)
\( (* 1 \text{ * } \exists x_0 \leq i \ast j \neg ) \)
\( (* ?! \text{ * } \exists x_0 \leq (i + 1) \ast j \neg ) \)

\( (* \text{ *** Goal "1" *** *) } \)

\( (* 1 \text{ * } \exists x_0 \leq j \neg ) \)
\( (* ?! \text{ * } \exists x_0 \leq 0 \ast j \neg ) \)

Subgoal 1 is proved by rewriting with the assumptions.

SML
\[ a(\text{asm}\_\text{rewrite}\_\text{tac}[]); \]

Although the original goal was not a linear arithmetic result, induction has reduced the problem to one of linear arithmetic. Subgoal 2 can be proved in the proof context \( z\_\text{lin}\_\text{arith} \):

SML
\[ a(PC\_T1"z\_\text{lin}\_\text{arith}\ast\text{asm}\_\text{prove}\_\text{tac}[]); \]
\[ \text{pop}\_\text{thm}(); \]

ProofPower output

Tactic produced 0 subgoals:

Current and main goal achieved
Chapter 3

PROVING VCS

As ordinary Z goals, VCs may be proved using all of the normal facilities provided by ProofPower for proof in Z. Chapter 2 provides an overview, based on the ProofPower Z Tutorial [1], of the recommended techniques for using ProofPower-Z for doing proofs. In addition, many VCs use Z toolkit extensions which are contained in the theory cn. Some custom support is provided in the Compliance Tool to assist with reasoning in this theory, most notably the proof contexts cn1, cn_ext and cn. There are also some purpose built tactics available, e.g. cn_vc_simp_tac and cn_\_type_tac which have been designed for use with VC proofs. The main objective of this chapter is to show you how to use these tactics with facilities available in the proof context cn1 to strip away the “Compliance Tool specific” bits of a VC proof to transform it into an “ordinary” Z proof. From there, hopefully the material in section 2 will guide you in progressing your proof.

3.1 Getting Started

It is assumed that you are using the Compliance Tool and are in a position to access the VCs, i.e. you are either in the same theory as your specification, or in a theory which has your specification as a parent. (Compliance Tool — User Guide [4] gives a comprehensive account of the Compliance Notation functions, in particular how to access the VCs generated by the tool from a literate script).

Processing a Compliance Notation script gives rise to definitions and axioms that are not necessarily in a form that makes for easy reasoning. Before you tackle the VC proofs, there is some Compliance Tool support available that generates some theorems for you that should facilitate rewriting when proving the VCs. First call the function all.cn_make_script_support with your choice of name as string argument. For example:

```
val my_thms = all.cn_make_script_support "mycn";
```

generates all these supporting theorems, binds them (in a list) to the ML variable my_thms, and generates the supporting proof context mycn. After a few seconds, you will see a raft of theorems scroll by in the journal window. Each of the definitions and axioms generated by processing the Compliance Notation give rise to one or two supporting theorems. There is always one rewriting theorem, and if this does not contain all the type information implicit in the definition, there will be another signature theorem. These theorems are at least as good as what you get with z_get_spec, and in many cases are in a much better form for rewriting.

All supporting theorems are stored in the current theory, and are prefixed by cn_. The rewriting theorems finish with _thm, and the signature theorems finish with _sig_thm. The theorems are also bound to ML variables of the same name. Such theorems will have been generated from all the definitions in your theory. For example, the type OPERATION in the package TC in the calculator example, see section 4, gives rise to the definition TCoOPERATION, for which one supporting theorem has been generated:

```
|cn_TCoOPERATION_thm
| ⊢ TCoOPERATION = TCoPLUS .. TCoEQUALS
```
and an attribute definition $TCoOPERATIONvPOS$, for which two supporting theorems have been generated:

\[
\begin{align*}
\text{cn\textunderscore TCoOPERATIONvPOS\textunderscore thm} & \quad \vdash \forall i : TCoOPERATION \rightarrow TCoOPERATIONvPOS \ i = i \\
\text{cn\textunderscore TCoOPERATIONvPOS\textunderscore sig\textunderscore thm} & \quad \vdash TCoOPERATIONvPOS \in TCoOPERATION \rightarrow TCoOPERATION
\end{align*}
\]

The supporting proof context $mycn$ created for you is the proof context $cn1$ extended by these supporting theorems. It is not the intention that you should normally set $mycn$ as a proof context, because it would typically be too aggressive. The effect of rewriting in proof context $mycn$ would be to unwind everything in a goal with its basic definition. However, there are some cases when this is exactly what you want to do, for example, when numerical values in a specification are significant. In this case, you would probably have set the proof context to $cn1$ at the start of the proof, and then at some appropriate stage applied the following:

\[
\text{a(\text{PC\textunderscore T1 } "mycn" rewrite\textunderscore tac[])};
\]

The theory $cn$ contains the Compliance Tool Z toolkit extensions. The recommended user interface to the conversions etc. described there is via the proof context $cn1$. In this proof context the SPARK boolean and relational operators are converted fairly directly into Z. Additional theorems in the theory $cn$ are also available for reasoning about the numeric operators $\text{intdiv}$, $\text{intmod}$ and $\text{rem}$. The more aggressive proof contexts, $cn$ and $cn\textunderscore ext$, are also available. These, and all the other custom proof tools are described in section 6.1 of the Compliance Tool — User Guide [4]. For references purposes, a full listing of the theory $cn$ is also provided in that user guide.

### 3.2 Compliance Tool Proofs

Application of the tactic $cn\textunderscore vc\textunderscore simp\textunderscore tac$, see section 6.1 of Compliance Tool — User Guide [4], is the favoured way of beginning a VC proof. In all but the most obscure cases this should simplify the goal, and may even be sufficient to achieve a proof. $cn\textunderscore vc\textunderscore simp\textunderscore tac$ first rewrites the conclusion of the goal with the rewriting rules of the current proof context, some associativity theorems, and any theorems of your choice. Outer universal quantifiers are stripped away and any resulting redundancy in the goal removed. For example, using the proof context $cn1$, $cn\textunderscore vc\textunderscore simp\textunderscore tac$, will transform the goal:

\[
\begin{align*}
?\vdash \forall x : \text{INTEGER}; y : \text{INTEGER}; z : \text{INTEGER} \\
& \quad (x + y) + 1 \ eq \ z = \text{TRUE} \ \& \ (x \geq 0 \ \& \ y \geq 0) \ \& \ x \geq 0 \\
& \quad \bullet \ \ x \geq 0 \ \& \ z\ greater\_eq \ 0 = \text{TRUE}
\end{align*}
\]

into:

\[
\begin{align*}
?\vdash x \in \text{INTEGER} \ \& \ y \in \text{INTEGER} \ \& \ z \in \text{INTEGER} \\
& \quad \land \ x + y + 1 = z \ \land \ 0 \leq x \ \& \ 0 \leq y \\
& \quad \Rightarrow \ 0 \leq z
\end{align*}
\]

You should now be in a position to prove the VCs. The names of the VCs can be obtained from the current theory using $\text{get\_conjectures } "$ $"$. 

First, set the proof context, typically $cn1$, and set the VC, called, say, $vcn\_n$, as a ProofPower-Z goal.
Now apply the simplification tactic `cn_vc_simp_tac`. This may achieve the proof; if not and you believe that the goal is simply a predicate calculus result, perhaps with a bit of linear arithmetic, then you may care to attack it with `prove_tac` (in the linear arithmetic proof context if applicable). In general, though, this type of automatic proof procedure would not be appropriate. Next, you might try the following (until the goal is achieved):

- Apply `REPEAT strip_tac`. If this results in the generation of additional subgoals, you may want to "undo 1" and then perform more controlled stripping. For example

  ```sml
  set_pc"cn1";
  set_goal([], get_conjecture="vcn_n");
  ```

  ProofPower output

  ```
  (*** Goal "" *** *)
  (* ⊢ *) \( \forall x : X; y : X \) \( P(x) \land Q(x) \land x = y \Rightarrow P(y) \land Q(y) \)
  ```

  `REPEAT strip_tac` will generate two subgoals from the conjunction in the right hand side of the implication obtained by `cn_vc_simp_tac`:

  ```sml
  a(REPEAT strip_tac);
  ```

  ProofPower output

  ```
  Tactic produced 2 subgoals:
  (*** Goal "2" *** *)
  (* 5 *) \( \forall x \in X \)
  (* 4 *) \( \forall y \in X \)
  (* 3 *) \( \forall x \in X \)
  (* 2 *) \( \forall Q x \)
  (* 1 *) \( \forall x = y \)
  ```

  ```
  (*** Goal "1" *** *)
  ```
Whereas $\Rightarrow_{\text{tac}}$ will strip the left hand side of the implication into the assumptions without affecting the right hand side:

SML

\begin{verbatim}
undo 1;
a $\Rightarrow_{\text{tac}}$;
\end{verbatim}

ProofPower output

\begin{verbatim}
Tactic produced 1 subgoal:
\end{verbatim}

\begin{verbatim}
(* *** Goal "" *** *)
\end{verbatim}

\begin{verbatim}
(* 5 *) $\exists x \in X$\end{verbatim}

\begin{verbatim}
(* 4 *) $\exists y \in X$\end{verbatim}

\begin{verbatim}
(* 3 *) $\exists P x$\end{verbatim}

\begin{verbatim}
(* 2 *) $\exists Q x$\end{verbatim}

\begin{verbatim}
(* 1 *) $\exists x = y$\end{verbatim}

\begin{verbatim}
(* ?!- *) $\exists P y \wedge Q y$\end{verbatim}

- Stripping may not succeed if it generates an assumption which is an equation with a variable on one side of the equals. Eliminating that variable throughout, with a variation on the theme of \texttt{var_elim_asm_tac}, before using \texttt{asm_rewrite_tac} or \texttt{asm_fc_tac} may solve the goal. The example above illustrates this. Eliminating $x$ from assumption 1 then rewriting with the assumptions should complete proof:

SML

\begin{verbatim}
a(all_var_elim_asm_tac1);
a(asm_rewrite_tac[]);
pop_thm();
\end{verbatim}

ProofPower output

\begin{verbatim}
Tactic produced 0 subgoals:
Current and main goal achieved
\end{verbatim}

- A For Loop Statement may give rise to a VC with an assumption which is quantified over a range $i..i-1$. Such an assumption is false, and the application of \texttt{asm_prove_tac} in the linear arithmetic proof context will suffice to prove the VC:

$PC_T1$"z_lin_arith"asm_prove_tac[]
3.2. Compliance Tool Proofs

- Rewriting is usually the next thing to try. There are two “levels” of definitions that can be unwound:
  - the SPARK definitions in the specification, e.g. the attributes TCoDIGITvFIRST and TCoDIGITvLAST in the calculator example, section 4
  - the Z in the specification, e.g. the Z schema DO_DIGIT in the calculator example

Perhaps you could progress your proof by rewriting explicitly with the supporting theorems cn_TCoDIGITvFIRST_thm, or cn_DO_DIGIT_thm that were generated for you as described in section 3.1.

It is worth remembering that forward chaining is often required in order to obtain usable rewrites from Z definitions. For example, the theorem available from the definition of fact in the calculator example, section 4, is:

\[
\begin{align*}
\vdash & \text{fact} \in \mathbb{N} \rightarrow \mathbb{N} \\
& \wedge \text{fact} 0 = 1 \\
& \wedge (\forall m : \mathbb{N} \cdot \text{fact} (m + 1) = (m + 1) \ast \text{fact} m)
\end{align*}
\]

To use the third conjunct of this theorem, it is necessary to have an assumption of the form \(m \in \mathbb{N}\) so that you can forward chain, with, say, \(fc\_tac\), to obtain the result that \(\text{fact} (m + 1) = (m + 1) \ast \text{fact} m\). It is also not uncommon to have the following pattern in a Z axiomatic definition:

\[
\begin{align*}
\forall & x : X; y : Y \cdot y \in \text{SOMEPROPERTY} x \Leftrightarrow \text{SOMEPREDICATE}
\end{align*}
\]

Suppose you are required to prove the goal:

\[
\begin{align*}
| & a \in \text{SOMEPROPERTY} b
\end{align*}
\]

Then, useful rewriting may be achieved by forward chaining using

\[
\begin{align*}
\text{ALL\_FC\_T1} & \text{ fc} \Leftrightarrow \text{canon rewrite_tac}[z\_get\_spec\_z\_SOMEPROPERTY]\]
\end{align*}
\]

- Some insight as to “what to do next” may be gleaned from chapter 2. The degree of difficulty experienced in proving VCs rather depends on the refinement steps which generated the VCs in the first place. A complex sequence of intermediate refinements gives rise to complex VCs, whereas refinements which rely heavily on the underlying Z may require you to unwind a lot of definitions.

It is worth remembering at this time that there is always the possibility that the VC you are trying to prove may not be true, or more likely, may not be provable. You may save yourself pain if, before you embark on the proof using the tool, you spend a few minutes at the outset inspecting the VC to convince yourself that you have a fair chance of achieving a proof. Problems will occur if the pre-condition of a refinement statement is too weak, or the post-condition too strong, to prove the VC.

Typically you may find it advantageous to prove subsidiary lemmas before embarking upon a VC proof. These will be either general purpose lemmas that are not available in ProofPower-Z but can easily be proven with the proof support available, or they will be application specific. Such lemmas are proven separately either because they are results that are found to be needed repeatedly during
the process of proving the VCs, or because proving them in isolation is simpler than proving them in the context of a VC proof. Actually, in practice, it is common to experience a sense of \textit{deja vu} when in the middle of a proof. You find that you need a result that you have already proved in an earlier VC proof, and it is at that stage that you decide to backtrack and extract the bits from the previous proof as a separate lemma. Section 3.3 provides examples of the sort of subsidiary lemmas that might typically be useful.

3.3 Additional Techniques

The \textit{Compliance Notation — Language Description} [6] describes how particular SPARK constructs are translated into Z. Some of these constructs give rise to idioms requiring special styles of proof.

3.3.1 Array Component Assignments

Functional overrides occur in the VCs from SPARK array assignments. These assignments can be to an arbitrary level of nesting. Suppose in your VC proofs, the following lemma would be useful:

\[ \forall f : X \rightarrow Y 
\rightarrow Z; x : X; y2 : Y; y1 : U; z : U \mid \neg y2 = y1 \bullet \]

\[ f \oplus \{ x \mapsto x \oplus \{ y1 \mapsto z \}\} x \cdot y2 = f \cdot x \cdot y2 \]

Although this does not exist as a theorem in \textit{ProofPower-Z}, it is straightforward for you to prove as a separate lemma using \texttt{z_fun_in_clauses}, as described in section 2.7, together with the theorems \texttt{z_\oplus_\rightarrow_app_thm} and \texttt{z_\oplus_\rightarrow_app_thm1}:

\[ \forall f : U; x : U; y : U \bullet (f \oplus \{ x \mapsto y \}) x = y \]

\[ z_\oplus_\rightarrow_app_thm1 \]

\[ \forall f : X \rightarrow Y; x2 : X; x1 : U; y : U \mid \neg x2 = x1 \bullet \]

\[ (f \oplus \{ x1 \mapsto y \}) x2 = f \cdot x2 \]

\[ \text{SML} \]

\[ \texttt{set_goal}([\forall f : X \rightarrow Y 
\rightarrow Z; x : X; y2 : Y; y1 : U; z : U \mid \neg y2 = y1 \bullet \}

\[ (f \oplus \{ x \mapsto x \oplus \{ y1 \mapsto z \}\}) x \cdot y2 = f \cdot x \cdot y2)'\};

\[ \texttt{a(REPEAT strip_tac)}; \]

\[ \texttt{a(rewrite_tac[z_\oplus_\rightarrow_app_thm])}; \]

\[ \texttt{a(all_asm_fc_tac[z_fun_in_clauses])}; \]

\[ \texttt{a(ALL_ASM_FC_T rewrite_tac[z_\oplus_\rightarrow_app_thm1])}; \]

\[ \texttt{pop_thm}(); \]

\[ \text{ProofPower output} \]

\[ Tactic \ produced \ 0 \ subgoals: \]

\[ Current \ and \ main \ goal \ achieved \]
3.3.2 Set Membership of Record Components

The form of this class of lemma is \( X \text{.field} \in \text{set} \), which arises from indexing into an array of records in SPARK. The tactic \( cn \_\in \_\text{type} \_\text{tac} \) is available to simplify the proof of this type of lemma. A typical example of the set in question is \( BOOLEAN \). For example, consider the following specification snippet from a Compliance Notation script:

```plaintext
subtype SWITCHTYPE is INTEGER range 1 .. 3;

type SWITCHDATA is
record
    STATE : BOOLEAN; -- on or off
    NEXTSWITCH : SWITCHTYPE;
end record;

type SWITCHBOARD is array (SWITCHTYPE) of SWITCHDATA;
```

Suppose the VCs have been generated and the supporting proof context made, as described in section 3.1:

```plaintext
val switch_thms = all_cn_make_script_support "switch_cn";
```

Now, suppose we find that for the VC proofs we need to recast something of the form

\[ \text{not} (sb \ s).\text{STATE} = \text{Boolean false} \]

more naturally as

\[ (sb \ s).\text{STATE} = \text{Boolean true} \]

To do this, we first need to prove that \((sb \ s).\text{STATE}\) is a member of \( BOOLEAN \):

```plaintext
set_goal([\forall sb:SWITCHBOARD; s: SWITCHTYPE\bullet(sb \ s).\text{STATE} \in BOOLEAN\text{^\top})];
```

ProofPower output

\[ (* 2 *) \exists sb \in SWITCHBOARD\text{^\top} \]
\[ (* 1 *) \exists s \in SWITCHTYPE\text{^\top} \]
\[ (* ? \top *) \exists (sb \ s).\text{STATE} \in BOOLEAN\text{^\top} \]

Now if we apply \( cn \_\in \_\text{type} \_\text{tac} \) in the supporting proof context \( switch \_\text{cn} \), the goal will be achieved:

```plaintext
val switch\_\in\_thm = pop_thm();
```

ProofPower output

| Tactic produced 0 subgoals: |
| Current and main goal achieved |

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We can now prove the recasting result using $\text{switch} \in \text{thm}$ together with $\text{cn}_\cdot \text{boolean}_\cdot \text{clauses1}$ from the theory $\text{cn}$:

$$\vdash (\forall x : \text{BOOLEAN} \cdot \neg x = \text{Boolean} \, (\neg x = \text{Boolean} \, \text{true}))$$

\[
\land (\forall x, y : \text{BOOLEAN} \cdot \bullet x \land y = \text{Boolean} \, (x = \text{Boolean} \, \text{true} \land y = \text{Boolean} \, \text{true}))
\]

\[
\land (\forall x, y : \text{BOOLEAN} \cdot \bullet x \lor y = \text{Boolean} \, (x = \text{Boolean} \, \text{true} \lor y = \text{Boolean} \, \text{true}))
\]

\[
\land (\forall x, y : \text{BOOLEAN} \cdot \bullet x \times y = \text{Boolean} \, (\neg x = \text{Boolean} \, \text{true} \iff y = \text{Boolean} \, \text{true}))
\]

SML

\[
\text{set_goal}([]) \forall sb : \text{SWITCHBOARD}, s : \text{SWITCHTYPE} \cdot \\
\quad \neg (sb.s).\text{STATE} = \text{Boolean} \, \text{false} \iff (sb.s).\text{STATE} = \text{Boolean} \, \text{true})
\]

\[
\text{a}(z.\forall \_\cdot \_ \cdot \text{tac} \text{ THEN REPEAT } \Rightarrow \_\cdot \_\cdot \_\cdot \text{tac})
\]

\[
\text{a}(\text{all_asm_fc_tac}[\text{switch} \in \text{thm}])
\]

\[
\text{a}(\text{ALL_ASM_FC_T rewrite_tac}[\text{cn}_\cdot \text{boolean}_\cdot \text{clauses1}])
\]

\[
\text{pop_thm}();
\]

ProofPower output

Tactic produced 0 subgoals:
Current and main goal achieved

### 3.3.3 Record a Member of Record Set

The form of this class of lemma is \((x \equiv x_1, y \equiv y_1, \ldots) \in \text{set of recs}\), which arises from assignment to an array of records in SPARK. Using the switchboard example of the previous section, we could be required to prove the following, say:

SML

\[
\text{set_goal}([]) \exists (\text{STATE} \equiv \text{Boolean} \, \text{true}, \text{NEXTSWITCH} \equiv 2) \in \text{SWITCHDATA})
\]

Again, applying $\text{cn}_\cdot \text{type}_\cdot \text{tac}$ in the proof context $\text{switch}_\cdot \text{cn}$ will prove the goal:

SML

\[
\text{a}(\text{PC_T1}" \text{switch}_\cdot \text{cn}" \text{cn}_\cdot \text{type}_\cdot \text{tac}])
\]

\[
\text{pop_thm}();
\]

ProofPower output

Tactic produced 0 subgoals:
Current and main goal achieved

As this is a one line proof, you may choose not to prove the result as a separate lemma. It rather depends on how many times the result is needed during the course of the VC proofs, and whether it matters to you that the application of this tactic in the supporting proof context may take some time (depending on the size of your specification).
3.3.4 Real Numbers

Ada fixed point and floating point types (collectively referred to as real types) are represented in Z using the real numbers of pure mathematics as implemented in the ProofPower-Z theory \( z_{\text{reals}} \). In addition, the theory \( cn \) provides some operations on real numbers that are specific to the Compliance Notation (e.g., the operator \( _e_ \) that is used in the translation of real literals).

Several proof contexts are available to assist in working with Ada real types. These are listed in the following table:

<table>
<thead>
<tr>
<th>Name</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>( z_{\text{reals}} )</td>
<td>This is a component proof context that provides general purpose rules for the theory ( z_{\text{reals}} ).</td>
</tr>
<tr>
<td>( z_{\mathbb{R}_{\text{lin_arith}}} )</td>
<td>This is a complete proof context whose main purpose is to provide a decision procedure for the linear fragment of the theory of reals.</td>
</tr>
<tr>
<td>( \text{cn}_{\text{reals}} )</td>
<td>This is a component proof context that provides rules that eliminate the special operators on reals in the theory ( cn ) in favour of the underlying Z operators. Note that it does not expand the ( <em>e</em> ) operator except in the special case where the exponent is 0. A theorem ( \text{cn}_{\text{e}} \text{thm} ) is provided for use as a rewrite rule to expand other uses of the ( <em>e</em> ) operator.</td>
</tr>
</tbody>
</table>

To see these proof contexts in use, consider the following example of a package specification and body. (For brevity, we have suppressed the \( \text{new_script} \) commands).

```
Compliance Notation
package real_eg is
  type angle is delta 1.0 / 360.0 range 0.0 .. 1.0 − 1.0 / 360.0;
  procedure interpolate(a, b: in angle; c : out angle);
end real_eg;

Compliance Notation
package body real_eg is
  procedure interpolate(a, b: in angle; c : out angle)
  \( \Delta \) C \( [A +_R \text{REAL\_EGoANGLEvDELTA}<_R B, A <_R C <_R B] \)
  is
  begin
    if a + angle’delta < b
      then c := a + angle’delta;
    end if;
  end interpolate;
end real_eg;
```

This produces the following VCs (in the theory \( \text{REAL\_EGoINTERPOLATE’proc} \)):

```
vc\text{REAL\_EGoINTERPOLATE’1} ?:-
\exists A, B : \text{REAL\_EGoANGLE}
  \left| \begin{array}{l}
    \| A +_R \text{REAL\_EGoANGLEvDELTA}<_R B \land A +_R \text{REAL\_EGoANGLEvDELTA} \text{real_less} B = \text{TRUE} \\
    \bullet A <_R A +_R \text{REAL\_EGoANGLEvDELTA} \land A +_R \text{REAL\_EGoANGLEvDELTA}<_R B
  \end{array} \right.
```

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∀ A, B, C : REAL_EGoANGLE

\[ A +_R \text{REAL}_E\text{GoANGLE}_\text{vDELTA} <_R B \wedge A +_R \text{REAL}_E\text{GoANGLE}_\text{vDELTA} \text{ real} \_\text{less} B = \text{FALSE} \]

\[ \bullet A <_R C \wedge C <_R B \]

To prove them, we work in the proof context obtained by merging 'cn_reals and 'z_reals with the standard complete proof context for the \( cn \) theory, \( cn1 \):

```sml
open_theory "REAL_EGoINTERPOLATE" proc;
push_merge_pcs["cn_reals", "z_reals", "cn1"];```

We will go through the proof of the first VC. (The second VC is proved immediately with \( cn\_vc\_simp\_tac \)).

```sml
set_goal([], get_conjecture="vcREAL_EGoINTERPOLATE_1");
a(cn_vc_simp_tac[] THEN REPEAT strip_tac);
```

This results in the following:

\[\vdash 0.0 <_R \text{REAL}_E\text{GoANGLE}_\text{vDELTA} \]

Here the assumptions are not relevant, we are simply being asked to prove that \( \text{REAL}_E\text{GoANGLE}_\text{vDELTA} \) is positive. We now appeal to the definition of \( \text{REAL}_E\text{GoANGLE}_\text{vDELTA} \):

```sml
a(rewrite_tac[z_get_spec"REAL_EGoANGLE_vDELTA"]);
```

This results in:

\[\vdash \text{real} 0 <_R 1.0 /_R 360.0 \]

Using the computational rules in the proof context '\( z_\mathbb{R}_\text{lin_arith} \) complete the proof:

```sml
a(PC_T1 "z_\mathbb{R}_\text{lin_arith} prove_tac");
```
CALCULATOR EXAMPLE

This Compliance Notation example is concerned with the computational aspects of a simple calculator. Section 4.1 provides the literate scripts for the example and section 4.2 overviews the VCs generated then provides proofs for a subset of them. For reference purposes, a complete set of VC proofs is available in a separate document, [7]. A listing of the theories generated by the calculator example is available in appendix A. In addition, a listing of the generated SPARK program is available in appendix B.
4.1 The Literate Scripts

Though we hide the detail in this document, remember that there will be one literate script per compilation unit (such as a package specification or body).

4.1.1 Basic Definitions

In this section, we define types and constants which will be of use throughout the rest of the scripts. The SPARK package $TC$ below helps record the following facts:

- The calculator deals with signed integers expressed using up to six decimal digits.
- It has a numeric keypad and 6 operation buttons labelled $+, -, \times, +/-, \sqrt{}, !,$ and $=.$

```spark
Compliance Notation

package TC is

  BASE : constant INTEGER := 10;
  PRECISION : constant INTEGER := 6;
  MAX_NUMBER : constant INTEGER := BASE ** PRECISION - 1;
  MIN_NUMBER : constant INTEGER := -MAX_NUMBER;

  subtype DIGIT is INTEGER range 0 .. BASE - 1;

  subtype NUMBER is INTEGER range MIN_NUMBER .. MAX_NUMBER;

  type OPERATION is
    (PLUS, MINUS, TIMES, CHANGE_SIGN, SQUARE_ROOT, FACTORIAL, EQUALS);

end TC;
```

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4.1.2 The State

In this section, we define a package which contains all the state variables of the calculator.

The package $GV$ below defines the global variables we will use to implement the following informal description of part of the calculator’s behaviour:

- The calculator has two numeric state variables: the display, which contains the number currently being entered, and the accumulator, which contains the last result calculated.
- The user is considered to be in the process of entering a number whenever a digit button is pressed, and entry of a number is terminated by pressing one of the operation keys.
- When a binary operation key is pressed, the operation is remembered so that the appropriate value can be calculated when the second operand has been entered.

As it is a new package, and thus a new compilation unit, we will require a new script (though here, and elsewhere in this document, we hide the details of this action).

Compliance Notation

```verbatim
with TC;
package GV is

   DISPLAY, ACCUMULATOR : TC.NUMBER;

   LAST_OP : TC.OPERATION;

   IN_NUMBER : BOOLEAN;

end GV;
```
4.1.3 The Operations

In this section, we define a package which contains procedures corresponding to pressing the calculator buttons.

4.1.3.1 Package Specification

We now want to introduce a package \textit{OPS} which implements the following informal description of how the calculator responds to button presses:

- The behaviour when a digit button is pressed depends on whether a number is currently being entered into the display. If a number is being entered, then the digit is taken as part of the number. If a number is not being entered (e.g., if an operation button has just been pressed), then the digit is taken as the most significant digit of a new number in the display.

- When a binary operation button is pressed, any outstanding calculation is carried out and the answer (which will be the first operand of the operation) is displayed; the calculator is then ready for the user to enter the other operand of the operation.

- When a unary operation button is pressed, the result of performing that operation to the displayed number is computed and displayed; the accumulator is unchanged, but entry of the displayed number is considered to be complete.

- When the button marked = is pressed, any outstanding calculation is carried out and the answer is displayed.

The package implementing this is defined in section 4.1.3.2 below after we have dealt with some preliminaries.

4.1.3.1.1 Z Preliminaries

To abbreviate the description of the package, we do some work in Z first, corresponding to the various sorts of button press.

Note that the use of Z rather than \textit{TCoNUMBER} reflects the fact that we are ignoring questions of arithmetic overflow here. If we used the Z set which accurately represents the SPARK type, then we would have to add in pre-conditions saying that the operations do not overflow. The following schema defines what happens when a digit button is pressed.

\[
\begin{array}{l}
\text{GVoDISPLAY}_0, \text{GVoDISPLAY} : \mathbb{Z}; \\
\text{GVoIN\_NUMBER}_0, \text{GVoIN\_NUMBER} : \text{BOOLEAN}; \\
D : \text{TCoDIGIT} \\
\text{GVoIN\_NUMBER}_0 = \text{TRUE} \Rightarrow \text{GVoDISPLAY} = \text{GVoDISPLAY}_0 \ast \text{TCoBASE} + D; \\
\text{GVoIN\_NUMBER}_0 = \text{FALSE} \Rightarrow \text{GVoDISPLAY} = D; \\
\text{GVoIN\_NUMBER} = \text{TRUE}
\end{array}
\]

We now define sets \textit{UNARY} and \textit{BINARY} which partition the two sorts of operation key. Note that = can be considered as a sort of binary operation (which given operands \(x\) and \(y\) returns \(x\)).

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4.1. The Literate Scripts

\[ UNARY \doteq \{ TCoCHANGE\_SIGN, TCoFACTORIAL, TCoSQUARE\_ROOT \} \]

\[ BINARY \doteq TCoOPERATION \setminus UNARY \]

We need to define a function for computing factorials in order to define the response to the factorial operation button.

\[
\text{fact} : \mathbb{N} \rightarrow \mathbb{N}
\]

\[
\text{fact} \ 0 = 1 ; \\
\forall m: \mathbb{N} \bullet \text{fact}(m+1) = (m + 1) \ast \text{fact} \ m
\]

Unary operations behave as specified by the following schema. In which we do specify explicitly that the accumulator and last operation values are unchanged for clarity and for simplicity later on (when we group the unary and binary operations together).

\[ _\text{DO\_UNARY\_OPERATION} \]

\[
\begin{align*}
GVo\text{ACCUMULATOR}_0, & \ GVo\text{ACCUMULATOR} : \mathbb{Z}; \\
GVo\text{DISPLAY}_0, & \ GVo\text{DISPLAY} : \mathbb{Z}; \\
GVo\text{LAST\_OP}_0, & \ GVo\text{LAST\_OP} : \mathbb{Z}; \\
GVo\text{IN\_NUMBER} & : \text{BOOLEAN}; \\
O & : \text{UNARY}
\end{align*}
\]

\[
\begin{align*}
GVo\text{IN\_NUMBER} &= \text{FALSE}; \\
GVo\text{ACCUMULATOR} &= GVo\text{ACCUMULATOR}_0; \\
GVo\text{LAST\_OP} &= GVo\text{LAST\_OP}_0; \\
O &= TCo\text{CHANGE\_SIGN} \Rightarrow GVo\text{DISPLAY} = \sim GVo\text{DISPLAY}_0; \\
O &= TCo\text{FACTORIAL} \land GVo\text{DISPLAY}_0 \geq 0 \Rightarrow \\
& \hspace{1cm} GVo\text{DISPLAY} = \text{fact} \ GVo\text{DISPLAY}_0; \\
O &= TCo\text{SQUARE\_ROOT} \land GVo\text{DISPLAY}_0 \geq 0 \Rightarrow \\
& \hspace{1cm} GVo\text{DISPLAY} \ast\ast 2 \leq GVo\text{DISPLAY}_0 < (GVo\text{DISPLAY} + 1) \ast\ast 2
\end{align*}
\]
The binary operations are specified by the following schema.

\[
\text{\_DO\_BINARY\_OPERATION}
\]

\[
\begin{align*}
&GVoACCUMULATOR_0, GVoACCUMULATOR : \mathbb{Z}; \\
&GVoDISPLAY_0, GVoDISPLAY : \mathbb{Z}; \\
&GVoLAST\_OP_0, GVoLAST\_OP : \mathbb{Z}; \\
&GVoIN\_NUMBER : BOOLEAN; \\
&O : BINARY
\end{align*}
\]

\[
\begin{align*}
&GVoIN\_NUMBER = \text{\textit{FALSE}}; \\
&GVoDISPLAY = GVoACCUMULATOR; \\
&GVoLAST\_OP = O; \\
&GVoLAST\_OP_0 = \text{TCoEQUALS} \Rightarrow \\
&\quad GVoACCUMULATOR = GVoDISPLAY_0; \\
&GVoLAST\_OP_0 = \text{TCoPLUS} \Rightarrow \\
&\quad GVoACCUMULATOR = GVoACCUMULATOR_0 + GVoDISPLAY_0; \\
&GVoLAST\_OP_0 = \text{TCoMINUS} \Rightarrow \\
&\quad GVoACCUMULATOR = GVoACCUMULATOR_0 - GVoDISPLAY_0; \\
&GVoLAST\_OP_0 = \text{TCoTIMES} \Rightarrow \\
&\quad GVoACCUMULATOR = GVoACCUMULATOR_0 \ast GVoDISPLAY_0
\end{align*}
\]

The disjunction of the schemas for the unary and binary operations is then what is needed to define the response to pressing an arbitrary button press.

\[
\text{\_DO\_OPERATION} \equiv \text{\_DO\_UNARY\_OPERATION} \lor \text{\_DO\_BINARY\_OPERATION}
\]
4.1.3.2 The SPARK Package

We now use the schemas of the previous section to define the package $OPS$.

Compliance Notation

```
| with TC, GV;  |
| package OPS is |
| procedure DIGIT_BUTTON (D : in TC.DIGIT) |
|   Δ GVoDISPLAY, GVoIN_NUMBER [ DO.DIGIT ]; |
| procedure OPERATION_BUTTON (O : in TC.OPERATION) |
|   Δ GVoACCUMULATOR, GVoDISPLAY, |
|     GVoIN_NUMBER, GVoLAST_OP [ DO.OPERATION ]; |
| end OPS; |
```

4.1.3.3 Package Implementation

4.1.3.3.1 Package Body  The following specification of the package body is derived from the package specification in the obvious way. We leave a k-slot for any extra declarations we may need.

Compliance Notation

```
$| references TC, GV;  |
| package body OPS is |
| procedure DIGIT_BUTTON (D : in TC.DIGIT) |
|   Δ GVoDISPLAY, GVoIN_NUMBER [ DO.DIGIT ] |
|   is begin |
|   Δ GVoDISPLAY, GVoIN_NUMBER [ DO.DIGIT ] (3001) |
|   end DIGIT_BUTTON; |
| procedure OPERATION_BUTTON (O : in TC.OPERATION) |
|   Δ GVoACCUMULATOR, GVoDISPLAY, |
|     GVoIN_NUMBER, GVoLAST_OP [ DO.OPERATION ] |
|   is |
|   ⟨ Extra Declarations ⟩ (500) |
|   begin |
|   Δ GVoACCUMULATOR, GVoDISPLAY, |
|     GVoIN_NUMBER, GVoLAST_OP [ DO.OPERATION ] (3002) |
|   end OPERATION_BUTTON; |
| end OPS; |
```

4.1.3.3.2 Supporting Functions  We choose to separate out the computation of factorials and square roots into separate functions which replace the k-slot labelled 500. In both cases, we prepare for the necessary algorithms. Our approach for both functions is to introduce and initialise appropriately a variable called $RESULT$, demand that this be set to the desired function return value and return that value.

SML

```
open scope "OPS.OPERATION_BUTTON";
```

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Compliance Notation

\[(500) \equiv \]
\[
\text{function FACT } (M : \text{NATURAL}) \text{ return NATURAL}
\]
\[
\Xi [ \text{FACT}(M) = \text{fact}(M) ]
\]
\[
\text{is}
\]
\[
\text{RESULT : NATURAL};
\]
\[
\begin{align*}
\text{begin} \\
\text{RESULT} & := 1; \\
\Delta \text{RESULT} & [M \geq 0 \land \text{RESULT} = 1, \text{RESULT} = \text{fact } M ] \\
\text{return RESULT}; \\
\end{align*} \tag{1001}
\]
\[
\text{end FACT};
\]
\[
\text{function SQRT } (M : \text{NATURAL}) \text{ return NATURAL}
\]
\[
\Xi [\text{SQRT}(M) ** 2 \leq M < (\text{SQRT}(M) + 1) ** 2]
\]
\[
\text{is}
\]
\[
\text{RESULT : NATURAL}; \\
\langle \text{other local vars} \rangle \tag{2}
\]
\[
\begin{align*}
\text{begin} \\
\text{RESULT} & := 0; \\
\Delta \text{RESULT} & [\text{RESULT} = 0, \text{RESULT} ** 2 \leq M < (\text{RESULT} + 1) ** 2] \tag{2001}
\text{return RESULT}; \\
\end{align*}
\]
\[
\text{end SQRT};
\]

4.1.3.3.3 Algorithm for Factorial  Factorial is implemented by a for-loop with loop-counter \(J\) and an invariant requiring that as \(J\) steps from 2 up to \(M\), \(\text{RESULT}\) is kept equal to the factorial of \(J\):

\[
\text{SML}\]
\[
\text{open scope } "\text{OPS.OPERATION\_BUTTON.FACT}";
\]

Compliance Notation

\[(1001) \subseteq \]
\[
\text{for } J \text{ in INTEGER range } 2 \ldots M \\
\text{loop} \\
\Delta \text{RESULT} [J \geq 1 \land \text{RESULT} = \text{fact (J-1)}, \text{RESULT} = \text{fact } J] \tag{1002}
\text{end loop;}
\]

Now we can complete the implementation of the factorial function by providing the loop body:

Compliance Notation

\[(1002) \subseteq \]
\[
\text{RESULT} := J \times \text{RESULT};
\]

4.1.3.3.4 Algorithm for Square Root  For square root, we need two extra variables to implement a binary search for the square root.
4.1. The Literate Scripts

SML

\[
\text{open\_scope"OPS.OPERATION\_BUTTON\_SQRT";} \\
\]

Compliance Notation

\[
\begin{align*}
(2) & \equiv \\
& \text{MID, HI : INTEGER;}
\end{align*}
\]

The following just says that we propose to achieve the desired effect on RESULT using MID and HI as well.

Compliance Notation

\[
\begin{align*}
(2001) & \subseteq \\
& \Delta \text{RESULT, MID, HI} \\
& \quad [\text{RESULT} = 0, \text{RESULT} \cdot 2 \leq M < (\text{RESULT} + 1) \cdot 2] \quad (2002)
\end{align*}
\]

Now we give the initialisation for HI and describe the loop which will find the square root:

Compliance Notation

\[
\begin{align*}
(2002) & \subseteq \\
& \text{HI := M + 1;} \\
& \quad \text{\$till} [\text{RESULT} \cdot 2 \leq M < (\text{RESULT} + 1) \cdot 2] \\
& \quad \quad \Delta \text{RESULT, MID, HI} \\
& \quad \quad \quad [\text{RESULT} \cdot 2 \leq M < \text{HI} \cdot 2, \text{RESULT} \cdot 2 \leq M < \text{HI} \cdot 2] \quad (2003)
\end{align*}
\]

Now we implement the exit for the loop and specify the next step:

Compliance Notation

\[
\begin{align*}
(2003) & \subseteq \\
& \quad \text{exit when RESULT + 1 = HI;} \\
& \quad \Delta \text{RESULT, MID, HI} \\
& \quad \quad [\text{RESULT} \cdot 2 \leq M < \text{HI} \cdot 2, \text{RESULT} \cdot 2 \leq M < \text{HI} \cdot 2] \quad (2004)
\end{align*}
\]

Now we can fill in the last part of the loop:

Compliance Notation

\[
\begin{align*}
(2004) & \subseteq \\
& \quad \text{MID := (RESULT + HI + 1) / 2;} \\
& \quad \quad \text{if MID \cdot 2 > M} \\
& \quad \quad \quad \text{then HI := MID;} \\
& \quad \quad \quad \text{else RESULT := MID;} \\
& \quad \quad \text{end if;}
\end{align*}
\]

4.1.3.3.5 Digit Button Algorithm We now continue with the body of the digit button procedure. An if-statement handling the two cases for updating the display, followed by an assignment to the flag should meet the bill here.

SML

\[
\text{open\_scope"OPS.DIGIT\_BUTTON";} \\
\]

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4.1.3.3.6 Operation Button Algorithm  We now complete the implementation and verification of the package *OPS* by giving the body of the procedure for handling the operation buttons.

**SML**

```sml
open_scope "OPS.OPERATION_BUTTON";
```

**Compliance Notation**

\[(3002) \subseteq \]

```plaintext
if \( O = TC.CHANGE\_SIGN \)
then \( GV.DISPLAY := -GV.DISPLAY; \)
elsif \( O = TC.FACTORIAL \)
then \( GV.DISPLAY := FACT(GV.DISPLAY); \)
elsif \( O = TC.SQUARE\_ROOT \)
then \( GV.DISPLAY := SQRT(GV.DISPLAY); \)
else if \( GV.LAST\_OP = TC.EQUALS \)
then \( GV.ACCUMULATOR := GV.DISPLAY; \)
elsif \( GV.LAST\_OP = TC.PLUS \)
then \( GV.ACCUMULATOR := GV.ACCUMULATOR + GV.DISPLAY; \)
elsif \( GV.LAST\_OP = TC.MINUS \)
then \( GV.ACCUMULATOR := GV.ACCUMULATOR - GV.DISPLAY; \)
elsif \( GV.LAST\_OP = TC.TIMES \)
then \( GV.ACCUMULATOR := GV.ACCUMULATOR * GV.DISPLAY; \)
end if;
\( GV.DISPLAY := GV.ACCUMULATOR; \)
\( GV.LAST\_OP := O; \)
end if;
\( GV.IN\_NUMBER := false; \)
```
4.2 Proving the VCs

The Compliance Tool has generated 37 VCs. The following list outlines where each group of VCs has come from, and proofs of a representative selection from these groups are given below.

8 from the introduction of the package body:  
\(vc\text{OPS}_1\)  
\(vc\text{OPS}_2\)  
\(vc\text{OPS}_3\)  
\(vc\text{OPS}_4\)  
\(vc\text{OPSoDIGIT\_BUTTON}_1\)  
\(vc\text{OPSoDIGIT\_BUTTON}_2\)  
\(vc\text{OPSoOPERATION\_BUTTON}_1\)  
\(vc\text{OPSoOPERATION\_BUTTON}_2\)

4 from the introduction of supporting functions:  
\(vc\text{OPSoOPERATION\_BUTTON\_FACT}_1\)  
\(vc\text{OPSoOPERATION\_BUTTON\_FACT}_2\)  
\(vc\text{OPSoOPERATION\_BUTTON\_SQRT}_1\)  
\(vc\text{OPSoOPERATION\_BUTTON\_SQRT}_2\)

5 from the refinement steps in the body of FACT:  
\(vc\text{1001}_1\)  
\(vc\text{1001}_2\)  
\(vc\text{1001}_3\)  
\(vc\text{1001}_4\)  
\(vc\text{1002}_1\)

10 from the refinement steps in the body of SQRT:  
\(vc\text{2001}_1\)  
\(vc\text{2001}_2\)  
\(vc\text{2002}_1\)  
\(vc\text{2002}_2\)  
\(vc\text{2002}_3\)  
\(vc\text{2003}_1\)  
\(vc\text{2003}_2\)  
\(vc\text{2003}_3\)  
\(vc\text{2004}_1\)  
\(vc\text{2004}_2\)

2 from if-statement in the digit button procedure:  
\(vc\text{3001}_1\)  
\(vc\text{3001}_2\)

8 from if-statement in the operations button procedure:  
\(vc\text{3002}_1\)  
\(vc\text{3002}_2\)  
\(vc\text{3002}_3\)  
\(vc\text{3002}_4\)  
\(vc\text{3002}_5\)  
\(vc\text{3002}_6\)  
\(vc\text{3002}_7\)  
\(vc\text{3002}_8\)
4.2.1 Preliminaries

The first thing to do is to generate the supporting theorems and supporting proof context for the calculator example, as described in section 3.1. We do this in the theory calc_prelims.

SML

\begin{verbatim}
open theory "calc_prelims";
val calc_thms = all cn_make_script_support "calc_prelims";
\end{verbatim}

We will conduct the VC proofs in the proof context cn1; any subsidiary general purpose results that we may need will be proved in the proof context z_library1.

4.2.2 Package Body VCs

Eight VCs are produced from the introduction of the package body, see section 4.1.3.3.1. These are trivial because the package body is derived directly from the package specification. For example:

\begin{verbatim}
vOps_1
  true ⇒ true

vOps_2
  ∀ GVoDISPLAY, GVoDISPLAY₀ : TCoNUMBER;
     GVoIN_NUMBER, GVoIN_NUMBER₀ : BOOLEAN;
     D : TCoDIGIT | true ∧ DO_DIGIT • DO_DIGIT
\end{verbatim}

cn_vc_simp_tac will solve all of these VCs. For example:

SML

\begin{verbatim}
open_scope "OPS";
set_pc"cn1";
set_goal([],get_conjecture"="vcOPS_2");
a(cn_vc_simp_tac[]);
save_pop_thm"vcOPS_2";
\end{verbatim}

4.2.3 Function Definition VCs

These four VCs result from the requirement to show that the function bodies of FACT and SQRT achieve the statement of their specifications, see section 4.1.3.3.2. For example:

\begin{verbatim}
v500_1
  ∀ M : NATURAL • M ≥ 0 ∧ 1 = 1

v500_2
  FACT : NATURAL → NATURAL; RESULT : NATURAL; M : NATURAL
     | true ∧ RESULT = fact M ∧ FACT M = RESULT
     • FACT M = fact M
\end{verbatim}

The proof of \textit{vc500}_1 requires the following general purpose lemma about SPARK natural numbers:

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4.2. Proving the VCs

SML

open
theory
"calc_prelims";
push_pc"z_library1"
set_goal([], \forall m : NATURAL\bullet m \geq 0
\); a(rewrite_tac[z_get_spec\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\land\lan
open theory "calc_prelims";
push_pc "z_library1";
set_goal([], \x. fact 0 = 1 \land fact 1 = 1);
a(rewrite_tac[cn_fact_thm, rewrite_rule[cn_fact_thm]
   (\(z. \forall x. 0 < x \land \forall y. y < x \rightarrow x = y\)));
val fact_thm = save_pop_thm "fact_thm";

pop_pc();

To prove vc1001_1, simplify the VC, then strip and rewrite with fact_thm and the assumptions:

set_goal([], get_conjecture "vc1001_1");
a(cn_vc_simp_tac[]);
a(REPEAT strip_tac THEN asm_rewrite_tac[fact_thm]);
save_pop_thm "vc1001_1";

For vc1001_2, first simplify and then strip:

set_goal([], get_conjecture "vc1001_2");
a(cn_vc_simp_tac[]);
a(REPEAT strip_tac);

ProofPower output

|(* *** Goal "" *** *)
|(* 5 *) \(\exists\) RESULT \in NATURAL
|(* 4 *) \(\exists\) M \in NATURAL
|(* 3 *) \(\exists\) 0 \leq M
|(* 2 *) \(\exists\) RESULT = 1
|(* 1 *) \(\nexists\) 2 \leq M
|(* ?! *) \(\nexists\) RESULT = fact M

Assumption 1 gives you M = 0 or M = 1, you will need the linear arithmetic proof context for this, then rewriting with fact_thm completes the proof:

a(lemma_tac \(\exists\) M = 0 \lor M = 1
   THEN PC_T1 "z_lin_arith" asm_prove_tac[]
   THEN asm_rewrite_tac[fact_thm]);
save_pop_thm "vc1001_2";
4.2.5 \textbf{SQRT Refinement Steps VCs}

The implementation of square root produces ten VCs, see section 4.1.3.3.4. Eight of them are trivial and are proved by \texttt{cn vc simp tac}. For example:

\begin{verbatim}
vc2002_2
∀ HI : INTEGER; RESULT, RESULT_0 : NATURAL; M : NATURAL
| RESULT_0 = 0 ∧ RESULT ** 2 ≤ M ∧ M < HI ** 2
• RESULT ** 2 ≤ M ∧ M < HI ** 2
\end{verbatim}

\texttt{SML}
\begin{verbatim}
open_scope "OPS OPERATION BUTTON . SQRT";
set pc "cn1";
set_goal([], get_conjecture"−""vc2002_2");
a(cn vc simp_tac[]);
save_pop_thm"vc2002_2";
\end{verbatim}

\texttt{vc2003_1} also requires stripping and variable elimination to achieve a proof. Extra work is needed in the proof of \texttt{vc2002_1}:

\begin{verbatim}
vc2002_1
∀ RESULT : NATURAL; M : NATURAL
| RESULT = 0
• RESULT ** 2 ≤ M ∧ M < (M + 1) ** 2
\end{verbatim}

This depends on some facts about the exponentiation operator, which you would probably provide as separate lemmas:

\texttt{SML}
\begin{verbatim}
push_pc"z_library1";
set_goal([], ∀x: Z• x ** 1 = x);
a(REPEAT strip_tac);
a(rewrite_tac[rewrite_rule][
    z_∀_elim(z ≡ x, y ≡ 0)\land (\_right elim(z get spec^cZ (•**\_))))]);
val star_star_1_thm = save_pop_thm"star_star_1_thm";
\end{verbatim}

\texttt{SML}
\begin{verbatim}
set_goal([], ∀x: Z• x ** 2 = x * x);
a(REPEAT strip_tac);
a(rewrite_tac[star_star_1_thm, rewrite_rule][
    z_∀_elim(z ≡ x, y ≡ 1)\land (\_right elim(z get spec^cZ (•**\_))))]);
val star_star_2_thm = save_pop_thm"star_star_2_thm";
pop_pc();
\end{verbatim}

To prove \texttt{vc2002_1}, first simplify the goal, strip the assumptions and eliminate \texttt{RESULT}:

\texttt{SML}
\begin{verbatim}
set_goal([], get_conjecture "−" "vc2002_1");
a(cn vc simp_tac[]);
a(REPEAT ⇒_tac THEN all_var elim_asm_tac1);
\end{verbatim}
ProofPower output

\
\[
(\ast \ 2 \ast) \ \forall M \in \textsc{natural}^n
\]

\
\[
(\ast \ 1 \ast) \ \forall 0 \in \textsc{natural}^n
\]

\[
(\ast \ ?! \ast) \ \forall 0 \ (\ast \ ? \ast) \ \forall 2 \leq M \land \neg (M + 1) \ (\ast \ ? \ast) \ \forall 2 \leq M^\gamma
\]

Throw away the irrelevant assumption then forward chain with natural_thm:

\[
\text{SML}
\]

\[
a(POP\_ASM\_T \ discard\_tac \ THEN \ all\_fc\_tac[natural\_thm]);
\]

ProofPower output

\
\[
(\ast \ 2 \ast) \ \forall M \in \textsc{natural}^n
\]

\[
(\ast \ 1 \ast) \ \forall 0 \leq M^\gamma
\]

\[
(\ast \ ?! \ast) \ \forall 2 \leq M \land \neg (M + 1) \ (\ast \ ? \ast) \ \forall 2 \leq M^\gamma
\]

We no longer need assumption 2. Then rewriting with the assumptions and \textit{star}\_\textit{star}\_\textit{2}\_\textit{thm} will deal with the first conjunct in the goal and expand the second:

\[
\text{SML}
\]

\[
a(DROP\_NTH\_ASM\_T \ 2 \ discard\_tac);
\]

\[
a(asm\_rewrite\_tac[star\_star\_2\_thm]);
\]

The proof is progressed by induction on \(M\):

\[
\text{SML}
\]

\[
a(\leq\_induction\_tac\_\gamma M^\gamma);
\]

ProofPower output

\[
\text{Tactic produced 2 subgoals:}
\]

\[
(\ast \ *** \ \text{Goal } "2" \ *** \ \ast)
\]

\[
(\ast \ 2 \ast) \ \forall 0 \leq i^\gamma
\]

\[
(\ast \ 1 \ast) \ \forall (i + 1) \ast (i + 1) \leq i^\gamma
\]

\[
(\ast \ ?! \ast) \ \forall ((i + 1) + 1) \ast ((i + 1) + 1) \leq i + 1^\gamma
\]

\[
(\ast \ *** \ \text{Goal } "1" \ *** \ \ast)
\]

\[
(\ast \ ?! \ast) \ \forall (0 + 1) \ast (0 + 1) \leq 0^\gamma
\]

The first subgoal is proved by rewriting in the current proof context. The second is proved automatically in the linear arithmetic proof context:
4.2. Proving the VCs

SML

\[
\begin{align*}
&\begin{array}{l}
(\ast \ast \ast \ Goal \ "1" \ \ast \ast \ast \ \ast \\
&(\ast \ast \ast \ Goal \ "2" \ \ast \ast \ast \ \ast)
\end{array} \\
&\text{a(rewrite_tac[]);}
&\text{a(PC_T1 \ "z_lin_arith" \ asm_prove_tac[]);}
&\text{save_pop_thm "vc2002_1";}
\end{align*}
\]

ProofPower output

Tactic produced 0 subgoals:
Current and main goal achieved
...

4.2.6 Digit Button VCs

Two VCs result from the if statement in the digit button procedure, see section 4.1.3.3.5. For example:

\[
\forall \ GVoDISPLAY : TCoNUMBER, \ GVoIN\_NUMBER : BOOLEAN; \ D : \ TCoDIGIT \\
\begin{array}{l}
\quad | \ true \ \land \ GVoIN\_NUMBER = \text{TRUE} \\
\quad \quad \bullet \ (D \equiv D, \ GVoDISPLAY \equiv GVoDISPLAY \ \ast TCoBASE + D, \\
\quad \quad \quad GVoDISPLAY_0 \equiv GVoDISPLAY, \ GVoIN\_NUMBER \equiv \text{TRUE}, \\
\quad \quad \quad GVoIN\_NUMBER_0 \equiv GVoIN\_NUMBER)
\end{array} \\
\in \text{DO}\_DIGIT
\]

Both are proven by simplifying, stripping and then rewriting with the definition of DO\_DIGIT:

SML

\[
\begin{align*}
&\text{open_scope "OPS.DIGIT\_BUTTON";} \\
&\text{set_pc "cn1";} \\
&\text{set_goal([], get_conjecture"=""vc3001_1")}; \\
&\text{a(cn vc simp_tac[]);} \\
&\text{a(REPEAT strip_tac THEN asm_rewrite_tac[cn DO\_DIGIT thm])}; \\
&\text{save_pop_thm"vc3001_1";}
\end{align*}
\]

4.2.7 Operations Button VCs

Eight VCs result from the branches in the operations button procedure, 3 unary and 5 binary, see section 4.1.3.3.6. For example, the unary operation produces:
∀ GVoDISPLAY, GVoACCUMULATOR : TCoNUMBER;
   GVoLAST_OP : TCoOPERATION;
   O : TCoOPERATION
| true ∧ O eq TCoCHANGE_SIGN = TRUE
• (GVoACCUMULATOR = GVoACCUMULATOR,
   GVoACCUMULATOR₀ = GVoACCUMULATOR,
   GVoDISPLAY = GVoDISPLAY, GVoDISPLAY₀ = GVoDISPLAY,
   GVoIN_NUMBER = FALSE, GVoLAST_OP = GVoLAST_OP,
   GVoLAST_OP₀ = GVoLAST_OP, O = O)
∈ DO_OPERATION

To prove this VC, we do need to unwind the basic definitions, so we will use the list of supporting theorems calc.thms that we generated before starting out on the proofs, section 4.2.1. (We could equally well have chosen to rewrite in the supporting proof context calc.cn with no additional theorems.) This proof gives an example of where REPEAT strip_tac would be too brutal because it would generate many subgoals.

SML
open_scope "OPS.OPERATION_BUTTON";
set_pc "cn1";
set_goal([], get_conjecture="vc3002_1");
a(cn_vc_simp_tac calc.thms);
a ⇒_tac;
a(asm_rewrite_tac[]);
save_pop_thm"vc3002_1";

The remaining two unary operations VCs require us to make the (reasonable) assumption that a non-negative number of the precision handled by the calculator will fit in a SPARK NATURAL. This amounts to the following axiom:

```
Z
| TCoMAX_NUMBER ≤ INTEGER₀LAST
```

SML
val number_ax = get_axiom="Constraint 1";

To prove vc3002_2, apply the same tactics as for vc3002_1:

SML
set_goal([], get_conjecture="vc3002_2");
a(cn_vc_simp_tac calc.thms);
a ⇒_tac;
a(asm_rewrite_tac[]);
4.2. Proving the VCs

ProofPower output

...  
(* 3*) \( \exists x \leq 999999 \leq G\text{Vo}\text{ACCUMULATOR} \)
(* 4*) \( \exists x \leq 999999 \leq G\text{Vo}\text{DISPLAY} \)
(* 5*) \( \exists x \leq 999999 \leq G\text{Vo}\text{DISPLAY} \)
(* 6*) \( \exists x \leq G\text{Vo}\text{LAST}_\text{OP} \)
(* 7*) \( \exists x \leq G\text{Vo}\text{DISPLAY} \)
(* 8*) \( \exists x \leq G\text{Vo}\text{DISPLAY} \)
(* 9*) \( \exists x \leq G\text{Vo}\text{DISPLAY} \)
(* 10*) \( \exists x \leq G\text{Vo}\text{DISPLAY} \)

Then remove the redundant assumptions and strip the goal:

SML

```sml
a(\text{LIST\_DROP\_NTH\_ASM\_T}[1,2,3,4,5,6,8,9,10] \ (\text{MAP\_EVERY} \ \text{discard\_tac}));
a \text{strip\_tac};
```

ProofPower output

...  
(* 3*) \( \exists x \leq 999999 \leq G\text{Vo}\text{DISPLAY} \)
(* 4*) \( \exists x \leq \text{FACT} \ G\text{Vo}\text{DISPLAY} \)

All we need now is to add to the assumptions the fact that \( G\text{Vo}\text{DISPLAY} \in \text{NATURAL} \), then forward chain with the definition of \( \text{FACT} \). Notice that during the proof of the lemma we switch to the proof context \( \text{calc\_cn} \) which contains the supporting theorems generated in section 4.2.1.

SML

```sml
a(\text{lemma\_tac} \ \exists x \ G\text{Vo}\text{DISPLAY} \in \text{NATURAL});
(* 3*) \( \exists x \leq \text{FACT} \ G\text{Vo}\text{DISPLAY} \)
```

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The proof of \( vc3002.3 \) will be almost identical, except that the last step will be to forward chain with the definition of \( \text{SQRT} \).

Because the binary operations only involve built-in arithmetic operators, they are a little easier to verify than the unary ones. For example the VC:

\[
\begin{align*}
\forall & \ GVo\text{DISPLAY}, \ GVo\text{ACCUMULATOR} : \ TCo\text{NUMBER}; \\
& \quad \ GVo\text{LAST}_\text{OP} : \ TCo\text{OPERATION}; \\
& \quad \ O : \ TCo\text{OPERATION}
\end{align*}
\]

\[
\begin{align*}
& \quad \quad \text{true} \\
& \quad \quad \land \ O \ eq \ TCo\text{CHANGE}\_\text{SIGN} = \text{FALSE} \\
& \quad \quad \land \ O \ eq \ TCo\text{FACTORIAL} = \text{FALSE} \\
& \quad \quad \land \ O \ eq \ TCo\text{SQUARE}\_\text{ROOT} = \text{FALSE} \\
& \quad \quad \land \ GVo\text{LAST}_\text{OP} \ eq \ TCo\text{EQUALS} = \text{TRUE}
\end{align*}
\]

\[
\begin{align*}
\bullet \ GVo\text{ACCUMULATOR} \equiv GVo\text{DISPLAY}, \ GVo\text{ACCUMULATOR}_\text{0} \equiv GVo\text{ACCUMULATOR}, \\
& \quad \ GVo\text{DISPLAY} \equiv GVo\text{DISPLAY}, \ GVo\text{DISPLAY}_\text{0} \equiv GVo\text{DISPLAY}, \\
& \quad \ GVo\text{IN}\_\text{NUMBER} \equiv \text{FALSE}, \ GVo\text{LAST}_\text{OP} \equiv O, \ GVo\text{LAST}_\text{OP}_\text{0} \equiv GVo\text{LAST}_\text{OP}, \\
& \quad \ O \ eq \ O
\end{align*}
\]

will be proved by the following:

SML:

\[
\begin{align*}
\text{sml}
\text{set_goal([], get_conjecture"-"} "vc3002.4"\text{);} \\
\text{a(} \text{PC}\_\text{T1} \text{"calc_prelims"} \text{cn}_\text{vc}\_\text{simp_tac}[])\text{;} \\
\text{a(} \text{\Rightarrow_tac THEN asm_rewrite_tac[]}\text{);} \\
\text{save_pop_thm"} \text{vc3002.4"}\text{;}
\end{align*}
\]

The same proof will suffice for the remaining four binary operation VCs.
EXAMPLE THEORY LISTINGS

The examples in this document give rise to 13 theories as follows:

A.1  THE Z THEORY EX’proc

A.1.1  Parents

cn

A.1.2  Global Variables

T
CONSTSPEC
SWITCHTYPE
SWITCHTYPEvFIRST
SWITCHTYPEvLAST
SWITCHTYPEvSUCC
SWITCHTYPEvPRED
SWITCHTYPEvPOS
SWITCHTYPEvVAL
SWITCHDATA
SWITCHDATAuSTATE[g1, g2]
[NEXTSWITCH : g2; STATE : g1] × g1
↔ [NEXTSWITCH : g2; STATE : g1]
SWITCHDATAuNEXTSWITCH[g1, g2]
[NEXTSWITCH : g2; STATE : g1] × g2
↔ [NEXTSWITCH : g2; STATE : g1]
SWITCHBOARD
SWITCHBOARDvFIRST
Z
SWITCHBOARDvLAST
Z
SWITCHBOARDvLENGTH
Z
SWITCHBOARDvRANGE
P Z
A.1.3 Axioms

\[ \text{CONSTSPEC} \vdash \text{CONSTSPEC} \in \mathbb{P}_1 \ W \rightarrow \mathbb{N} \land \text{true} \]

SWITCHDATA \sqcup \text{STATE}

SWITCHDATA \sqcup \text{NEXTSWITCH}

\[ \vdash [g_1, g_2] ((\text{SWITCHDATA}\sqcup \text{STATE}[g_1, g_2]) \in [\text{STATE} : g_1; \text{NEXTSWITCH} : g_2] \times g_1 \]

\[ \land \text{SWITCHDATA}\sqcup \text{NEXTSWITCH}[g_1, g_2] \in [\text{STATE} : g_1; \text{NEXTSWITCH} : g_2] \times g_2 \rightarrow [\text{STATE} : g_1; \text{NEXTSWITCH} : g_2]) \]

\[ \land (\forall r : [\text{STATE} : g_1; \text{NEXTSWITCH} : g_2]; \]

\[ x_1 : g_1; \]

\[ x_2 : g_2 \]

\[ \bullet \text{SWITCHDATA}\sqcup \text{STATE}[g_1, g_2] (r, x_1) = (\text{NEXTSWITCH} \equiv r.\text{NEXTSWITCH}, \text{STATE} \equiv x_1) \]

\[ \land \text{SWITCHDATA}\sqcup \text{NEXTSWITCH}[g_1, g_2] (r, x_2) = (\text{NEXTSWITCH} \equiv x_2, \text{STATE} \equiv r.\text{STATE})) \]

A.1.4 Definitions

\[ \text{T} \vdash T = \mathbb{U} \]

SWITCHTYPE \vdash \text{SWITCHTYPE} = 1 .. 3

SWITCHTYPEvFIRST \vdash \text{SWITCHTYPEvFIRST} = 1

SWITCHTYPEvLAST \vdash \text{SWITCHTYPEvLAST} = 3

SWITCHTYPEvSUCC \vdash \text{SWITCHTYPEvSUCC} = \text{INTEGERvSUCC}

SWITCHTYPEvPRED \vdash \text{SWITCHTYPEvPRED} = \text{INTEGERvPRED}

SWITCHTYPEvPOS \vdash \text{SWITCHTYPEvPOS} = \text{INTEGERvPOS}

SWITCHTYPEvVAL \vdash \text{SWITCHTYPEvVAL} = \text{INTEGERvVAL}

SWITCHDATA \vdash \text{SWITCHDATA} = [\text{STATE} : \text{BOOLEAN}; \text{NEXTSWITCH} : \text{SWITCHTYPE}]

SWITCHBOARD \vdash \text{SWITCHBOARD} = \text{SWITCHTYPE} \rightarrow \text{SWITCHDATA}

SWITCHBOARDvFIRST \vdash \text{SWITCHBOARDvFIRST} = \text{SWITCHTYPEvFIRST}

SWITCHBOARDvLAST
\[ \vdash \text{SWITCHBOARD}_v^\text{LENGTH} = \text{SWITCHTYPE}_v^\text{LENGTH} \]
\[ \vdash \text{SWITCHBOARD}_v^\text{LENGTH} = \# \text{SWITCHTYPE} \]
\[ \vdash \text{SWITCHBOARD}_v^\text{RANGE} = \text{SWITCHTYPE} \]
\[ \vdash \text{SWITCHBOARD}_v^\text{FIRST}_v^1 = \text{SWITCHTYPE}_v^\text{FIRST} \]
\[ \vdash \text{SWITCHBOARD}_v^\text{LAST}_v^1 = \text{SWITCHTYPE}_v^\text{LAST} \]
\[ \vdash \text{SWITCHBOARD}_v^\text{LENGTH}_v^1 = \# \text{SWITCHTYPE} \]
\[ \vdash \text{SWITCHBOARD}_v^\text{RANGE}_v^1 = \text{SWITCHTYPE} \]
\[ \vdash \text{SWITCHBOARD}_v^\text{LENGTH}_v^1 = \# \text{SWITCHTYPE} \]
\[ \vdash \text{SWITCHBOARD}_v^\text{RANGE}_v^1 = \text{SWITCHTYPE} \]
\[ \vdash \text{SWITCHBOARD}_v^\text{RANGE} = \text{SWITCHTYPE} \]
\[ \vdash \text{SWITCHBOARD}_v^\text{LENGTH} = \# \text{SWITCHTYPE} \]
\[ \vdash \text{SWITCHBOARD}_v^\text{LAST} = \text{SWITCHTYPE}_v^\text{LAST} \]
\[ \vdash \text{SWITCHBOARD}_v^\text{FIRST} = \text{SWITCHTYPE}_v^\text{FIRST} \]
\[ \vdash \text{SWITCHBOARD} = \text{SWITCHTYPE} \rightarrow \text{SWITCHDATA} \]
\[ \vdash \text{SWITCHDATA} = [\text{NEXTSWITCH} : \text{SWITCHTYPE} ; \text{STATE} : \text{BOOLEAN}] \]
\[ \vdash \text{SWITCHTYPE}_v^\text{VAL} = \text{INTEGER}_v^\text{VAL} \]
\[ \vdash \text{SWITCHTYPE}_v^\text{POS} = \text{INTEGER}_v^\text{POS} \]
\[ \vdash \text{SWITCHTYPE}_v^\text{PRED} = \text{INTEGER}_v^\text{PRED} \]
\[ \vdash \text{SWITCHTYPE}_v^\text{SUCC} = \text{INTEGER}_v^\text{SUCC} \]
\[ \vdash \text{SWITCHTYPE}_v^\text{LAST} = 3 \]
\[ \vdash \text{SWITCHTYPE}_v^\text{FIRST} = 1 \]
cn_SWITCHTYPE thm
  ⊢ SWITCHTYPE = 1 .. 3

cn_T thm
  ⊢ T = ∪

cn_SWITCHDATAuSTATE sig thm
  ⊢ |g1, g2|(SWITCHDATAuSTATE[g1, g2]
  ∈ [STATE : g1; NEXTSWITCH : g2] × g1
  → [STATE : g1; NEXTSWITCH : g2])

cn_SWITCHDATAuNEXTSWITCH sig thm
  ⊢ |g1, g2|(SWITCHDATAuNEXTSWITCH[g1, g2]
  ∈ [STATE : g1; NEXTSWITCH : g2] × g2
  → [STATE : g1; NEXTSWITCH : g2])

cn_SWITCHDATAuSTATE thm
  ⊢ |g1, g2|∀ r : [STATE : g1; NEXTSWITCH : g2];
    x1 : g1;
    x2 : g2
    • SWITCHDATAuSTATE[g1, g2] (r, x1)
      = (NEXTSWITCH ≡ r.NEXTSWITCH, STATE ≡ x1)
    ∧ SWITCHDATAuNEXTSWITCH[g1, g2] (r, x2)
      = (NEXTSWITCH ≡ x2, STATE ≡ r.STATE))

cn_CONSTSPEC sig thm
  ⊢ CONSTSPEC ∈ P₁ T ↠ N

A.2 THE Z THEORY REAL_EG’spec

A.2.1 Parents

A.2.2 Children

REAL_EGoINTERPOLATE′ context REAL_EG′body

A.2.3 Global Variables

REAL_EGoANGLE
  P R
REAL_EGoANGLEvDELTA
  R
REAL_EGoANGLEvFIRST
  R
REAL_EGoANGLEvLAST
  R
A.3. THE Z THEORY REAL_EG’body

A.3.1 Parents

\[ \text{REAL\_EG}'spec \quad cn \]

A.4 THE Z THEORY REAL_EGoINTERPOLATE’proc

A.4.1 Parents

\[ \text{REAL\_EGoINTERPOLATE}'context \]

A.4.2 Conjectures

\text{vcREAL\_EGoINTERPOLATE\_1}
\[ \forall A, B : \text{REAL\_EGoANGLE} \quad | \quad A +_R \text{REAL\_EGoANGLEvDELTA} <_R B \]
\[ \land A +_R \text{REAL\_EGoANGLEvDELTA real\_less} B = \text{TRUE} \]
\[ \bullet A <_R A +_R \text{REAL\_EGoANGLEvDELTA} \quad \land \quad A +_R \text{REAL\_EGoANGLEvDELTA} <_R B \]

\text{vcREAL\_EGoINTERPOLATE\_2}
\[ \forall A, B, C : \text{REAL\_EGoANGLE} \quad | \quad A +_R \text{REAL\_EGoANGLEvDELTA} <_R B \]
\[ \land A +_R \text{REAL\_EGoANGLEvDELTA real\_less} B = \text{FALSE} \]
\[ \bullet A <_R C \land C <_R B \]

A.4.3 Theorems

\text{vcREAL\_EGoINTERPOLATE\_1}
\[ \vdash \forall A, B : \text{REAL\_EGoANGLE} \quad | \quad A +_R \text{REAL\_EGoANGLEvDELTA} <_R B \]
\[ \land A +_R \text{REAL\_EGoANGLEvDELTA real\_less} B = \text{TRUE} \]
\[ \bullet A <_R A +_R \text{REAL\_EGoANGLEvDELTA} \quad \land \quad A +_R \text{REAL\_EGoANGLEvDELTA} <_R B \]

\text{vcREAL\_EGoINTERPOLATE\_2}
\[ \vdash \forall A, B, C : \text{REAL\_EGoANGLE} \quad | \quad A +_R \text{REAL\_EGoANGLEvDELTA} <_R B \]
\[ \land A +_R \text{REAL\_EGoANGLEvDELTA real\_less} B = \text{FALSE} \]
\[ \bullet A <_R C \land C <_R B \]
A.5 THE Z THEORY TC'spec

A.5.1 Parents

cn

A.5.2 Children

\[ \text{OPSoOPERATION\_BUTTON'}\ context \quad \text{OPT'}\ spec \]
\[ \text{OPSoDIGIT\_BUTTON'}\ context \quad \text{GV'}\ spec \]
\[ \text{OPT'}\ body \]

A.5.3 Global Variables

\[
\begin{align*}
\text{TCoBASE} & \quad Z \\
\text{TCoPRECISION} & \quad Z \\
\text{TCoMAX\_NUMBER} & \quad Z \\
\text{TCoMIN\_NUMBER} & \quad Z \\
\text{TCoDIGIT} & \quad P \quad Z \\
\text{TCoDIGIT\_vFIRST} & \quad Z \\
\text{TCoDIGIT\_vLAST} & \quad Z \\
\text{TCoDIGIT\_vSUCC} & \quad Z \leftrightarrow Z \\
\text{TCoDIGIT\_vPRED} & \quad Z \leftrightarrow Z \\
\text{TCoDIGIT\_vPOS} & \quad Z \leftrightarrow Z \\
\text{TCoDIGIT\_vVAL} & \quad Z \leftrightarrow Z \\
\text{TCoNUMBER} & \quad P \quad Z \\
\text{TCoNUMBER\_vFIRST} & \quad Z \\
\text{TCoNUMBER\_vLAST} & \quad Z \\
\text{TCoNUMBER\_vSUCC} & \quad Z \leftrightarrow Z \\
\text{TCoNUMBER\_vPRED} & \quad Z \leftrightarrow Z \\
\text{TCoNUMBER\_vPOS} & \quad Z \leftrightarrow Z \\
\text{TCoNUMBER\_vVAL} & \quad Z \leftrightarrow Z \\
\text{TCoPLUS} & \quad Z \\
\text{TCoMINUS} & \quad Z \\
\text{TCoTIMES} & \quad Z \\
\text{TCoCHANGE\_SIGN} & \quad Z \\
\text{TCoSQUARE\_ROOT} & \quad Z
\end{align*}
\]
A.5. THE Z THEORY TC’spec

TC\text{FACTORIAL} \quad Z

TC\text{EQUALS} \quad Z

TC\text{OPERATION} \quad P \ Z

TC\text{OPERATIONvFIRST} \quad Z

TC\text{OPERATIONvLAST} \quad Z

TC\text{OPERATIONvSUCC} \quad Z \leftrightarrow Z

TC\text{OPERATIONvPRED} \quad Z \leftrightarrow Z

TC\text{OPERATIONvPOS} \quad Z \leftrightarrow Z

TC\text{OPERATIONvVAL} \quad Z \leftrightarrow Z

A.5.4 Axioms

TC\text{BASE} \quad \vdash TC\text{BASE} \in \text{INTEGER} \land TC\text{BASE} = 10

TC\text{PRECISION} \quad \vdash TC\text{PRECISION} \in \text{INTEGER} \land TC\text{PRECISION} = 6

TC\text{MAX\_NUMBER} \quad \vdash TC\text{MAX\_NUMBER} \in \text{INTEGER} \\
\quad \land TC\text{MAX\_NUMBER} = TC\text{BASE} \ast TC\text{PRECISION} - 1

TC\text{MIN\_NUMBER} \quad \vdash TC\text{MIN\_NUMBER} \in \text{INTEGER} \\
\quad \land TC\text{MIN\_NUMBER} = \sim TC\text{MAX\_NUMBER}

A.5.5 Definitions

TC\text{DIGIT} \quad \vdash TC\text{DIGIT} = 0 .. TC\text{BASE} - 1

TC\text{DIGITvFIRST} \quad \vdash TC\text{DIGITvFIRST} = 0

TC\text{DIGITvLAST} \quad \vdash TC\text{DIGITvLAST} = TC\text{BASE} - 1

TC\text{DIGITvSUCC} \quad \vdash TC\text{DIGITvSUCC} = \text{INTEGERvSUCC}

TC\text{DIGITvPRED} \quad \vdash TC\text{DIGITvPRED} = \text{INTEGERvPRED}

TC\text{DIGITvPOS} \quad \vdash TC\text{DIGITvPOS} = \text{INTEGERvPOS}

TC\text{DIGITvVAL} \quad \vdash TC\text{DIGITvVAL} = \text{INTEGERvVAL}

TC\text{NUMBER} \quad \vdash TC\text{NUMBER} = TC\text{MIN\_NUMBER} .. TC\text{MAX\_NUMBER}

TC\text{NUMBERvFIRST} \quad \vdash TC\text{NUMBERvFIRST} = TC\text{MIN\_NUMBER}

TC\text{NUMBERvLAST} \quad \vdash TC\text{NUMBERvLAST} = TC\text{MAX\_NUMBER}

TC\text{NUMBERvSUCC} \quad \vdash TC\text{NUMBERvSUCC} = \text{INTEGERvSUCC}

TC\text{NUMBERvPRED} \quad \vdash TC\text{NUMBERvPRED} = \text{INTEGERvPRED}

TC\text{NUMBERvPOS} \quad \vdash TC\text{NUMBERvPOS} = \text{INTEGERvPOS}
TCoNUMBER\textsubscript{vVAL}
\vdash TCoNUMBER\textsubscript{vVAL} = INTEGER\textsubscript{vVAL}

TCoPLUS
\vdash TCoPLUS = 0

TCoMINUS
\vdash TCoMINUS = 1

TCoTIMES
\vdash TCoTIMES = 2

TCoCHANGE\_SIGN
\vdash TCoCHANGE\_SIGN = 3

TCoSQUARE\_ROOT
\vdash TCoSQUARE\_ROOT = 4

TCoFACTORIAL
\vdash TCoFACTORIAL = 5

TCoEQUALS
\vdash TCoEQUALS = 6

TCoOPERATION
\vdash TCoOPERATION = TCoPLUS .. TCoEQUALS

TCoOPERATION\textsubscript{vFIRST}
\vdash TCoOPERATION\textsubscript{vFIRST} = TCoPLUS

TCoOPERATION\textsubscript{vLAST}
\vdash TCoOPERATION\textsubscript{vLAST} = TCoEQUALS

TCoOPERATION\textsubscript{vSUCC}
\vdash TCoOPERATION\textsubscript{vSUCC} = (TCoOPERATION \ \{TCoOPERATION\textsubscript{vLAST}\}) < succ

TCoOPERATION\textsubscript{vPRED}
\vdash TCoOPERATION\textsubscript{vPRED} = TCoOPERATION\textsubscript{vSUCC} ~

TCoOPERATION\textsubscript{vPOS}
\vdash TCoOPERATION\textsubscript{vPOS} = id TCoOPERATION

TCoOPERATION\textsubscript{vVAL}
\vdash TCoOPERATION\textsubscript{vVAL} = TCoOPERATION\textsubscript{vPOS} ~

A.6 THE Z THEORY GV\textsuperscript{\textprime}spec

A.6.1 Parents

\[TC\textsuperscript{\textprime}spec \quad cn\]

A.6.2 Children

\[OPS\textsuperscript{\textprime}OPERATION\_BUTTON\textsuperscript{\textprime}context \quad OPS\textsuperscript{\textprime}spec\]
\[OPS\textsuperscript{\textprime}DIGIT\_BUTTON\textsuperscript{\textprime}context \quad calc\_prelims\]
\[OPS\textsuperscript{\textprime}body\]

A.7 THE Z THEORY calc\_prelims

A.7.1 Parents

\[GV\textsuperscript{\textprime}spec\]

A.7.2 Children

\[OPS\textsuperscript{\textprime}spec\]
A.7.3 Global Variables

\[ \text{DO\_DIGIT} \quad \vdash \quad [D, \text{GVoDISPLAY}_0, \text{GVoDISPLAY}_\theta, \text{GVoIN\_NUMBER}_0, \text{GVoIN\_NUMBER}_\theta : \mathbb{Z}] \]

\[ \text{UNARY} \quad \vdash \quad \mathbb{P} \ Z \]

\[ \text{BINARY} \quad \vdash \quad \mathbb{P} \ Z \]

\[ \text{fact} \quad \vdash \quad Z \mapsto Z \]

\[ \text{DO\_UNARY\_OPERATION} \quad \vdash \quad [\text{GVoACCUMULATOR}_0, \text{GVoACCUMULATOR}_\theta, \text{GVoDISPLAY}_0, \text{GVoDISPLAY}_\theta, \text{GVoIN\_NUMBER}_0, \text{GVoIN\_NUMBER}_\theta, \text{GVoLAST\_OP}_0, \text{GVoLAST\_OP}_\theta, O : \mathbb{Z}] \]

\[ \text{DO\_BINARY\_OPERATION} \quad \vdash \quad [\text{GVoACCUMULATOR}_0, \text{GVoACCUMULATOR}_\theta, \text{GVoDISPLAY}_0, \text{GVoDISPLAY}_\theta, \text{GVoIN\_NUMBER}_0, \text{GVoIN\_NUMBER}_\theta, \text{GVoLAST\_OP}_0, \text{GVoLAST\_OP}_\theta, O : \mathbb{Z}] \]

\[ \text{DO\_OPERATION} \quad \vdash \quad [\text{GVoACCUMULATOR}_0, \text{GVoACCUMULATOR}_\theta, \text{GVoDISPLAY}_0, \text{GVoDISPLAY}_\theta, \text{GVoIN\_NUMBER}_0, \text{GVoIN\_NUMBER}_\theta, \text{GVoLAST\_OP}_0, \text{GVoLAST\_OP}_\theta, O : \mathbb{Z}] \]

A.7.4 Axioms

\[ \vdash \text{fact} \in \mathbb{N} \to \mathbb{N} \]
\[ \quad \wedge \text{fact } 0 = 1 \]
\[ \quad \wedge (\forall m : \mathbb{N} \bullet \text{fact } (m + 1) = (m + 1) \ast \text{fact } m) \]

A.7.5 Definitions

\[ \vdash \text{DO\_DIGIT} \]
\[ = [\text{GVoDISPLAY}_\theta, \text{GVoDISPLAY} : \mathbb{Z}; \]
\[ \text{GVoIN\_NUMBER}_\theta, \text{GVoIN\_NUMBER} : \text{BOOLEAN}; \]
\[ D : \text{TCo\_DIGIT} \]
\[ \quad \mid (\text{GVoIN\_NUMBER}_\theta = \text{TRUE} \]
\[ \quad \quad \Rightarrow \text{GVoDISPLAY} = \text{GVoDISPLAY}_\theta \ast \text{TCo\_BASE} + D) \]
\[ \quad \wedge (\text{GVoIN\_NUMBER}_\theta = \text{FALSE} \Rightarrow \text{GVoDISPLAY} = D) \]
\[ \wedge \text{GVoIN\_NUMBER} = \text{TRUE} \]

\[ \vdash \text{UNARY} \]
\[ = \{\text{TCo\_CHANGE\_SIGN}, \text{TCo\_FACTORIAL}, \text{TCo\_SQUARE\_ROOT}\} \]

\[ \vdash \text{BINARY} = \text{TCo\_OPERATION} \setminus \text{UNARY} \]

\[ \vdash \text{DO\_UNARY\_OPERATION} \]
\[ = [\text{GVoACCUMULATOR}_\theta, \text{GVoACCUMULATOR} : \mathbb{Z}; \]
\[ \text{GVoDISPLAY}_\theta, \text{GVoDISPLAY} : \mathbb{Z}; \]
\[ \text{GVoLAST\_OP}_\theta, \text{GVoLAST\_OP} : \mathbb{Z}; \]
\[ \text{GVoIN\_NUMBER} : \text{BOOLEAN}; \]
\[ O : \text{UNARY} \]
\[ \quad \mid \text{GVoIN\_NUMBER} = \text{FALSE} \]
\[ \quad \wedge \text{GVoACCUMULATOR} = \text{GVoACCUMULATOR}_\theta \]
∧ GV\text{LAST\_OP} = GV\text{LAST\_OP}_0
∧ (O = TCo\text{CHANGE\_SIGN})
⇒ GV\text{DISPLAY} = \sim GV\text{DISPLAY}_0
∧ (O = TCo\text{FACTORIAL} \land GV\text{DISPLAY}_0 \geq 0)
⇒ GV\text{DISPLAY} = \text{fact} \ GV\text{DISPLAY}_0
∧ (O = TCo\text{SQUARE\_ROOT} \land GV\text{DISPLAY}_0 \geq 0)
⇒ GV\text{DISPLAY} ** 2 \leq GV\text{DISPLAY}_0
∧ GV\text{DISPLAY}_0 < (GV\text{DISPLAY} + 1) ** 2)

\text{DO\_BINARY\_OPERATION}
\vdash \text{DO\_BINARY\_OPERATION}
\begin{align*}
&= \left[ GV\text{ACCUMULATOR}_0, \ GV\text{ACCUMULATOR} : \mathbb{Z}; \\
&\quad GV\text{DISPLAY}_0, \ GV\text{DISPLAY} : \mathbb{Z}; \\
&\quad GV\text{LAST\_OP}_0, \ GV\text{LAST\_OP} : \mathbb{Z}; \\
&\quad GV\text{IN\_NUMBER} : \text{BOOLEAN}; \\
&\quad O : \text{BINARY} \\
&\quad | \ GV\text{IN\_NUMBER} = \text{FALSE} \\
&\quad \land \ GV\text{DISPLAY} = GV\text{ACCUMULATOR} \\
&\quad \land (GV\text{LAST\_OP}_0 = O) \\
&\quad \land (GV\text{LAST\_OP}_0 = TCo\text{EQUALS})
\Rightarrow GV\text{ACCUMULATOR} = GV\text{DISPLAY}_0 \\
&\quad \land (GV\text{LAST\_OP}_0 = TCo\text{PLUS})
\Rightarrow GV\text{ACCUMULATOR} \\
&\quad = GV\text{ACCUMULATOR}_0 + GV\text{DISPLAY}_0 \\
&\quad \land (GV\text{LAST\_OP}_0 = TCo\text{MINUS})
\Rightarrow GV\text{ACCUMULATOR} \\
&\quad = GV\text{ACCUMULATOR}_0 - GV\text{DISPLAY}_0 \\
&\quad \land (GV\text{LAST\_OP}_0 = TCo\text{TIMES})
\Rightarrow GV\text{ACCUMULATOR} \\
&\quad = GV\text{ACCUMULATOR}_0 \ast GV\text{DISPLAY}_0 \right]
\end{align*}

\text{DO\_OPERATION}
\vdash \text{DO\_OPERATION}
\begin{align*}
&= (DO\_UNARY\_OPERATION \lor DO\_BINARY\_OPERATION)
\end{align*}

A.7.6 Theorems

\text{cn\_TCo\text{OPERATION\_v\_VAL}}_{\text{thm}}
\vdash \forall i : TCo\text{OPERATION} \bullet TCo\text{OPERATION\_v\_VAL} i = i

\text{cn\_TCo\text{OPERATION\_v\_VAL}}_{\text{sig\_thm}}
\vdash TCo\text{OPERATION\_v\_VAL} \in TCo\text{OPERATION} \rightarrow TCo\text{OPERATION}

\text{cn\_TCo\text{OPERATION\_v\_POS}}_{\text{thm}}
\vdash \forall i : TCo\text{OPERATION} \bullet TCo\text{OPERATION\_v\_POS} i = i

\text{cn\_TCo\text{OPERATION\_v\_POS}}_{\text{sig\_thm}}
\vdash TCo\text{OPERATION\_v\_POS} \in TCo\text{OPERATION} \rightarrow TCo\text{OPERATION}

\text{cn\_TCo\text{OPERATION\_v\_PRED}}_{\text{thm}}
\vdash \forall i : TCoPLUS + 1 .. TCo\text{EQUALS} \bullet TCo\text{OPERATION\_v\_PRED} i = i + \sim 1

\text{cn\_TCo\text{OPERATION\_v\_PRED}}_{\text{sig\_thm}}
\vdash TCo\text{OPERATION\_v\_PRED} \in TCoPLUS + 1 .. TCo\text{EQUALS}
\rightarrow TCoPLUS .. TCo\text{EQUALS} + \sim 1

\text{cn\_TCo\text{OPERATION\_v\_SUCC}}_{\text{thm}}
\vdash \forall i : TCoPLUS .. TCo\text{EQUALS} + \sim 1
A.7. THE Z THEORY calc_prelims

- $TCoOPERATION\text{vSUCC} \ i = i + 1$

\[
\begin{align*}
\text{cn}_{\text{TCoOPERATION\text{vSUCC}_\text{sig.thm}}}
\vdash & \ TCoOPERATION\text{vSUCC} \\
& \in \ TCoPLUS \ .. \ TCoEQUALS + \sim 1 \\
& \rightarrow \ TCoPLUS + 1 \ .. \ TCoEQUALS
\end{align*}
\]

\[
\begin{align*}
\text{cn}_{\text{TCoOPERATION\text{vLAST}_\text{thm}}}
\vdash & \ TCoOPERATION\text{vLAST} = TCoEQUALS
\end{align*}
\]

\[
\begin{align*}
\text{cn}_{\text{TCoOPERATION\text{vFIRST}_\text{thm}}}
\vdash & \ TCoOPERATION\text{vFIRST} = TCoPLUS
\end{align*}
\]

\[
\begin{align*}
\text{cn}_{\text{TCoEQUALS}_\text{thm}}
\vdash & \ TCoEQUALS = 6
\end{align*}
\]

\[
\begin{align*}
\text{cn}_{\text{TCoFACTORIAL}_\text{thm}}
\vdash & \ TCoFACTORIAL = 5
\end{align*}
\]

\[
\begin{align*}
\text{cn}_{\text{TCoSQUARE\_ROOT}_\text{thm}}
\vdash & \ TCoSQUARE\_ROOT = 4
\end{align*}
\]

\[
\begin{align*}
\text{cn}_{\text{TCoCHANGE\_SIGN}_\text{thm}}
\vdash & \ TCoCHANGE\_SIGN = 3
\end{align*}
\]

\[
\begin{align*}
\text{cn}_{\text{TCoTIMES}_\text{thm}}
\vdash & \ TCoTIMES = 2
\end{align*}
\]

\[
\begin{align*}
\text{cn}_{\text{TCoMINUS}_\text{thm}}
\vdash & \ TCoMINUS = 1
\end{align*}
\]

\[
\begin{align*}
\text{cn}_{\text{TCoPLUS}_\text{thm}}
\vdash & \ TCoPLUS = 0
\end{align*}
\]

\[
\begin{align*}
\text{cn}_{\text{TCoNUMBER\text{vVAL}_\text{thm}}}
\vdash & \ TCoNUMBER\text{vVAL} = INTEGER\text{vVAL}
\end{align*}
\]

\[
\begin{align*}
\text{cn}_{\text{TCoNUMBER\text{vPOS}_\text{thm}}}
\vdash & \ TCoNUMBER\text{vPOS} = INTEGER\text{vPOS}
\end{align*}
\]

\[
\begin{align*}
\text{cn}_{\text{TCoNUMBER\text{vPRED}_\text{thm}}}
\vdash & \ TCoNUMBER\text{vPRED} = INTEGER\text{vPRED}
\end{align*}
\]

\[
\begin{align*}
\text{cn}_{\text{TCoNUMBER\text{vSUCC}_\text{thm}}}
\vdash & \ TCoNUMBER\text{vSUCC} = INTEGER\text{vSUCC}
\end{align*}
\]

\[
\begin{align*}
\text{cn}_{\text{TCoNUMBER\text{vLAST}_\text{thm}}}
\vdash & \ TCoNUMBER\text{vLAST} = TCoMAX\_NUMBER
\end{align*}
\]

\[
\begin{align*}
\text{cn}_{\text{TCoNUMBER\text{vFIRST}_\text{thm}}}
\vdash & \ TCoNUMBER\text{vFIRST} = TCoMIN\_NUMBER
\end{align*}
\]

\[
\begin{align*}
\text{cn}_{\text{TCoNUMBER}_\text{thm}}
\vdash & \ TCoNUMBER = TCoMIN\_NUMBER \ .. \ TCoMAX\_NUMBER
\end{align*}
\]

\[
\begin{align*}
\text{cn}_{\text{TCoDIGIT\text{vVAL}_\text{thm}}}
\vdash & \ TCoDIGIT\text{vVAL} = INTEGER\text{vVAL}
\end{align*}
\]

\[
\begin{align*}
\text{cn}_{\text{TCoDIGIT\text{vPOS}_\text{thm}}}
\vdash & \ TCoDIGIT\text{vPOS} = INTEGER\text{vPOS}
\end{align*}
\]

\[
\begin{align*}
\text{cn}_{\text{TCoDIGIT\text{vPRED}_\text{thm}}}
\vdash & \ TCoDIGIT\text{vPRED} = INTEGER\text{vPRED}
\end{align*}
\]

\[
\begin{align*}
\text{cn}_{\text{TCoDIGIT\text{vSUCC}_\text{thm}}}
\vdash & \ TCoDIGIT\text{vSUCC} = INTEGER\text{vSUCC}
\end{align*}
\]

\[
\begin{align*}
\text{cn}_{\text{TCoDIGIT\text{vLAST}_\text{thm}}}
\vdash & \ TCoDIGIT\text{vLAST} = TCoBASE + \sim 1
\end{align*}
\]

\[
\begin{align*}
\text{cn}_{\text{TCoDIGIT\text{vFIRST}_\text{thm}}}
\vdash & \ TCoDIGIT\text{vFIRST} = 0
\end{align*}
\]
\text{Appendix A. EXAMPLE THEORY LISTINGS}
\begin{verbatim}
cn_TCoDIGIT_thm
| TCoDIGIT = 0 .. TCoBASE + \sim 1
cn_TCoMIN_NUMBER_sig_thm
| TCoMIN_NUMBER \in \text{INTEGER}
cn_TCoMIN_NUMBER_thm
| TCoMIN_NUMBER = \sim TCoMAX_NUMBER
cn_TCoMAX_NUMBER_number_sig_thm
| TCoMAX_NUMBER \in \text{INTEGER}
cn_TCoMAX_NUMBER_number_thm
| TCoMAX_NUMBER = TCoBASE ** TCoPRECISION + \sim 1
cn_TCoPRECISION_sig_thm
| TCoPRECISION \in \text{INTEGER}
cn_TCoPRECISION_thm
| TCoPRECISION = 6
cn_TCoBASE_sig_thm
| TCoBASE \in \text{INTEGER}
cn_TCoBASE_thm
| TCoBASE = 10
cn_DO_OPERATION_thm
| \text{DO\_OPERATION} = (\text{DO\_UNARY\_OPERATION} \lor \text{DO\_BINARY\_OPERATION})
cn_DO_BINARY_OPERATION_thm
| \text{DO\_BINARY\_OPERATION} = [GVoACCUMULATOR : Z;
GVoACCUMULATOR_0 : Z;
GVoDISPLAY : Z;
GVoDISPLAY_0 : Z;
GVoIN\_NUMBER : BOOLEAN;
GVoLAST\_OP : Z;
GVoLAST\_OP_0 : Z;
O : BINARY
| GVoIN\_NUMBER = \text{FALSE}
\land GVoDISPLAY = GVoACCUMULATOR
\land GVoLAST\_OP = O
\land (GVoLAST\_OP_0 = TCoEQUALS
\Rightarrow GVoACCUMULATOR = GVoDISPLAY_0)
\land (GVoLAST\_OP_0 = TCoPLUS
\Rightarrow GVoACCUMULATOR
= GVoACCUMULATOR_0 + GVoDISPLAY_0)
\land (GVoLAST\_OP_0 = TCoMINUS
\Rightarrow GVoACCUMULATOR
= GVoACCUMULATOR_0 - GVoDISPLAY_0)
\land (GVoLAST\_OP_0 = TCoTIMES
\Rightarrow GVoACCUMULATOR
= GVoACCUMULATOR_0 \ast GVoDISPLAY_0)]
cn_DO_UNARY_OPERATION_thm
| \text{DO\_UNARY\_OPERATION} = [GVoACCUMULATOR : Z;
GVoACCUMULATOR_0 : Z;
GVoDISPLAY : Z;
GVoDISPLAY_0 : Z;
\end{verbatim}
A.8 THE Z THEORY OPS’spec

A.8.1 Parents

GVspec  TCspec  calc_prelims  cn

A.8.2 Children

OPSOPERATION_BUTTON’context  OPS’body
OPSODIGIT_BUTTON’context

cn_BINARY_thm
\[ BINARY = TCo OPERATION \setminus UNARY \]

cn_UNARY_thm
\[ UNARY = \{ TCo CHANGE\_SIGN, TCo FACTORIAL, TCo SQUARE\_ROOT \} \]

cn_DO_DIGIT_thm
\[ DO\_DIGIT = [D : TCo DIGIT; GVoDISPLAY : Z; GVoDISPLAY_0 : Z; GVoIN\_NUMBER : BOOLEAN; GVoIN\_NUMBER_0 : BOOLEAN | (GVoIN\_NUMBER_0 = TRUE \Rightarrow GVoDISPLAY = GVoDISPLAY_0 * TCo BASE + D) \wedge (GVoIN\_NUMBER_0 = FALSE \Rightarrow GVoDISPLAY = D) \wedge GVoIN\_NUMBER = TRUE] \]

cn_fact_sig_thm
\[ \text{fact} \in \mathbb{N} \rightarrow \mathbb{N} \]

cn_fact_thm
\[ \text{fact } 0 = 1 \wedge (\forall m : \mathbb{N} \bullet \text{fact } (m + 1) = (m + 1) * \text{fact } m) \]

natural_thm
\[ \forall m : \text{NATURAL} \bullet m \geq 0 \]

fact_thm
\[ \text{fact } 0 = 1 \wedge \text{fact } 1 = 1 \]
A.9 THE Z THEORY OPS’body

A.9.1 Parents

\[ \text{OPS}' \text{spec} \quad \text{GV}' \text{spec} \quad \text{TC}' \text{spec} \quad \text{cn} \]

A.9.2 Conjectures

\begin{itemize}
  \item \textbf{vcOPS}_1 \quad \text{true} \Rightarrow \text{true}
  \item \textbf{vcOPS}_2 \quad \forall \text{GVo}\_\text{NUMBER}, \text{GVo}\_\text{NUMBER}_0 : \text{BOOLEAN};
      \quad \text{D} : \text{TCoDIGIT};
      \quad \text{GVoDISPLAY}, \text{GVoDISPLAY}_0 : \text{TCoNUMBER}
      \quad | \text{true} \land \text{DO}_\text{DIGIT}
      \quad \bullet \text{DO}_\text{DIGIT}
  \item \textbf{vcOPS}_3 \quad \text{true} \Rightarrow \text{true}
  \item \textbf{vcOPS}_4 \quad \forall \text{GVo}\_\text{NUMBER} : \text{BOOLEAN};
      \quad \text{GVo}\_\text{ACCUMULATOR}, \text{GVo}\_\text{ACCUMULATOR}_0, \text{GVoDISPLAY},
      \quad \text{GVoDISPLAY}_0 : \text{TCoNUMBER};
      \quad \text{GVo}\_\text{LAST}\_\text{OP}, \text{GVo}\_\text{LAST}\_\text{OP}_0, \text{O} : \text{TCoOPERATION}
      \quad | \text{true} \land \text{DO}_\text{OPERATION}
      \quad \bullet \text{DO}_\text{OPERATION}
\end{itemize}

A.9.3 Theorems

\begin{itemize}
  \item \textbf{vcOPS}_2 \quad \vdash \forall \text{GVo}\_\text{NUMBER}, \text{GVo}\_\text{NUMBER}_0 : \text{BOOLEAN};
      \quad \text{D} : \text{TCoDIGIT};
      \quad \text{GVoDISPLAY}, \text{GVoDISPLAY}_0 : \text{TCoNUMBER}
      \quad | \text{true} \land \text{DO}_\text{DIGIT}
      \quad \bullet \text{DO}_\text{DIGIT}
\end{itemize}

A.10 THE Z THEORY OPSoDIGIT_.BUTTON’proc

A.10.1 Parents

\[ \text{OPSoDIGIT\_BUTTON}' \text{context} \]

A.10.2 Conjectures

\begin{itemize}
  \item \textbf{vcOPSoDIGIT\_BUTTON}_1 \quad \text{true} \Rightarrow \text{true}
  \item \textbf{vcOPSoDIGIT\_BUTTON}_2 \quad \forall \text{GVo}\_\text{NUMBER}, \text{GVo}\_\text{NUMBER}_0 : \text{BOOLEAN};
      \quad \text{D} : \text{TCoDIGIT};
      \quad \text{GVoDISPLAY}, \text{GVoDISPLAY}_0 : \text{TCoNUMBER}
      \quad | \text{true} \land \text{DO}_\text{DIGIT}
      \quad \bullet \text{DO}_\text{DIGIT}
  \item \textbf{vc3001}_1 \quad \forall \text{GVo}\_\text{NUMBER} : \text{BOOLEAN};
      \quad \text{D} : \text{TCoDIGIT};
      \quad \text{GVoDISPLAY} : \text{TCoNUMBER}
\end{itemize}
A.11. THE Z THEORY OPSoOPERATION_BUTTON’proc

A.11.1 Parents

OPSoOPERATION_BUTTON’context

A.11.2 Global Variables

FACT  Z ↔ Z
SQRT  Z ↔ Z

A.11.3 Axioms

FACT  \( \vdash \text{FACT} \in \text{NATURAL} \rightarrow \text{NATURAL} \)  
\& (\( \forall M : \text{NATURAL} \vdash \text{true} \Rightarrow \text{FACT} M = \text{fact} M \))

SQRT  \( \vdash \text{SQRT} \in \text{NATURAL} \rightarrow \text{NATURAL} \)  
\& (\( \forall M : \text{NATURAL} \)  
\& true  
\Rightarrow \text{SQRT} M ** 2 \leq M  
\land M < (\text{SQRT} M + 1) ** 2 )

Constraint 1  \( \vdash \text{TCoMAX\_NUMBER} \leq \text{INTEGERsLAST} \)
A.11.4 Conjectures

\texttt{vcOPS\text{\textunderscore}OPERATION\_BUTTON\_1}
\[\text{true} \Rightarrow \text{true}\]

\texttt{vcOPS\text{\textunderscore}OPERATION\_BUTTON\_2}
\[\forall \ G\text{Vo}_{\text{IN}\_\text{NUMBER}} : \text{BOOLEAN};
G\text{Vo}\text{ACC\text{\textunderscore}UMULATOR}, \ G\text{Vo}\text{ACC\text{\textunderscore}UMULATOR}_0, \ G\text{Vo}\text{DISPLAY},
G\text{Vo}\text{DISPLAY}_0 : \text{TCo\text{NUMBER}};
G\text{Vo}_{\text{LAST\textunderscore\text{OP}}}, \ G\text{Vo}_{\text{LAST\textunderscore\text{OP}}}_0, \ O : \text{TCo\text{OPERATION}}\]
\[\text{true} \land \text{DO\_\text{OPERATION}}\]
\[\bullet \ \text{DO\_\text{OPERATION}}\]

\texttt{vc3002\_1}
\[\forall \ G\text{Vo}\text{ACC\text{\textunderscore}UMULATOR}, \ G\text{Vo}\text{DISPLAY} : \text{TCo\text{NUMBER}};
G\text{Vo}_{\text{LAST\textunderscore\text{OP}}}, \ O : \text{TCo\text{OPERATION}}\]
\[\land \ O \ eq \ \text{TCo\text{CHANGE\_SIGN}} = \text{TRUE}\]
\[\bullet \ (G\text{Vo}\text{ACC\text{\textunderscore}UMULATOR} \equiv G\text{Vo}\text{ACC\text{\textunderscore}UMULATOR},
G\text{Vo}\text{ACC\text{\textunderscore}UMULATOR}_0 \equiv G\text{Vo}\text{ACC\text{\textunderscore}UMULATOR},
G\text{Vo}\text{DISPLAY} \equiv \sim G\text{Vo}\text{DISPLAY},
G\text{Vo}\text{DISPLAY}_0 \equiv G\text{Vo}\text{DISPLAY},
G\text{Vo}_{\text{IN}\_\text{NUMBER}} \equiv \text{FALSE}, \ G\text{Vo}_{\text{LAST\textunderscore\text{OP}}} \equiv G\text{Vo}_{\text{LAST\textunderscore\text{OP}}},
G\text{Vo}_{\text{LAST\textunderscore\text{OP}}}_0 \equiv G\text{Vo}_{\text{LAST\textunderscore\text{OP}}}, \ O \equiv O)\]
\[\in \text{DO\_\text{OPERATION}}\]

\texttt{vc3002\_2}
\[\forall \ G\text{Vo}\text{ACC\text{\textunderscore}UMULATOR}, \ G\text{Vo}\text{DISPLAY} : \text{TCo\text{NUMBER}};
G\text{Vo}_{\text{LAST\textunderscore\text{OP}}}, \ O : \text{TCo\text{OPERATION}}\]
\[\land \ O \ eq \ \text{TCo\text{CHANGE\_SIGN}} = \text{FALSE}\]
\[\land \ O \ eq \ \text{TCo\text{FACTORIAL}} = \text{TRUE}\]
\[\bullet \ (G\text{Vo}\text{ACC\text{\textunderscore}UMULATOR} \equiv G\text{Vo}\text{ACC\text{\textunderscore}UMULATOR},
G\text{Vo}\text{ACC\text{\textunderscore}UMULATOR}_0 \equiv G\text{Vo}\text{ACC\text{\textunderscore}UMULATOR},
G\text{Vo}\text{DISPLAY} \equiv \text{FACT} G\text{Vo}\text{DISPLAY},
G\text{Vo}\text{DISPLAY}_0 \equiv G\text{Vo}\text{DISPLAY},
G\text{Vo}_{\text{IN}\_\text{NUMBER}} \equiv \text{FALSE}, \ G\text{Vo}_{\text{LAST\textunderscore\text{OP}}} \equiv G\text{Vo}_{\text{LAST\textunderscore\text{OP}}},
G\text{Vo}_{\text{LAST\textunderscore\text{OP}}}_0 \equiv G\text{Vo}_{\text{LAST\textunderscore\text{OP}}}, \ O \equiv O)\]
\[\in \text{DO\_\text{OPERATION}}\]

\texttt{vc3002\_3}
\[\forall \ G\text{Vo}\text{ACC\text{\textunderscore}UMULATOR}, \ G\text{Vo}\text{DISPLAY} : \text{TCo\text{NUMBER}};
G\text{Vo}_{\text{LAST\textunderscore\text{OP}}}, \ O : \text{TCo\text{OPERATION}}\]
\[\land \ O \ eq \ \text{TCo\text{CHANGE\_SIGN}} = \text{FALSE}\]
\[\land \ O \ eq \ \text{TCo\text{FACTORIAL}} = \text{FALSE}\]
\[\land \ O \ eq \ \text{TCo\text{SQUARE\_ROOT}} = \text{TRUE}\]
\[\bullet \ (G\text{Vo}\text{ACC\text{\textunderscore}UMULATOR} \equiv G\text{Vo}\text{ACC\text{\textunderscore}UMULATOR},
G\text{Vo}\text{ACC\text{\textunderscore}UMULATOR}_0 \equiv G\text{Vo}\text{ACC\text{\textunderscore}UMULATOR},
G\text{Vo}\text{DISPLAY} \equiv \text{SQRT} G\text{Vo}\text{DISPLAY},
G\text{Vo}\text{DISPLAY}_0 \equiv G\text{Vo}\text{DISPLAY},
G\text{Vo}_{\text{IN}\_\text{NUMBER}} \equiv \text{FALSE}, \ G\text{Vo}_{\text{LAST\textunderscore\text{OP}}} \equiv G\text{Vo}_{\text{LAST\textunderscore\text{OP}}},
G\text{Vo}_{\text{LAST\textunderscore\text{OP}}}_0 \equiv G\text{Vo}_{\text{LAST\textunderscore\text{OP}}}, \ O \equiv O)\]
\[\in \text{DO\_\text{OPERATION}}\]

\texttt{vc3002\_4}
\[\forall \ G\text{Vo}\text{ACC\text{\textunderscore}UMULATOR}, \ G\text{Vo}\text{DISPLAY} : \text{TCo\text{NUMBER}};
G\text{Vo}_{\text{LAST\textunderscore\text{OP}}}, \ O : \text{TCo\text{OPERATION}}\]
\[\land \ O \ eq \ \text{TCo\text{CHANGE\_SIGN}} = \text{FALSE}\]
\[\land \ O \ eq \ \text{TCo\text{FACTORIAL}} = \text{FALSE}\]
\( \land O \ \text{eq} \ \text{TCoSQUARE\_ROOT} = \text{FALSE} \)
\( \land G\text{Vo}\text{LAST\_OP} \ \text{eq} \ \text{TCoEQUALS} = \text{TRUE} \)

\( \bullet \ (G\text{Vo}\text{ACCUMULATOR} \ \text{eq} \ G\text{Vo}\text{DISPLAY}, \)
\( G\text{Vo}\text{ACCUMULATOR} \_0 \ \text{eq} \ G\text{Vo}\text{ACCUMULATOR}, \)
\( G\text{Vo}\text{DISPLAY} \ \text{eq} \ G\text{Vo}\text{DISPLAY}, \)
\( G\text{Vo}\text{DISPLAY} \_0 \ \text{eq} \ G\text{Vo}\text{DISPLAY}, \)
\( G\text{Vo}\text{IN\_NUMBER} \ \text{eq} \ \text{FALSE}, G\text{Vo}\text{LAST\_OP} \ \text{eq} \ O, \)
\( G\text{Vo}\text{LAST\_OP} \_0 \ \text{eq} \ G\text{Vo}\text{LAST\_OP}, O \ \text{eq} \ O) \)
\( \in \text{DO\_OPERATION} \)

\text{vc3002\_5} \ \forall \ G\text{Vo}\text{ACCUMULATOR}, G\text{Vo}\text{DISPLAY} : \text{TCoNUMBER};
\( G\text{Vo}\text{LAST\_OP}, O : \text{TCoOPERATION} \)
\[ \text{true} \]
\( \land O \ \text{eq} \ \text{TCoCHANGE\_SIGN} = \text{FALSE} \)
\( \land O \ \text{eq} \ \text{TCoFACTORIAL} = \text{FALSE} \)
\( \land O \ \text{eq} \ \text{TCoSQUARE\_ROOT} = \text{FALSE} \)
\( \land G\text{Vo}\text{LAST\_OP} \ \text{eq} \ \text{TCoEQUALS} = \text{FALSE} \)
\( \land G\text{Vo}\text{LAST\_OP} \ \text{eq} \ \text{TCoPLUS} = \text{TRUE} \)

\( \bullet \ (G\text{Vo}\text{ACCUMULATOR} \ \text{eq} \ G\text{Vo}\text{ACCUMULATOR} + G\text{Vo}\text{DISPLAY}, \)
\( G\text{Vo}\text{ACCUMULATOR} \_0 \ \text{eq} \ G\text{Vo}\text{ACCUMULATOR}, \)
\( G\text{Vo}\text{DISPLAY} \ \text{eq} \ G\text{Vo}\text{ACCUMULATOR} + G\text{Vo}\text{DISPLAY}, \)
\( G\text{Vo}\text{DISPLAY} \_0 \ \text{eq} \ G\text{Vo}\text{DISPLAY}, \)
\( G\text{Vo}\text{IN\_NUMBER} \ \text{eq} \ \text{FALSE}, G\text{Vo}\text{LAST\_OP} \ \text{eq} \ O, \)
\( G\text{Vo}\text{LAST\_OP} \_0 \ \text{eq} \ G\text{Vo}\text{LAST\_OP}, O \ \text{eq} \ O) \)
\( \in \text{DO\_OPERATION} \)

\text{vc3002\_6} \ \forall \ G\text{Vo}\text{ACCUMULATOR}, G\text{Vo}\text{DISPLAY} : \text{TCoNUMBER};
\( G\text{Vo}\text{LAST\_OP}, O : \text{TCoOPERATION} \)
\[ \text{true} \]
\( \land O \ \text{eq} \ \text{TCoCHANGE\_SIGN} = \text{FALSE} \)
\( \land O \ \text{eq} \ \text{TCoFACTORIAL} = \text{FALSE} \)
\( \land O \ \text{eq} \ \text{TCoSQUARE\_ROOT} = \text{FALSE} \)
\( \land G\text{Vo}\text{LAST\_OP} \ \text{eq} \ \text{TCoEQUALS} = \text{FALSE} \)
\( \land G\text{Vo}\text{LAST\_OP} \ \text{eq} \ \text{TCoPLUS} = \text{FALSE} \)
\( \land G\text{Vo}\text{LAST\_OP} \ \text{eq} \ \text{TCoMINUS} = \text{TRUE} \)

\( \bullet \ (G\text{Vo}\text{ACCUMULATOR} \ \text{eq} \ G\text{Vo}\text{ACCUMULATOR} - G\text{Vo}\text{DISPLAY}, \)
\( G\text{Vo}\text{ACCUMULATOR} \_0 \ \text{eq} \ G\text{Vo}\text{ACCUMULATOR}, \)
\( G\text{Vo}\text{DISPLAY} \ \text{eq} \ G\text{Vo}\text{ACCUMULATOR} - G\text{Vo}\text{DISPLAY}, \)
\( G\text{Vo}\text{DISPLAY} \_0 \ \text{eq} \ G\text{Vo}\text{DISPLAY}, \)
\( G\text{Vo}\text{IN\_NUMBER} \ \text{eq} \ \text{FALSE}, G\text{Vo}\text{LAST\_OP} \ \text{eq} \ O, \)
\( G\text{Vo}\text{LAST\_OP} \_0 \ \text{eq} \ G\text{Vo}\text{LAST\_OP}, O \ \text{eq} \ O) \)
\( \in \text{DO\_OPERATION} \)

\text{vc3002\_7} \ \forall \ G\text{Vo}\text{ACCUMULATOR}, G\text{Vo}\text{DISPLAY} : \text{TCoNUMBER};
\( G\text{Vo}\text{LAST\_OP}, O : \text{TCoOPERATION} \)
\[ \text{true} \]
\( \land O \ \text{eq} \ \text{TCoCHANGE\_SIGN} = \text{FALSE} \)
\( \land O \ \text{eq} \ \text{TCoFACTORIAL} = \text{FALSE} \)
\( \land O \ \text{eq} \ \text{TCoSQUARE\_ROOT} = \text{FALSE} \)
\( \land G\text{Vo}\text{LAST\_OP} \ \text{eq} \ \text{TCoEQUALS} = \text{FALSE} \)
\( \land G\text{Vo}\text{LAST\_OP} \ \text{eq} \ \text{TCoPLUS} = \text{FALSE} \)
\( \land G\text{Vo}\text{LAST\_OP} \ \text{eq} \ \text{TCoMINUS} = \text{FALSE} \)
\( \land G\text{Vo}\text{LAST\_OP} \ \text{eq} \ \text{TCoTIMES} = \text{TRUE} \)

\( \bullet \ (G\text{Vo}\text{ACCUMULATOR} \ \text{eq} \ G\text{Vo}\text{ACCUMULATOR} \times G\text{Vo}\text{DISPLAY}, \)
\( \text{GVoACCUMULATOR}_0 \triangleq \text{GVoACCUMULATOR}, \)
\( \text{GVoDISPLAY} \triangleq \text{GVoACCUMULATOR} \ast \text{GVoDISPLAY}, \)
\( \text{GVoDISPLAY}_0 \triangleq \text{GVoDISPLAY}, \)
\( \text{GVoIN\_NUMBER} \triangleq \text{FALSE}, \text{GVoLAST\_OP} \triangleq \mathcal{O}, \)
\( \text{GVoLAST\_OP}_0 \triangleq \text{GVoLAST\_OP}, \mathcal{O} \triangleq \mathcal{O}) \)
\( \in \text{DO\_OPERATION} \)

\text{vc3002}_8 \quad \forall \ \text{GVoACCUMULATOR}, \text{GVoDISPLAY} : \text{TCoNUMBER}; \)
\( \text{GVoLAST\_OP}, \mathcal{O} : \text{TCoOPERATION} \)
\( \mid \text{true} \land \mathcal{O} \text{ eq TCoCHANGE\_SIGN} = \text{FALSE} \)
\( \land \mathcal{O} \text{ eq TCoFACTORIAL} = \text{FALSE} \)
\( \land \mathcal{O} \text{ eq TCoSQUARE\_ROOT} = \text{FALSE} \)
\( \land \text{GVoLAST\_OP} \text{ eq TCoEQUALS} = \text{FALSE} \)
\( \land \text{GVoLAST\_OP} \text{ eq TCoPLUS} = \text{FALSE} \)
\( \land \text{GVoLAST\_OP} \text{ eq TCoMINUS} = \text{FALSE} \)
\( \land \text{GVoLAST\_OP} \text{ eq TCoTIMES} = \text{FALSE} \)
\( \bullet (\text{GVoACCUMULATOR} \triangleq \text{GVoACCUMULATOR}, \)
\( \text{GVoACCUMULATOR}_0 \triangleq \text{GVoACCUMULATOR}, \)
\( \text{GVoDISPLAY} \triangleq \text{GVoACCUMULATOR}, \)
\( \text{GVoDISPLAY}_0 \triangleq \text{GVoDISPLAY}, \)
\( \text{GVoIN\_NUMBER} \triangleq \text{FALSE}, \text{GVoLAST\_OP} \triangleq \mathcal{O}, \)
\( \text{GVoLAST\_OP}_0 \triangleq \text{GVoLAST\_OP}, \mathcal{O} \triangleq \mathcal{O}) \)
\( \in \text{DO\_OPERATION} \)

\text{vc3002}_1 \quad \vdash \forall \ \text{GVoACCUMULATOR}, \text{GVoDISPLAY} : \text{TCoNUMBER}; \)
\( \text{GVoLAST\_OP}, \mathcal{O} : \text{TCoOPERATION} \)
\( \mid \text{true} \land \mathcal{O} \text{ eq TCoCHANGE\_SIGN} = \text{TRUE} \)
\( \bullet (\text{GVoACCUMULATOR} \triangleq \text{GVoACCUMULATOR}, \)
\( \text{GVoACCUMULATOR}_0 \triangleq \text{GVoACCUMULATOR}, \)
\( \text{GVoDISPLAY} \triangleq \sim \text{GVoDISPLAY}, \)
\( \text{GVoDISPLAY}_0 \triangleq \text{GVoDISPLAY}, \)
\( \text{GVoIN\_NUMBER} \triangleq \text{FALSE}, \)
\( \text{GVoLAST\_OP} \triangleq \text{GVoLAST\_OP}, \)
\( \text{GVoLAST\_OP}_0 \triangleq \text{GVoLAST\_OP}, \mathcal{O} \triangleq \mathcal{O}) \)
\( \in \text{DO\_OPERATION} \)

\text{vc3002}_2 \quad \vdash \forall \ \text{GVoACCUMULATOR}, \text{GVoDISPLAY} : \text{TCoNUMBER}; \)
\( \text{GVoLAST\_OP}, \mathcal{O} : \text{TCoOPERATION} \)
\( \mid \text{true} \land \mathcal{O} \text{ eq TCoCHANGE\_SIGN} = \text{FALSE} \)
\( \land \mathcal{O} \text{ eq TCoFACTORIAL} = \text{TRUE} \)
\( \bullet (\text{GVoACCUMULATOR} \triangleq \text{GVoACCUMULATOR}, \)
\( \text{GVoACCUMULATOR}_0 \triangleq \text{GVoACCUMULATOR}, \)
\( \text{GVoDISPLAY} \triangleq \text{FACT GVoDISPLAY}, \)
\( \text{GVoDISPLAY}_0 \triangleq \text{GVoDISPLAY}, \)
\( \text{GVoIN\_NUMBER} \triangleq \text{FALSE}, \)
\( \text{GVoLAST\_OP} \triangleq \text{GVoLAST\_OP}, \)
\( \text{GVoLAST\_OP}_0 \triangleq \text{GVoLAST\_OP}, \mathcal{O} \triangleq \mathcal{O}) \)
\( \in \text{DO\_OPERATION} \)

\text{vc3002}_4 \quad \vdash \forall \ \text{GVoACCUMULATOR}, \text{GVoDISPLAY} : \text{TCoNUMBER};
A.12. THE Z THEORY OPSoOPERATION

 BUTTONnFACT'func


\[ GVoLAST\_OP, O : TCoOPERATION \]
\[ \vdash true \]
\[ \land O eq TCoCHANGE\_SIGN = FALSE \]
\[ \land O eq TCoFACTORIAL = FALSE \]
\[ \land O eq TCoSQUARE\_ROOT = FALSE \]
\[ \land GVoLAST\_OP eq TCoEQUALS = TRUE \]
\[ \bullet (GVoACCUMULATOR \equiv GVoDISPLAY, \]
\[ GVoACCUMULATOR_0 \equiv GVoACCUMULATOR, \]
\[ GVoDISPLAY \equiv GVoDISPLAY, \]
\[ GVoDISPLAY_0 \equiv GVoDISPLAY, \]
\[ GVoIN\_NUMBER \equiv FALSE, GVoLAST\_OP \equiv O, \]
\[ GVoLAST\_OP_0 \equiv GVoLAST\_OP, O \equiv O) \]
\[ \in DO\_OPERATION \]

A.12 THE Z THEORY OPSoOPERATION BUTTONnFACT’func

A.12.1 Parents

OPSoOPERATION BUTTONnFACT’context

A.12.2 Conjectures

vcOPSoOPERATION_BUTTONnFACT_1
\[ \forall M : NATURAL \bullet M \geq 0 \land 1 = 1 \]

vcOPSoOPERATION_BUTTONnFACT_2
\[ \forall M, RESULT : NATURAL; FACT : NATURAL \rightarrow NATURAL \]
\[ \vdash true \land RESULT = fact M \land FACT M = RESULT \]
\[ \bullet FACT M = fact M \]

vc1001_1
\[ \forall M, RESULT : NATURAL \]
\[ \vdash (M \geq 0 \land RESULT = 1) \land 2 \leq M \]
\[ \bullet 2 \geq 1 \land RESULT = fact (2 - 1) \]

vc1001_2
\[ \forall M, RESULT : NATURAL \]
\[ \vdash (M \geq 0 \land RESULT = 1) \land 2 > M \]
\[ \bullet RESULT = fact M \]

vc1001_3
\[ \forall J : INTEGER; M, RESULT, RESULT_0 : NATURAL \]
\[ \vdash (M \geq 0 \]
\[ \land RESULT_0 = 1) \]
\[ \land J \in 2 .. M \]
\[ \land J \neq M \]
\[ \land RESULT = fact J \]
\[ \bullet J + 1 \geq 1 \land RESULT = fact (J + 1 - 1) \]

vc1001_4
\[ \forall M, RESULT, RESULT_0 : NATURAL \]
\[ \vdash (M \geq 0 \land RESULT_0 = 1) \land RESULT = fact M \]
\[ \bullet RESULT = fact M \]

vc1002_1
\[ \forall J : INTEGER; RESULT : NATURAL \]
\[ \vdash J \geq 1 \land RESULT = fact (J - 1) \]
\[ \bullet J \ast RESULT = fact J \]
A.12.3 Theorems

\textit{vcOPS\textunderscore BUTTONoFACT\textunderscore 1}
\[\vdash \forall M : \text{NATURAL} \quad \bullet M \geq 0 \wedge 1 = 1\]

\textit{vcOPS\textunderscore BUTTONoFACT\textunderscore 2}
\[\vdash \forall M, \text{RESULT} : \text{NATURAL}; \text{FACT} : \text{NATURAL} \rightarrow \text{NATURAL}\]
\[| \quad \text{true} \wedge \text{RESULT} = \text{fact} M \wedge \text{FACT} M = \text{RESULT} \]
\[\quad \bullet \text{FACT} M = \text{fact} M\]

\textit{vc1001\textunderscore 1}
\[\vdash \forall M, \text{RESULT} : \text{NATURAL}\]
\[| \quad (M \geq 0 \wedge \text{RESULT} = 1) \wedge 2 \leq M\]
\[\quad \bullet 2 \geq 1 \wedge \text{RESULT} = \text{fact} (2 - 1)\]

\textit{vc1001\textunderscore 2}
\[\vdash \forall M, \text{RESULT} : \text{NATURAL}\]
\[| \quad (M \geq 0 \wedge \text{RESULT} = 1) \wedge 2 > M\]
\[\quad \bullet \text{RESULT} = \text{fact} M\]

A.13 THE Z THEORY OPSoOPERATION\textunderscore BUTTONoSQRT'\textit{func}

A.13.1 Parents

\textit{OPSoOPERATION\textunderscore BUTTONoSQRT'context}

A.13.2 Conjectures

\textit{vcOPS\textunderscore BUTTONoSQRT\textunderscore 1}
\[\text{true} \Rightarrow 0 = 0\]

\textit{vcOPS\textunderscore BUTTONoSQRT\textunderscore 2}
\[\forall M, \text{RESULT} : \text{NATURAL}; \text{SQRT} : \text{NATURAL} \rightarrow \text{NATURAL}\]
\[| \quad \text{true}\]
\[\quad \wedge (\text{RESULT} ** 2 \leq M)\]
\[\quad \wedge M < (\text{RESULT} + 1) ** 2\]
\[\quad \wedge \text{SQRT} M = \text{RESULT}\]
\[\quad \bullet \text{SQRT} M ** 2 \leq M \wedge M < (\text{SQRT} M + 1) ** 2\]

\textit{vc2001\textunderscore 1}
\[\forall \text{RESULT} : \text{NATURAL}\]
\[| \quad \text{RESULT} = 0 \quad \bullet \text{RESULT} = 0\]

\textit{vc2001\textunderscore 2}
\[\forall M, \text{RESULT}, \text{RESULT}\_0 : \text{NATURAL}\]
\[| \quad \text{RESULT}\_0 = 0\]
\[\quad \wedge \text{RESULT} ** 2 \leq M\]
\[\quad \wedge M < (\text{RESULT} + 1) ** 2\]
\[\quad \bullet \text{RESULT} ** 2 \leq M \wedge M < (\text{RESULT} + 1) ** 2\]

\textit{vc2002\textunderscore 1}
\[\forall M, \text{RESULT} : \text{NATURAL}\]
\[| \quad \text{RESULT} = 0\]
\[\quad \bullet \text{RESULT} ** 2 \leq M \wedge M < (M + 1) ** 2\]

\textit{vc2002\textunderscore 2}
\[\forall \text{HI} : \text{INTEGER}; M, \text{RESULT}, \text{RESULT}\_0 : \text{NATURAL}\]
\[| \quad \text{RESULT}\_0 = 0 \wedge \text{RESULT} ** 2 \leq M \wedge M < \text{HI} ** 2\]
\[\quad \bullet \text{RESULT} ** 2 \leq M \wedge M < \text{HI} ** 2\]

\textit{vc2002\textunderscore 3}
\[\forall M, \text{RESULT}, \text{RESULT}\_0 : \text{NATURAL}\]
\[| \quad \text{RESULT}\_0 = 0\]
\[\quad \wedge \text{RESULT} ** 2 \leq M\]
\[\quad \wedge M < (\text{RESULT} + 1) ** 2\]
\[\quad \bullet \text{RESULT} ** 2 \leq M \wedge M < (\text{RESULT} + 1) ** 2\]

\textit{vc2003\textunderscore 1}
\[\forall \text{HI} : \text{INTEGER}; M, \text{RESULT} : \text{NATURAL}\]
\[ (\text{RESULT} \times 2 \leq M \land M < \text{HI} \times 2) \land \text{RESULT} + 1 \text{ eq HI} = \text{TRUE} \]

\[ \text{RESULT} \times 2 \leq M \land M < (\text{RESULT} + 1) \times 2 \]

∀ HI : INTEGER; M, RESULT : NATURAL
\[ (\text{RESULT} \times 2 \leq M \land M < \text{HI} \times 2) \land \text{RESULT} + 1 \text{ eq HI} = \text{FALSE} \]

∀ HI, HI₀ : INTEGER; M, RESULT, RESULT₀ : NATURAL
\[ (\text{RESULT₀} \times 2 \leq M \land M < \text{HI₀} \times 2) \land \text{RESULT} \times 2 \leq M \land M < \text{HI} \times 2 \]

∀ HI : INTEGER; M, RESULT : NATURAL
\[ (\text{RESULT} \times 2 \leq M \land M < \text{HI} \times 2) \land ((\text{RESULT} + \text{HI} + 1) \text{ intdiv 2}) \times 2 \text{ greater } M = \text{TRUE} \]

∀ HI, HI₀ : INTEGER; M, RESULT, RESULT₀ : NATURAL
\[ (\text{RESULT₀} \times 2 \leq M \land M < \text{HI₀} \times 2) \land ((\text{RESULT} + \text{HI} + 1) \text{ intdiv 2}) \times 2 \text{ greater } M = \text{FALSE} \]

∀ HI : INTEGER; M, RESULT : NATURAL
\[ (\text{RESULT} \times 2 \leq M \land M < \text{HI} \times 2) \land ((\text{RESULT} + \text{HI} + 1) \text{ intdiv 2}) \times 2 \leq M \land M < \text{HI} \times 2 \]

A.13.3 Theorems

∀ HI : INTEGER; M, RESULT, RESULT₀ : NATURAL
\[ \text{RESULT₀} = 0 \land \text{RESULT} \times 2 \leq M \land M < \text{HI} \times 2 \]

\[ \forall x : \mathbb{Z} \bullet x \times 2 = x \times x \]

\[ \forall M, \text{RESULT} : \text{NATURAL} \]
\[ \text{RESULT} = 0 \]
\[ \text{RESULT} \times 2 \leq M \land M < (M + 1) \times 2 \]
package TC
is
  BASE : constant INTEGER := 10;
  PRECISION : constant INTEGER := 6;
  MAX_NUMBER : constant INTEGER := BASE ** PRECISION − 1;
  MIN_NUMBER : constant INTEGER := − MAX_NUMBER;
  subtype DIGIT is INTEGER range 0..BASE − 1;
  subtype NUMBER is INTEGER range MIN_NUMBER..MAX_NUMBER;
  type OPERATION is (PLUS, MINUS, TIMES, CHANGE_SIGN, SQUARE_ROOT, FACTORIAL, EQUALS);
end TC;

with TC;
package GV
is
  DISPLAY, ACCUMULATOR : TC.NUMBER;
  LAST_OP : TC.OPERATION;
  IN_NUMBER : BOOLEAN;
end GV;

with TC, GV;
package OPS
is
  procedure DIGIT_BUTTON (D : in TC.DIGIT);
  −− Spec ...
  procedure OPERATION_BUTTON (O : in TC.OPERATION);
  −− Spec ...
end OPS;
package body OPS
is
procedure DIGIT_BUTTON (D : in TC.DIGIT)
    -- Spec ...
    is
    begin
    if GV.IN_NUMBER
    then
        GV.DISPLAY := GV.DISPLAY * TC.BASE + D;
    else
        GV.DISPLAY := D;
    end if;
    GV.IN_NUMBER := true;
end DIGIT_BUTTON;
procedure OPERATION_BUTTON (O : in TC.OPERATION)
    -- Spec ...
    is
    function FACT (M : NATURAL) return NATURAL
        -- Spec ...
        is
        RESULT : NATURAL;
        begin
        RESULT := 1;
        for J in INTEGER range 2..M
        loop
            RESULT := J * RESULT;
        end loop;
        return RESULT;
    end FACT;
function SQRT (M : NATURAL) return NATURAL

-- Spec ...

is
RESULT : NATURAL;
MID, HI : INTEGER;

begin
RESULT := 0;
HI := M + 1;
-- \$TILL ...
loop
exit when RESULT + 1 = HI;
MID := (RESULT + HI + 1) / 2;
if MID ** 2 > M
then
HI := MID;
else
RESULT := MID;
end if;
end loop;
return RESULT;
end SQRT;

begin
if O = TC.CHANGE_SIGN
then
GV.DISPLAY := - GV.DISPLAY;
elsif O = TC.FACTORIAL
then
GV.DISPLAY := FACT (GV.DISPLAY);
elsif O = TC.SQUARE_ROOT
then
GV.DISPLAY := SQRT (GV.DISPLAY);
else
if GV.LAST_OP = TC.EQUALS
then
GV.ACCUMULATOR := GV.DISPLAY;
elsif GV.LAST_OP = TC.PLUS
then
GV.ACCUMULATOR := GV.ACCUMULATOR + GV.DISPLAY;
elsif GV.LAST_OP = TC.MINUS
then
GV.ACCUMULATOR := GV.ACCUMULATOR - GV.DISPLAY;
elsif GV.LAST_OP = TC.TIMES
then
GV.ACCUMULATOR := GV.ACCUMULATOR * GV.DISPLAY;
end if;

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GV.DISPLAY := GV.ACCUMULATOR;
GV.LAST_OP := O;
end if;
GV.IN_NUMBER := false;
end OPERATION_BUTTON;
end OPS;
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