

ProofPower

Compliance Tool — User Guide

PPTex-2.9.1w2.rda.110727

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<http://www.lemma-one.demon.co.uk/ProofPower/index.html>

This document is published by:

Lemma 1 Ltd.
2nd Floor
31A Chain Street
Reading
Berkshire
UK
RG1 2HX
e-mail: pp@lemma-one.com

CONTENTS

0	ABOUT THIS PUBLICATION	5
0.1	Purpose	5
0.2	Readership	5
0.3	Related Publications	5
0.4	Area Covered	5
0.5	Prerequisites	6
0.6	Acknowledgements	6
1	INTRODUCTION TO THE COMPLIANCE TOOL	7
1.1	The Compliance Notation	7
1.2	The Compliance Tool	7
2	GETTING STARTED	9
2.1	ProofPower Databases	9
2.2	Batch Working	9
2.3	Interactive Working	10
2.3.1	Starting <code>xpp</code>	10
2.3.2	Window Layout	10
3	LITERATE SCRIPT CONVENTIONS	13
3.1	Initialisation	13
3.2	Theory Hierarchy	14
3.3	Output Commands	15
4	COMPLIANCE TOOL FUNCTIONS	17
4.1	Loading Scripts	17
4.2	Generating the Ada Program	17
4.3	Generating and Reloading the Z document	18
4.4	Inspecting the VCs	19
4.5	Editing and Checking a Script	21
4.6	Accessing and Proving VCs	21
4.7	Printing and Previewing Scripts	23
5	EXAMPLE SCRIPT	25
5.1	The Literate Script	25
5.1.1	Initialisation Commands	25
5.1.2	The Compliance Argument	25
5.1.3	Output Commands	26
5.2	The Z Document	27
5.3	The Ada Program	29
5.4	The Proofs	30
5.5	The Theory Listing	32
5.5.1	Parents	32

5.5.2	Conjectures	32
5.5.3	Theorems	33
6	EVALUATION GUIDELINES	35
6.1	Introduction	35
6.2	Scope of a Compliance Argument	35
6.3	Conformance with the Ada Standard	35
6.4	Formal Development Steps	35
6.5	Informal Development Steps	36
6.6	Consistency	36
6.7	Checking for Errors	37
6.8	Treatment of Real Types	37
7	ProofPower-ML COMPLIANCE TOOL REFERENCE	39
7.1	Controlling the Tool	39
7.2	Custom Proof Facilities	46
7.3	THE Z THEORY cn	57
7.3.1	Parents	57
7.3.2	Global Variables	57
7.3.3	Fixity	62
7.3.4	Axioms	63
7.3.5	Definitions	72
7.3.6	Theorems	73
	REFERENCES	77
	INDEX	79

List of Figures

2.1	An Example Compliance Tool Session	12
4.1	Inspecting VCs with <code>xpp</code>	20
4.2	Compliance Notation Templates Tool	22

ABOUT THIS PUBLICATION

0.1 Purpose

This document describes the use of the Compliance Tool supplied with ProofPower.

0.2 Readership

This document is intended to be read by users of the Compliance Tool. It contains both introductory material for the new user and reference material for more experienced users.

0.3 Related Publications

A bibliography is given on page 77 of this document.

- Installation of the Compliance Tool is described in:
Compliance Tool — Installation and Operation [13]
- Advice on proving verification conditions generated by the tool is given in:
Compliance Tool — Proving VCs [14]
- The syntax and semantics of the Compliance Notation as supported by the Compliance Tool is described in:
Compliance Notation — Language Description [15]
- An overview of the of the Compliance notation can be found in the DRA document:
A commentary on the Specification of the Compliance Notation for SPARK and Z [6].
- A description of ProofPower may be found in:
ProofPower Description Manual [8],
which also contains a full list of other ProofPower documentation.

0.4 Area Covered

Once the Compliance Tool is installed on the user's workstation, by following the procedure described in *Compliance Tool Installation and Operation* [13], this User Guide should enable the user to undertake the following tasks:

1. Loading a Compliance Notation script into the tool;

2. Generation of the Ada program from a script;
3. Generation of the Z document from a script and reloading the Z document into ProofPower;
4. Preparation and checking of new scripts;
5. Using ProofPower facilities to work with VCs, e.g., to begin an attempt to prove a VC;
6. Evaluation of the rigour of a compliance argument by identifying the informal and formal parts of the argument and recognising potentially anomalous ways of using the facilities of the tool.

0.5 Prerequisites

This User Guide is *not* intended to be an introduction to the Z language, or to the Compliance Notation, or to the use of ProofPower to prepare the Z parts of a script.

Familiarity with the Compliance Notation and some familiarity with ProofPower are very desirable, although not essential for simple use of the tool to process existing scripts. Some familiarity with the use of ProofPower for developing Z specifications is required to use the tool to develop new scripts.

The Compliance Notation is described in *Compliance Notation — Language Description* [15]. There is also a formally specification of the notation in the DRA technical report [5]. The ProofPower-Z tutorial, [9], gives an introduction to the use of ProofPower for specification and proof in Z.

The ProofPower user documentation is supplied as part of the ProofPower release included with the Compliance Tool and is available for on-line reference.

The user interface to the tool described in this document is a Motif application running under the X Windows System. Users who are unfamiliar with X or Motif should consult local or supplier's documentation or expertise for further guidance (e.g. see [17]).

0.6 Acknowledgements

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INTRODUCTION TO THE COMPLIANCE TOOL

1.1 The Compliance Notation

The Compliance Tool supports a notation for demonstrating the compliance of Ada programs with Z specifications. The notation is described in detail in *Compliance Notation — Language Description* [15]. The Ada parts of the notation belong to the Compliance Notation subset of Ada and will be referred to as Compliance Notation Ada in the rest of this document. The Compliance Notation was designed by the Defence Research Agency, Malvern and the ideas behind its design are discussed in the DRA document [6].

Compliance Notation is prepared and presented in literate scripts containing a mixture of narrative text, Z and Compliance Notation Ada. Special constructs are provided for two main purposes:

1. to allow the Ada program to be presented and documented in an order independent of that prescribed by the Ada syntax (in a similar fashion to Knuth's Web system [2]);
2. to allow the behaviour of the Ada code to be formally specified in Z (using a style based on Morgan's refinement calculus [3]).

These constructs allow the user to assert formal connections between the Z parts and the Ada parts in a Compliance Notation script and permit the compliance of the Ada program against its Z specifications to be reduced to a set of conjectures known as verification conditions, whose truth guarantees compliance. The VC generation algorithm is formally specified in the DRA technical report [5] and explained by example in *Compliance Notation — Language Description* [15]. The algorithm maps a Compliance Notation script to a Z document containing the VCs formulated with Z conjectures together with supporting Z definitions.

1.2 The Compliance Tool

The Compliance Tool described in this document supports the use of the Compliance Notation. It performs the following principal functions:

1. Checking the syntax of a Compliance Notation script;
2. Generating the Z document including VCs from a script;
3. Extracting the Ada program from a script.

The Compliance Tool is implemented as an application of the **ProofPower** specification and proof tool. The tool provides all the facilities offered by **ProofPower** for developing specifications and proofs in HOL and Z and for preparing high quality printed output via the \LaTeX system. The tool

includes custom facilities for working with Compliance Notation including, in some installations an interactive tool called the VC browser and some customised proof procedures.

The use of the tool in the development of a compliance argument will involve several stages, typically including the following:

1. Initial preparation of the literate scripts using the editing and interactive checking facilities of the tool. During this stage, the Z documents and Ada program are produced and inspected as required to assist in the development.
2. Batch processing of the complete literate scripts. If the conventions suggested in this User Guide are followed, the Z documents and Ada program are produced automatically. The tool can be used interactively if required, e.g., to help diagnose errors.
3. Further analysis of the Z documents. Depending on circumstances and on the level of formality required, this might involve either or both of: *(a)*, inspection of the documents, on paper or using a viewer; *(b)*, use of the VC browser supplied with the tool; and, *(c)*, machine-checked proof of some or all of the VCs.

The Compliance Tool offers a range of facilities to help in all of these activities.

GETTING STARTED

The task of using of the Compliance Tool to process a script is very similar to that of using **ProofPower** to process a *Z* specification. Indeed, the *Z* paragraphs in a Compliance Notation script are handled using the usual **ProofPower** mechanisms and the constructs peculiar to the Compliance Notation are treated in a very similar way.

Simple use of the tool to process existing scripts does not require any great familiarity with **ProofPower**. Using the tool to create new scripts or edit existing ones requires some familiarity with the preparation of *Z* specifications using **ProofPower** (as described in [10]). The rest of this chapter explains how to make a start with the tool for either of these levels of use. Section 2.1 describes set-up procedures which are common to batch and interactive working; section 2.2 describes how to use the tool to process existing scripts using UNIX commands; section 2.3 explains how to start an interactive session with the tool and gives an overview of its main graphical features.

2.1 ProofPower Databases

As always with a **ProofPower** application, to use the Compliance Tool you must first pick the **ProofPower** database with which you wish to work. A **ProofPower** database is a file which holds code and data recording the results of work with **ProofPower**. These databases are organised in a hierarchy. To run the Compliance Tool, you use a database which is a descendant in this hierarchy of the database `pp_daz`. This database, held in a file called `sun4bin/sun4pp_daz.db` in the **ProofPower** installation directory, contains the code and data required to process Compliance Notation. If you are starting from scratch, you would create a suitable database, say called `mydatabase`, using a UNIX command line such as:

```
pp_make_database -p daz mydatabase
```

This creates an empty database, which you will generally refer to as ‘`mydatabase`’ when using the system. The database is held in a file called ‘`sun4mydatabase.db`’ and this is the name you would use to carry out UNIX file operations on the database, e.g., to delete it with the UNIX command ‘`rm`’.

2.2 Batch Working

Providing the conventions of chapter 3 have been followed in the development of the scripts, one or more literate scripts may be processed using the programs `docsm1` and `pp` to produce the *Z* documents and Ada program. For example, assuming a database `mydatabase` has been set up as described above, the following UNIX commands will process the script in file `wrk501.doc`.

```
docsm1 wrk501
pp -f wrk501 -d mydatabase >wrk501.run.log
```

Here the first command produces a file called `wrk501.sml`, containing the formal material from `wrk501.doc` without the narrative text. The second command causes the Compliance Tool proper to process `wrk501.sml`, updating the database `mydatabase` and producing the Z document and Ada program for the script. If the conventions of chapter 3 have been followed in the development of the script, the Z document will be in a file called `wrk501.zdoc` and the Ada program will be in `wrk501.ada`. The log produced as the standard output from `pp` has been directed to a file `wrk501.run.log`, since it is likely to contain non-ASCII characters.

In a compliance argument involving more than one script, the `docsm1-pp` command sequence above is entered in turn for each script. Note that the order of the scripts is important, and providers of compliance arguments must indicate the required order (e.g., by supplying a make file or a shell script containing the necessary sequence of commands).

The Ada program is an Ada source file which may be examined with any text editor. The Z document contains non-ASCII characters and is best examined either using the `xpp` editor or using the program `docpr` to print it. The log file is best examined in the same way. The following UNIX command will print the Z document in our example:

```
docpr wrk501.zdoc
```

Basic use of the `xpp` editor is described in section 2.3.1. To use it to examine the Z document in this example, one would use the UNIX command:

```
xpp -file wrk501.zdoc
```

2.3 Interactive Working

2.3.1 Starting xpp

To start an interactive session with the Compliance Tool, you use the UNIX command `xpp`. `xpp` comprises a custom editor for working on your scripts together with a command interface for executing ProofPower commands and for loading Z and Compliance Notation constructs into the database.

The `xpp` command has options to identify the file containing the script you want edit and to give the ProofPower command options (including the database to use). To begin work on the script ‘`wrk501.doc`’ with the database ‘`mydatabase`’, you might use the UNIX command line:

```
xpp -file wrk501.doc -command pp -d mydatabase
```

This will create a Motif window similar to the one shown in figure 2.1. This is referred to as the `xpp` Main Window. If you omit the `-command` option, then you will get a so-called edit-only session, in which the lower part of the window will be missing.

2.3.2 Window Layout

This section describes briefly the main elements of the `xpp` Main Window. Further information may be obtained either from the tool’s help system or from the ProofPower documentation.

In overview, the top part of the window acts as an editor for literate scripts. Material from the editor can be transferred into the ProofPower-ML system for processing (e.g. to type check a specification paragraph or initiate a proof step). The journal of the transactions with ProofPower occupies the bottom part of the window. Figure 2.1 also shows a popup window (in this case a palette of special symbols). The size and position of all the windows can be adjusted using standard Motif techniques.

Sections 2.3.2.1 to 2.3.2.4 below describe the principal features of the `xpp` Main Window.

2.3.2.1 Menu Bar

The menu bar is at the top of the window shown in figure 2.1. The menus are all pulldown menus operated in the usual Motif fashion. The functions they perform are briefly described below.

File Menu This menu is for common file operations including: loading and saving the literate script; creating a new literate script; deleting a file.

Tools Menu This menu is used to create popup windows to perform various tasks. An example is the palette window shown in figure 2.1.

Edit Menu This menu provides script editing operations: ‘Cut’, ‘Copy’, ‘Paste’ and ‘Undo’.

Command Menu This menu is primarily used to cause text from the script window to be executed by the ProofPower-ML system. It also provides various control functions for the ProofPower-ML system.

Help Menu This menu provides help on various topics. Its ‘Tutorial’ item may be consulted for more information on basic operation of `xpp`.

2.3.2.2 File Name Bar

This displays the name of the file containing the script which is being edited in the Script Window.

2.3.2.3 Script Window

The Script Window is the upper large Motif text window in the `xpp` Main Window. It provides a general purpose editor for viewing and modifying scripts. In figure 2.1 on page 12, a Compliance Notation script containing definitions of a Z global variable, ‘*primed*’, and an Ada procedure, ‘*primes*’, is being displayed in the Script Window.

Part of the Script Window in figure 2.1 is hidden behind a window containing a ‘palette’ of symbols. This palette has been selected from the Tools Menu and can be used to enter the symbols into the literate script. Pushing one of the buttons on the palette causes the symbol to appear in the document as if it had been typed at the keyboard.

Note that the Z and Ada constructs in the Script Window are delimited using special mark-up sequences: ‘ $\textcircled{S}Z$ ’, ‘ $\textcircled{S}CN$ ’ and ‘ \blacksquare ’. A tool called the Templates Tool is available from the Tools Menu allowing easy entry of these mark-up sequences.

2.3.2.4 Journal Window

The Journal Window is the lower large Motif text window in the `xpp` Main Window. When ProofPower commands are executed the resulting responses from the ProofPower system are displayed

in the Journal Window. The Journal Window is a read-only text window — you cannot alter its contents by typing into it.

In 2.1, the user has just issued a command to process the procedure *primes*. This is done by selecting the procedure and its delimiting mark-up sequences in the Script Window and then using the ‘Execute’ item in the Command Menu. The absence of error messages in the Journal Window and the prompt (‘:>’) indicate that the procedure has been processed and accepted.

Figure 2.1: An Example Compliance Tool Session

LITERATE SCRIPT CONVENTIONS

Literate scripts are held in UNIX files, each file holding one or more scripts. Typically, each script contains exactly one compilation unit (such as a package specification or package body) together with any Z definitions needed to support the specification of that compilation unit. A script need not contain any Ada code at all, in which case the script just provides Z definitions for use in other scripts. A script may not contain more than one compilation unit.

Processing a script has three main goals: production of a Z document; production of the Ada program (i.e., the source code contained in the script); and modifications to the state of a database maintained by the tool and used in processing subsequent scripts. The literate scripts making up a complete compliance argument must be presented to the tool in some order, compatible with any dependencies between the scripts.

In addition to narrative text, Z paragraphs and Compliance Notation clauses, each script contains commands which direct the tool, e.g., to tell it to output the Ada program to a particular file. In this chapter, we recommend some conventions for these commands which are intended to make the script easy to process both interactively and in batch. A complete example of a script following these conventions is given in chapter 5.

The conventions described below are not enforced by the tool, and some users may wish to adapt them to their own needs. However, other sections of this user guide are written on the assumption that these conventions have been followed (e.g., section 2.2). The conventions are designed to make compliance arguments easy to assess. If you need to adopt different conventions, please refer to section 6 for further guidance on assessing a compliance argument.

3.1 Initialisation

The formal material in each literate script should be preceded by an initialisation section giving a name to the script in which it is to be processed. This is done with a **ProofPower-ML** command, *new_script*. Execution of *new_script* introduces a new **ProofPower** theory whose name is the name of the script and prepares the Compliance Tool to accept an Ada compilation unit. For example:

```
| new_script{name = "Utils", unit_type = "spec"};
```

Here the name is the name of the compilation unit and *unit_type* indicates its type, which must be one of "spec" (package specification), "body" (package body), "proc" (procedure) or "func" (function).

Dependencies on **ProofPower** theories that come from Ada constructs such as context clauses are handled automatically by the tool. However, in some circumstances, you may need to inform the tool that a script depends on one or more theories in the initialisation for the script. This might happen if you have an existing library of Z theories containing definitions that you want to use in the script. A variant of *new_script*, called *new_script1* allows you to give a list of *library theories* that become parents of the script theory. For example, if *script3* depends on the Z paragraphs stored in *mytheory1* and *mytheory2*, then you can use the command:

```

|   new_script1 {name = "Utils", unit_type = "spec",
|               library_theories=["mytheory1", "mytheory2"]};

```

In some circumstances, you may want to write Z paragraphs referring to the types, constants or functions in a package, P say, before giving the Ada compilation unit containing the context clause identifying P as a dependency. In this case, you can identify the package specification theory, P_{spec} , as a library theory in the script initialisation.

3.2 Theory Hierarchy

For each compilation unit, the corresponding *new_script* (or *new_script1* command introduces a **ProofPower** theory. For example, associated with a package body `utils` there will be a theory called *UTILS'body*.

For each each subprogram body inside a package or subprogram body, and for each package or subprogram stub, the Compliance Tool automatically creates a **ProofPower** theory whose name is derived from the expanded name of the package or subprogram. These theories are referred to as subprogram or stub theories.

A subprogram or stub theory is created with an automatically generated parent theory which is a duplicate of the theory associated with the enclosing body as it was at the point where the subprogram body or stub was processed (but with any VCs removed). This parent theory is called the context theory for the subprogram or stub. For example, if a package body *Utils* contains a procedure *sort*, a context theory called *UTILSoSORT'context* is created as a duplicate of the the script theory *UTILS'body*

The subprogram theories are used to hold the Z paragraphs representing the types and constants defined in the declarative part of the subprogram body and to hold VCs associated with the statement part. The Compliance Tool requires you to work in the appropriate theory when processing Compliance Notation clauses. The command *open_scope*, which takes as its argument the expanded name of the subprogram or package, is used to do this. For example, to refine a specification statement in the body of the procedure *sort* in the example mentioned above, you would need to make the following call:

```

|   open_scope "Utils.sort";

```

Stub theories are used to provide a context for processing the corresponding subunit. When a subunit is processed, the stub theory is automatically made a parent of the script theory.

For example, if the package `utils` contains the body of a procedure called `sort`, the tool will create a subprogram theory called *UTILSoSORT'proc*. If the package `utils` contains a stub for a package called `strings`, the tool will create a stub theory called *UTILSoSTRINGS'stub*. The list of all the theories associated with a script can be found using the function *get_script_theories*. For example:

```

|   get_script_theories "UTILS'body";

```

might result in the following output:

Compliance Tool Output

```

| val it = ["UTILS'body", "UTILSoSORT'proc", "UTILSoSTRINGS'stub"] : string list

```


3.3 Output Commands

The formal material in a literate script should be followed by directions for the production of the Z document and of the Ada program. This should be as in the following example:

```
| output_z_document{script="UTILS'spec", out_file="utils_ads.zdoc"};  
| output_ada_program{script="UTILS'spec", out_file="utils_ads.ada"};
```

Here *UTILS'*spec** is the script name, and conventional file name suffixes have been added to it for the Z document and the Ada program.

COMPLIANCE TOOL FUNCTIONS

Sections 4.1 to 4.7 below describe basic use of the main functions of the Compliance Tool.

You invoke many of these functions by executing **ProofPower-ML** commands. This may conveniently be done interactively using the Command Line Tool which can be started using the Tools Menu. An instance of the Command Line Tool may be seen in figure 4.1 on page 20. This tool allows single line **ProofPower-ML** commands to be entered and executed without changing the script which is being edited. The tool also has a scrollable list which you can use as a memory for common commands.

Some functions are provided as UNIX commands or as a combination of UNIX and **ProofPower-ML** commands.

4.1 Loading Scripts

To load a script, the UNIX command `docsm1` must first be used to extract the formal material. For example, the UNIX command:

```
docsm1 wrk501
```

copies the formal material from the file `wrk501.doc` into a file called `wrk501.sml`.

The `.sml` file produced by `docsm1` may then be loaded using the **ProofPower-ML** command:

```
| use_file "wrk501";
```

By default, the tool will raise an exception and stop processing a script if it detects an error. However, while preparing a compliance argument for a large program, it can be convenient to have the tool continue processing when errors are detected. This can be controlled using the flag `cn-stop-on-exceptions`. The tool maintains a record of the errors that have been detected which can be manipulated using the **ProofPower-ML** commands, `print_exception_log`, `output_exception_log` and `delete_exception_log`. See section 7 for more information.

4.2 Generating the Ada Program

The Ada program is output to a file using the **ProofPower-ML** command `output_ada_program`. This command is normally included at the end of each literate script as described in section 3.3 and may also be called at any time during the development of the script.

A function `print_ada_program` is also provided for displaying the compilation units of the Ada program on the standard output. It takes a string parameter giving the name of the script in which the compilation unit was introduced. A minus sign may be used as a short-hand for the name of the current script. For example, in an interactive session with `xpp`, the **ProofPower-ML** command:

```
| print_ada_program "-"
```

will cause the Ada program for the current script to be displayed in the journal window.

Some errors, in particular illegal redeclaration of names, are not detected until the Ada program is generated, so generation of the Ada program is an important part of processing a script. Even if these errors are detected, the Ada program is displayed or output to the file before the errors are reported. This allows a script containing informally developed code in which redeclaration of names is required to be processed by the tool (some redeclarations are legal Ada, but not legal Compliance Notation Ada).

When loading scripts, detection of an error will normally cause the tool to raise an exception and stop processing. You can override this behaviour selectively by using a ProofPower-ML exception handler. For example, the following ProofPower-ML command will output the Ada program to the file `SCRIPT.ada` and continue without raising an exception even if the program contains illegal redeclarations:

```
|      output_ada_program{script="-"=, out_file="SCRIPT.ada"}
|      handle Fail - => ();
```

The subset of Ada accepted by the Compliance Tool includes a number of features, e.g., use clauses, that are not supported in the SPARK subset of Ada (see [1] for the definition of the SPARK subset of Ada). You may ask for Ada comments highlighting constructs that do not conform to the SPARK syntax to be inserted in the Ada program using the flag `cn_spark_syntax_warnings`. See section 7 for more information.

4.3 Generating and Reloading the Z document

The Z document is output to a file using the ProofPower-ML command `output_z_document`. This command is normally called at the end of each literate script as described in section 3.3 and may also be called at any time during the development of the script.

A function `print_z_document` is also provided for displaying the Z document on the standard output. For example, in an interactive session with `xpp`, the ProofPower-ML command:

```
|      print_z_document "SCRIPT";
```

will cause the Z document for the script called `SCRIPT` to be displayed in the journal window.

In many situations, there is no actual need to reload the Z document, since it is automatically loaded as a side-effect of processing the literate script. Thus work on proof of VCs, for example, can begin immediately after the script is processed. The Z document can be reloaded into the tool, if required, simply by treating it in the same way as a `.sml` file produced by running `docsm1` on an ordinary Z specification. Thus one can use either a ProofPower-ML command such as:

```
|      use_file"SCRIPT.zdoc";
```

or a UNIX command such as:

```
pp -f SCRIPT.zdoc -d database
```

to load in the Z document. The following points should be noted in connection with reloading Z documents produced by the Compliance Tool:

1. the Z document requires the presence of the theory `cn`, which is not provided as standard in ProofPower-Z databases so that `database` in the example above should refer to a Compliance Tool database;

2. loading the Z document attempts to create **ProofPower** theories which will already exist in a database in which the literate script giving rise to the Z document has already been processed (so that these theories must be deleted, or a fresh Compliance Tool database created, if the loading is to succeed);
3. loading the Z document does not recreate the information about the Ada program which is computed and stored when the literate script is processed (so that facilities such as *print_ada_program* which require this information will no longer be useful).

4.4 Inspecting the VCs

Figure 4.1 on page 20 gives another example of an **xpp** Main Window. In this example, the user has used the Command Line Tool to issue commands to load an entire script into the tool and then display the Z document in the Journal Window. The Search-and-Replace Tool has been used to carry out a textual search in the Script Window for the refinement step which introduced the VC at the end of the Z document.

Using an editor to correlate the script with the VCs can be a useful technique but is often time consuming. The VC Browser is an interactive tool for examining the VCs and relating them to the Compliance Notation clauses which produced them. The VC Browser is provided in the **ProofPower** database **xdaz** on implementations of the Compliance Tool built with the Poly/ML compiler. To use it you must ensure that X Windows and Motif support is compiled into the Poly/ML driver program **poly**. See the Poly/ML documentation for more information.

To use the VC browser, you carry out your Compliance Notation work in an X Windows environment using the **xdaz** database rather than the **daz** database. I.e., using **pp -d xdaz** rather than **pp -d daz**. The VC Browser is started by running the **ProofPower-ML** command *'browse_vcs()'*. This brings up a window which can be used to inspect the VCs that have been generated and the Compliance Notation clauses which gave rise to them. Press the VC Browser's "Help" button for more information.

Figure 4.1: Inspecting VCs with xpp

4.5 Editing and Checking a Script

The `xpp` Script Window provides a general purpose editor for use in preparing scripts. The **ProofPower** document [7] or the `xpp` help system may be consulted for an introduction to its use. The easiest way to begin constructing a new script is to adapt an existing one, and a number of examples are supplied with the Compliance Tool for this purpose.

Just as with a **ProofPower-Z** specification, a Compliance Notation script may be entered a clause at a time so that errors can be detected and corrected interactively. Note however, that the initialisation section of the script as described in section 3.1 has to be entered first of all, and that clauses will generally need to be entered in the correct order.

If a Compliance Notation clause has been rejected because of an error, the clause can normally be re-entered once the error has been corrected. However, if a clause has been accepted, it cannot normally be re-entered. If you have loaded a script and wish to modify and reload it, the function `delete_script` may be used to avoid having to start again from scratch. `delete_script` takes as its parameter the name of the script to be deleted. It prints out a report of what it has deleted to help you determine what must be reloaded. A call of `delete_script` would normally be followed by a `new_script` command to begin work on the revised version of the script.

To make entry of Compliance Notation constructs easier, the Templates Tool in `xpp` has been customised so that you can enter a template for any of the following constructs with one button-press:

1. Replacement Step;
2. Refinement Step;
3. Compilation Unit;
4. Specification Statement;
5. Each of the 8 Z Paragraph forms.

The customised Templates Tool may be seen in figure 4.2 on page 22. The four buttons at the bottom are for the four Compliance Notation constructs listed above. In the figure the user has just pressed the refinement step button and has started to fill in the template at the bottom of the Script Window — the label has been filled in and the ellipsis, ‘...’, has been selected ready to be replaced by the statement part of the refinement.

4.6 Accessing and Proving VCs

VCS are represented in the tool using Z’s conjecture paragraph form. A conjecture paragraph associates a name with a Z predicate, and is used in specifications to record properties which the specifier believes to be true of the objects specified. In **ProofPower** the function `get_conjecture` retrieves a conjecture from a specification by name. The conjecture is retrieved as a Z term which can be used, e.g., to form a goal for proof with the **ProofPower** Subgoal Package.

The tool assigns a name to each Compliance Notation clause in the script being processed and this name is used as the basis for the names of any VCs generated by the clause. In the case of clauses containing a package or similar named Ada object, the name is taken from the object name; for other clauses the name is derived from the implicit or explicit numeric label associated with the clause.

Examples of VC names can be seen in 4.1 on page 20. Here the clause name *5* on the left refers to the refinement step refining label *5* as shown in the following extracts from the file `wrk501.doc`:

Figure 4.2: Compliance Notation Templates Tool

Compliance Notation

```

|  $\sqsubseteq$ 
|  $\Delta MULT[mult\_inv, mult\_inv \wedge MULT(N) \geq J]$ 
|  $\Delta JPRIME [mult\_inv \wedge MULT(N) \geq J,$ 
|  $mult\_inv \wedge MULT(N) \geq J \wedge JPRIME = MULT(N) \text{ noteq } J] \quad (5)$ 

```

Compliance Notation

```

| (5)  $\sqsubseteq$ 
|
|  $jprime := mult(n) \neq j;$ 

```

The VCs corresponding to the clause named *5* are named *vc5_1*, *vc5_2*, etc. (in this case there is only one). A clause name like *4_11* is used for the *11*-th unlabelled refinement step in the script, with the *4* indicating that this refinement step comes after the one labelled *4*.

To access the VC in the example, one would use the ProofPower-ML function call:

```

|  $get\_conjecture \text{ "PRIMES'proc" "vc5_1" };$ 

```

Here the first parameter gives the name of the literate script which gave rise to the VC. Proof of a VC will normally need to be carried out within the theory corresponding to the literate script in question. Thus, to begin work on a proof of the VC with the Subgoal Package, the following commands would be used:

```

|  $open\_theory \text{ "PRIMES'proc" };$ 
|  $set\_goal([], get\_conjecture \text{ "PRIMES'proc" "vc5_1" });$ 

```

As ordinary Z goals, VCs may be proved using all of the normal facilities provided by ProofPower for proof in Z. Some extensions to the Z toolkit are used in many VCs. These extensions are contained in the theory *cn*. Some custom support is provided in the Compliance Tool to assist with reasoning in this theory, most notably the proof contexts *cn1* and *cn1_ext*. These proof contexts (amongst others) and the other custom proof tools are described in section 7.2 of this document. The user interface to the conversions etc. described there is via the proof contexts, so that most users will only need to be familiar with the proof contexts. An example of a complete proof script for a literate script is included in chapter 5

4.7 Printing and Previewing Scripts

You can produce a L^AT_EX document from a Compliance Notation script using ProofPower's document preparation facilities, which are described in detail in ProofPower *Tutorial* [7]. The UNIX command `doctex` is used to produce the L^AT_EX file which you can then run through L^AT_EX using the UNIX command `texdvi`. You can then print or preview the resulting DVI file using the programs provided with your L^AT_EX installation.

So for example, to print the script in file `wrk507.doc` on a PostScript printer, you might use the following UNIX commands:

```

doctex wrk507
texdvi wrk507
dvips wrk507

```

You can also generate a DVI file with embedded hypertext links, which help you relate k-slots and specification statements with the corresponding replacement steps and refinement steps. To do this, you use a Compliance Tool function which generates an edit script which can be used by `doctex` to make the links. For example, the example script `wrk507.doc`, contains the following `ProofPower-ML` command at the end:

```
| output_hypertext_edit_script{out_file="wrk507.ex"};
```

When the script has been loaded, the DVI file with hypertext links can be produced using the UNIX commands:

```
| doctex -e wrk507.ex wrk507  
| texdvi wrk507
```

The resulting DVI file, `wrk507.dvi`, can then be viewed with a hypertext viewer such as `xhdvi`. You can navigate around the script by clicking on the underlined arrows which appear in the margins near k-slots, specification statements, refinement steps and replacement steps.

EXAMPLE SCRIPT

In this chapter, we give an example literate script, following the conventions recommended in chapter 3. The script shows the initial part of the compliance argument for an Ada procedure for computing integer square roots.

Section 5.1 contains the script and associated **ProofPower** commands. Sections 5.2 and 5.3 shows the corresponding Z document and Ada Program. Section 5.4 contains proofs of the VCs produced by the script and section 5.5 shows the listing of the resulting **ProofPower** theory.

5.1 The Literate Script

5.1.1 Initialisation Commands

We give the script the name *SQRT'proc* following the conventions for a script containing a compilation unit comprising a function named *SQRT*:

SML

```
|new_script{name = "SQRT", unit_type = "proc"};
```

5.1.2 The Compliance Argument

For simplicity, we present the square root function as a top-level procedure. The procedure has a specification statement requiring that for non-negative input values of X , the output value of Y is the integer part of the square root of X .

Compliance Notation

```
|procedure SQRT (X : INTEGER; Y : out INTEGER)
|
|Δ Y [X ≥ 0, Y ** 2 ≤ X < (Y + 1) ** 2]
|
|is
|  LO : INTEGER;
|
|  ⟨ local vars ⟩          (2)
|
|begin
|  LO := 0;
|
|  Δ LO [X ≥ 0 ∧ LO = 0, LO ** 2 ≤ X < (LO + 1) ** 2]
```

```
| Y := LO;
| end Sqrt;
```

Compliance Notation

```
| (2) ≡
|
| HI : INTEGER;
```

Compliance Notation

```
| ⊆
| Δ LO, HI [X ≥ 0 ∧ LO = 0, LO ** 2 ≤ X < (LO + 1) ** 2]
```

Compliance Notation

```
| ⊆
|
| HI := X + 1;
|
| $still [[LO ** 2 ≤ X < (LO + 1) ** 2]]
|
| loop
|
|   Δ LO, HI [LO ** 2 ≤ X < HI ** 2, LO ** 2 ≤ X < HI ** 2]
|
| end loop;
```

Compliance Notation

```
| ⊆
|
| exit when LO + 1 = HI;
|
| Δ LO, HI [LO ** 2 ≤ X < HI ** 2, LO ** 2 ≤ X < HI ** 2]
```

Note the development is not complete at this point. However, we can still output the Z document and the Ada program as if it were, and begin to do proofs. The fact that the development is not complete is made manifest in the Ada program which will contain null statements corresponding to the omissions, see section 5.3.

5.1.3 Output Commands

SML

```
| output_z_document{script="Sqrt'proc", out_file="usr501.zdoc"};
| output_ada_program{script="Sqrt'proc", out_file="usr501.ada"};
```

5.2 The Z Document

The following shows the Z document produced by the example script.

SML

```
| new_theory "SQRT' spec";
```

z

```
| vcSQRT_1 ?|-
|    $\forall X : \text{INTEGER} \mid X \geq 0 \bullet X \geq 0 \wedge 0 = 0$ 
```

z

```
| vcSQRT_2 ?|-
|    $\forall LO : \text{INTEGER}; X : \text{INTEGER}$ 
|   |  $X \geq 0 \wedge LO ** 2 \leq X \wedge X < (LO + 1) ** 2$ 
|   •  $LO ** 2 \leq X \wedge X < (LO + 1) ** 2$ 
```

z

```
| vc2_1_1 ?|-
|    $\forall LO : \text{INTEGER}; X : \text{INTEGER} \mid X \geq 0 \wedge LO = 0 \bullet X \geq 0 \wedge LO = 0$ 
```

z

```
| vc2_1_2 ?|-
|    $\forall LO, LO_0 : \text{INTEGER}; X : \text{INTEGER}$ 
|   |  $(X \geq 0 \wedge LO_0 = 0) \wedge LO ** 2 \leq X \wedge X < (LO + 1) ** 2$ 
|   •  $LO ** 2 \leq X \wedge X < (LO + 1) ** 2$ 
```

z

```
| vc2_2_1 ?|-
|    $\forall LO : \text{INTEGER}; X : \text{INTEGER}$ 
|   |  $X \geq 0 \wedge LO = 0$ 
|   •  $LO ** 2 \leq X \wedge X < (X + 1) ** 2$ 
```

z

```
| vc2_2_2 ?|-
|    $\forall HI : \text{INTEGER}; LO, LO_0 : \text{INTEGER}; X : \text{INTEGER}$ 
|   |  $(X \geq 0 \wedge LO_0 = 0) \wedge LO ** 2 \leq X \wedge X < HI ** 2$ 
|   •  $LO ** 2 \leq X \wedge X < HI ** 2$ 
```

z

```
| vc2_2_3 ?|-
|    $\forall LO, LO_0 : \text{INTEGER}; X : \text{INTEGER}$ 
|   |  $(X \geq 0 \wedge LO_0 = 0) \wedge LO ** 2 \leq X \wedge X < (LO + 1) ** 2$ 
|   •  $LO ** 2 \leq X \wedge X < (LO + 1) ** 2$ 
```

z

 $vc2_3_1 \text{ ?}\vdash$ $\forall HI : INTEGER; LO : INTEGER; X : INTEGER$ $| (LO ** 2 \leq X \wedge X < HI ** 2) \wedge LO + 1 \text{ eq } HI = TRUE$ $\bullet LO ** 2 \leq X \wedge X < (LO + 1) ** 2$

z

 $vc2_3_2 \text{ ?}\vdash$ $\forall HI : INTEGER; LO : INTEGER; X : INTEGER$ $| (LO ** 2 \leq X \wedge X < HI ** 2) \wedge LO + 1 \text{ eq } HI = FALSE$ $\bullet LO ** 2 \leq X \wedge X < HI ** 2$

z

 $vc2_3_3 \text{ ?}\vdash$ $\forall HI, HI_0 : INTEGER; LO, LO_0 : INTEGER; X : INTEGER$ $| (LO_0 ** 2 \leq X \wedge X < HI_0 ** 2) \wedge LO ** 2 \leq X \wedge X < HI ** 2$ $\bullet LO ** 2 \leq X \wedge X < HI ** 2$

SML

 $(* \text{ Number of VCs in theory "SQRT'spec" : 10 } *)$

5.3 The Ada Program

The following shows the Ada program produced by the example script. Note the *NULL* statement the tool has introduced because the development was not complete. The comment on the *NULL* statements means that the unlabelled specification statement to which the tool has assigned label *2_4* has not been refined.

```
PROCEDURE SQRT (X : IN INTEGER; Y : OUT INTEGER)
  -- Spec ...
IS
  LO : INTEGER;
  HI : INTEGER;
BEGIN
  LO := 0;
  HI := X + 1;
  -- ...
  LOOP
    EXIT WHEN LO + 1 = HI;
    NULL; -- 2_4
  END LOOP;
  Y := LO;
END SQRT;
```

5.4 The Proofs

To embark on the proofs of the VCs, we first open the theory for the literate script:

SML

```
|open_theory "SQRT' spec";
```

We will work in the proof context *cn1* most of the time:

SML

```
|set_pc "cn1";
```

The statements of the VCs may be seen in the Z document in section 5.2 or in the theory listing in section 5.5.

All but one of the VCs are little more than tautologies, and will be proved by repeating *strip_tac*.

SML

```
|set_goal([], get_conjecture "SQRT' spec" "vcSQRT_1");
|a(REPEAT strip_tac);
|val vcSQRT_1_thm = save_pop_thm "vcSQRT_1_thm";
```

SML

```
|set_goal([], get_conjecture "SQRT' spec" "vcSQRT_2");
|a(REPEAT strip_tac);
|val vcSQRT_2_thm = save_pop_thm "vcSQRT_2_thm";
```

SML

```
|set_goal([], get_conjecture "SQRT' spec" "vc2_1_1");
|a(REPEAT strip_tac);
|val vc2_1_1_thm = save_pop_thm "vc2_1_1_thm";
```

SML

```
|set_goal([], get_conjecture "SQRT' spec" "vc2_1_2");
|a(REPEAT strip_tac);
|val vc2_1_2_thm = save_pop_thm "vc2_1_2_thm";
```

Before proving the next VC, we need to prove some simple facts about exponentiation, namely: $x ** 1 = x$ and $x ** 2 = x * x$. The proofs require little more than specialising the definition of $(**_)$ appropriately.

SML

```
|set_goal([],  $\sqsupset \forall x: \mathbb{Z} \bullet x ** 1 = x$ );
|a(REPEAT strip_tac);
|a(rewrite_tac[rewrite_rule(
  |  $z \cdot \forall \text{elim} \sqsupset (x \hat{=} x, y \hat{=} 0) \sqsupset (\wedge \text{right\_elim}(z \cdot \text{get\_spec} \sqsupset (**_))$ )]));
|val star_star_1_thm = pop_thm();
```


SML

```

| set_goal([],  $\forall x: \mathbb{Z} \bullet x ** 2 = x * x$ );
| a(REPEAT strip_tac);
| a(rewrite_tac[star_star_1_thm, rewrite_rule](
|   z_∇_elim $\overline{z}$ ( $x \hat{=} x, y \hat{=} 1$ ) $^\top$  ( $\wedge$ _right_elim( $z$ _get_spec $\overline{z}$ ( $-**$ ) $^\top$ ))));
| val star_star_2_thm = pop_thm();

```

The meat of the next VC is that for non-negative X , $X \leq (X + 1) ** 2$. Induction is used to prove this, together with the above lemmas and the automatic prover for linear arithmetic.

SML

```

| set_goal([], get_conjecture "SQRT' spec" "vc2_2_1");
| a(REPEAT strip_tac THEN asm_rewrite_tac[star_star_2_thm]);
| a(DROP_NTH_ASM_T 2 ante_tac THEN DROP_ASMS_T discard_tac THEN strip_tac);
| a( $z \leq$ _induction_tac $\overline{z}$  $X$  $^\top$  THEN PC_T1 "z_lin_arith" asm_prove_tac[]);
| val vc2_2_1_thm = save_pop_thm"vc2_2_1_thm";

```

The remaining VCs are straightforward:

SML

```

| set_goal([], get_conjecture "SQRT' spec" "vc2_2_2");
| a(REPEAT strip_tac);
| val vc2_2_2_thm = save_pop_thm"vc2_2_2_thm";

```

SML

```

| set_goal([], get_conjecture "SQRT' spec" "vc2_2_3");
| a(REPEAT strip_tac);
| val vc2_2_3_thm = save_pop_thm"vc2_2_3_thm";

```

SML

```

| set_goal([], get_conjecture "SQRT' spec" "vc2_3_1");
| a(rewrite_tac[]);
| a(REPEAT strip_tac);
| a(all_var_elim_asm_tac1);
| val vc2_3_1_thm = save_pop_thm"vc2_3_1_thm";

```

SML

```

| set_goal([], get_conjecture "SQRT' spec" "vc2_3_2");
| a(REPEAT strip_tac);
| val vc2_3_2_thm = save_pop_thm"vc2_3_2_thm";

```

SML

```

| set_goal([], get_conjecture "SQRT' spec" "vc2_3_3");
| a(REPEAT strip_tac);
| val vc2_3_3_thm = save_pop_thm"vc2_3_3_thm";

```

5.5 The Theory Listing

The following is the listing of the theory *usr501* produced by the example script and the proofs in the previous section.

5.5.1 Parents

cache'daz *cn*

5.5.2 Conjectures

vcSqrt_1

$\forall X : \text{INTEGER} \mid X \geq 0 \bullet X \geq 0 \wedge 0 = 0$

vcSqrt_2

$\forall LO : \text{INTEGER}; X : \text{INTEGER}$
 $\mid X \geq 0 \wedge LO ** 2 \leq X \wedge X < (LO + 1) ** 2$
 $\bullet LO ** 2 \leq X \wedge X < (LO + 1) ** 2$

vc2_1_1

$\forall LO : \text{INTEGER}; X : \text{INTEGER}$
 $\mid X \geq 0 \wedge LO = 0$
 $\bullet X \geq 0 \wedge LO = 0$

vc2_1_2

$\forall LO, LO_0 : \text{INTEGER}; X : \text{INTEGER}$
 $\mid (X \geq 0$
 $\quad \wedge LO_0 = 0)$
 $\quad \wedge LO ** 2 \leq X$
 $\quad \wedge X < (LO + 1) ** 2$
 $\bullet LO ** 2 \leq X \wedge X < (LO + 1) ** 2$

vc2_2_1

$\forall LO : \text{INTEGER}; X : \text{INTEGER}$
 $\mid X \geq 0 \wedge LO = 0$
 $\bullet LO ** 2 \leq X \wedge X < (X + 1) ** 2$

vc2_2_2

$\forall HI : \text{INTEGER}; LO, LO_0 : \text{INTEGER}; X : \text{INTEGER}$
 $\mid (X \geq 0 \wedge LO_0 = 0) \wedge LO ** 2 \leq X \wedge X < HI ** 2$
 $\bullet LO ** 2 \leq X \wedge X < HI ** 2$

vc2_2_3

$\forall LO, LO_0 : \text{INTEGER}; X : \text{INTEGER}$
 $\mid (X \geq 0$
 $\quad \wedge LO_0 = 0)$
 $\quad \wedge LO ** 2 \leq X$
 $\quad \wedge X < (LO + 1) ** 2$
 $\bullet LO ** 2 \leq X \wedge X < (LO + 1) ** 2$

vc2_3_1

$\forall HI : \text{INTEGER}; LO : \text{INTEGER}; X : \text{INTEGER}$
 $\mid (LO ** 2 \leq X \wedge X < HI ** 2) \wedge LO + 1 \text{ eq } HI = \text{TRUE}$
 $\bullet LO ** 2 \leq X \wedge X < (LO + 1) ** 2$

vc2_3_2

$\forall HI : \text{INTEGER}; LO : \text{INTEGER}; X : \text{INTEGER}$
 $\mid (LO ** 2 \leq X \wedge X < HI ** 2) \wedge LO + 1 \text{ eq } HI = \text{FALSE}$
 $\bullet LO ** 2 \leq X \wedge X < HI ** 2$

vc2_3_3

$\forall HI, HI_0 : \text{INTEGER}; LO, LO_0 : \text{INTEGER}; X : \text{INTEGER}$
 $\mid (LO_0 ** 2 \leq X$
 $\quad \wedge X < HI_0 ** 2)$
 $\quad \wedge LO ** 2 \leq X$
 $\quad \wedge X < HI ** 2$
 $\bullet LO ** 2 \leq X \wedge X < HI ** 2$

5.5.3 Theorems

vcSQRT_1_thm

$$\vdash \forall X : \text{INTEGER} \mid X \geq 0 \bullet X \geq 0 \wedge 0 = 0$$

vcSQRT_2_thm

$$\begin{aligned} &\vdash \forall LO : \text{INTEGER}; X : \text{INTEGER} \\ &\quad \mid X \geq 0 \wedge LO ** 2 \leq X \wedge X < (LO + 1) ** 2 \\ &\quad \bullet LO ** 2 \leq X \wedge X < (LO + 1) ** 2 \end{aligned}$$

vc2_1_1_thm

$$\begin{aligned} &\vdash \forall LO : \text{INTEGER}; X : \text{INTEGER} \\ &\quad \mid X \geq 0 \wedge LO = 0 \\ &\quad \bullet X \geq 0 \wedge LO = 0 \end{aligned}$$

vc2_1_2_thm

$$\begin{aligned} &\vdash \forall LO, LO_0 : \text{INTEGER}; X : \text{INTEGER} \\ &\quad \mid (X \geq 0 \\ &\quad \quad \wedge LO_0 = 0) \\ &\quad \quad \wedge LO ** 2 \leq X \\ &\quad \quad \wedge X < (LO + 1) ** 2 \\ &\quad \bullet LO ** 2 \leq X \wedge X < (LO + 1) ** 2 \end{aligned}$$

vc2_2_1_thm

$$\begin{aligned} &\vdash \forall LO : \text{INTEGER}; X : \text{INTEGER} \\ &\quad \mid X \geq 0 \wedge LO = 0 \\ &\quad \bullet LO ** 2 \leq X \wedge X < (X + 1) ** 2 \end{aligned}$$

vc2_2_2_thm

$$\begin{aligned} &\vdash \forall HI : \text{INTEGER}; LO, LO_0 : \text{INTEGER}; X : \text{INTEGER} \\ &\quad \mid (X \geq 0 \wedge LO_0 = 0) \wedge LO ** 2 \leq X \wedge X < HI ** 2 \\ &\quad \bullet LO ** 2 \leq X \wedge X < HI ** 2 \end{aligned}$$

vc2_2_3_thm

$$\begin{aligned} &\vdash \forall LO, LO_0 : \text{INTEGER}; X : \text{INTEGER} \\ &\quad \mid (X \geq 0 \\ &\quad \quad \wedge LO_0 = 0) \\ &\quad \quad \wedge LO ** 2 \leq X \\ &\quad \quad \wedge X < (LO + 1) ** 2 \\ &\quad \bullet LO ** 2 \leq X \wedge X < (LO + 1) ** 2 \end{aligned}$$

vc2_3_1_thm

$$\begin{aligned} &\vdash \forall HI : \text{INTEGER}; LO : \text{INTEGER}; X : \text{INTEGER} \\ &\quad \mid (LO ** 2 \leq X \wedge X < HI ** 2) \wedge LO + 1 \text{ eq } HI = \text{TRUE} \\ &\quad \bullet LO ** 2 \leq X \wedge X < (LO + 1) ** 2 \end{aligned}$$

vc2_3_2_thm

$$\begin{aligned} &\vdash \forall HI : \text{INTEGER}; LO : \text{INTEGER}; X : \text{INTEGER} \\ &\quad \mid (LO ** 2 \leq X \\ &\quad \quad \wedge X < HI ** 2) \\ &\quad \quad \wedge LO + 1 \text{ eq } HI = \text{FALSE} \\ &\quad \bullet LO ** 2 \leq X \wedge X < HI ** 2 \end{aligned}$$

vc2_3_3_thm

$$\begin{aligned} &\vdash \forall HI, HI_0 : \text{INTEGER}; LO, LO_0 : \text{INTEGER}; X : \text{INTEGER} \\ &\quad \mid (LO_0 ** 2 \leq X \\ &\quad \quad \wedge X < HI_0 ** 2) \\ &\quad \quad \wedge LO ** 2 \leq X \\ &\quad \quad \wedge X < HI ** 2 \\ &\quad \bullet LO ** 2 \leq X \wedge X < HI ** 2 \end{aligned}$$

EVALUATION GUIDELINES

6.1 Introduction

The Compliance Notation gives considerable flexibility in the degree of formal rigour applied at each stage in the presentation of a compliance argument. This chapter gives some guidelines for assessing a compliance argument.

6.2 Scope of a Compliance Argument

A compliance argument will contain one or more specification statements acting as the starting points of the formal argument. These are the specification statements that give the top level statement of critical properties of the code.

For a subprogram library, the formal starting points might comprise the specification statements for each of the subprograms in a suite of package specifications defining the external interface of the library.

For a complete application, the formal starting point might comprise a single specification statement appearing in the body of the main subprogram of the application. The formal starting points will generally be explicitly identified as such in the narrative parts of the script.

6.3 Conformance with the Ada Standard

While the Compliance Tool detects many types of error, it does not detect all the types of error that an Ada implementation is required to detect during the compilation process. A compliance argument is only valid if the Ada code it contains can be compiled without error using an Ada implementation that conforms either to the 1995 version of the standard [12] or to the 1983 version [11]. Providing there are no compilation errors, the checks made by the tool ensure that the formally developed code is not sensitive to the difference between the two versions of the standard.

The predefined type *CHARACTER* was changed in the 1995 standard: the value *CHARACTER'LAST* was increased from 127 to 255. The tool takes 127 as a lower bound on *CHARACTER'LAST*. If the compliance argument includes an axiom that further constrains the value of *CHARACTER'LAST*, then the argument should include informal justification that the axiom is compatible with the intended Ada implementation.

6.4 Formal Development Steps

The formal argument for each starting point will comprise a chain, or tree, of formal steps each of which may produce one or more verification conditions (VCs). Type definitions may also give rise to

VCS. The various sorts of VC that may be generated are described in detail in *Compliance Notation — Language Description* [15]. There are several sorts of formal development step:

- **Explicit refinements:** in these the refinement symbol (\sqsubseteq) is used to assert that a sequence of statements correctly implements a specification statement.
- **Implicit refinements:** these occur when the semantics of the Compliance Notation require one specification statement to refine another (e.g., this happens when a formal subprogram in a package body is implemented: the specification statement of the formal subprogram in the package body must correctly implement the specification statement for the subprogram given in the package specification).
- **Declaration replacements:** these occur when a k-slot used as a declaration is expanded using the replacement symbol (\equiv).

A complete formal development comprises a sequence of steps of the above three sorts in which all specification statements have been refined to code. Given such a complete formal development, the VC generation algorithm is designed so that truth of the VCs entails “partial” or “algorithmic” correctness of the implementation, i.e., so that truth of the VCs implies that the code meets its specification providing it terminates and does not raise any exceptions. Truth of the VCs does not of itself guarantee either that the code terminates or that the code will not raise exceptions.

Note that the tool can only check the correctness of code that it has actually processed. For example, the VC generation algorithm assumes that the subprograms in an external package meet their specifications in the package specification. A formal development of the package body must be processed to ensure that this is the case.

6.5 Informal Development Steps

Other sorts of development step break the chain of formality. These informal development steps are as follows:

- **Arbitrary replacements:** these are introduced by the arbitrary replacement symbol ($!\equiv$) and enable a k-slot or refinement step to be expanded to an arbitrary fragment of Ada code in an unchecked fashion.
- **Statement replacements:** these occur when a k-slot as a statement or specification statement as a statement is expanded using the statement replacement symbol ($!\sqsubseteq$) rather than the refinement symbol (\sqsubseteq).

If either of these sorts of step occurs in a script then the VCs do not give a formal guarantee that the code is correct. An informal justification of the correctness of the informal step must be provided and evaluated in this case.

6.6 Consistency

An inconsistent axiom allows any property to be proved. **ProofPower** supports two modes of dealing with a *Z* axiomatic description: in the conservative mode, the defining property is replaced by a property which is guaranteed consistent and is equivalent to the desired property if the latter

is provably consistent; in the axiomatic mode, the defining property is asserted as an axiom. If consistency is a concern, then **ProofPower** should be put into its conservative mode of working by setting the system flag `z_use_axioms` to `false`. This may be done by:

```
|set_flag("z_use_axioms", false);
```

This will ensure that all axiomatic descriptions include a consistency caveat. Consistency proofs may then be carried out using the usual **ProofPower** mechanisms.

6.7 Checking for Errors

When an error is detected during loading of a script, the normal behaviour of the tool is to raise an exception and stop processing. To check that a script has been processed without error, the output from loading the script can be examined. Alternatively, the function `print_exception_log` can be used to print a log of all the errors and potential soundness problems that have been detected in a script. **ProofPower-ML** exception handlers and certain control flags can be used to make the tool continue processing after an error has been detected. There is also a flag `cn_stop_on_exceptions` defined as part of the Compliance Tool that may be used for this purpose (see section 7 below). The script may be checked for occurrences of the `handle` keyword or the `set_flag` command to see if error reporting has been suppressed inappropriately. See **ProofPower Description Manual** [8] for a description of the **ProofPower** control flags.

Some errors are only detected when the Ada program is generated (see section 4.2). Thus each script should include a command to generate the Ada program. This command should come after all the Ada code in the script. Errors detected during Ada program generation are logged even if the command is protected by a **ProofPower-ML** exception handler. In some cases, these errors may refer to informal parts of the Ada program and so may be considered acceptable.

The process of generating and reloading the Z document as described in section 4.3 is not intended to give rise to errors except in unusual circumstances. However, it does provide an extra layer of checking (by passing all the generated Z through the Z parser and type inferrer) and should increase confidence that the script has been processed correctly by the tool. For example, it ensures that all the names of Z objects that have been generated conform to the lexical rules for Z identifiers.

6.8 Treatment of Real Types

Ada floating point and fixed types are mapped by the Compliance Tool to the real numbers of pure mathematics as represented in the Z theory of real numbers. This is an idealisation and means outside the scope of the tool must be used to justify the approximations inherent in the use of computer arithmetic when the code is executed.

For fixed point types, the accuracy of the idealisation is heavily dependent on careful use of the Ada features for controlling the accuracy of the arithmetic. If used carefully, a fixed point type will represent a subset of the mathematical real numbers with complete accuracy (e.g., by using a representation clause, if necessary, to make “small” the precise value needed by the application and by restricting operations to addition, subtraction and multiplication by integers). On the other hand, a fixed point type whose “delta” is specified as $2^n - 1$ for a large integer n and which has no representation clause to define “small” will give a particularly bad approximation.

For floating point types, the accuracy of the idealisation can be heavily dependent on the compiler used if the Ada features for writing portable floating code are not used carefully.

ProofPower-ML COMPLIANCE TOOL REFERENCE

The following sections give reference documentation for the principal ProofPower-ML commands which are specific to the Compliance Tool, followed by the theory listing of “cn”. Consult the ProofPower reference manual for further information on ProofPower-ML commands mentioned in this document which are not mentioned here.

7.1 Controlling the Tool

SML

```
|signature ComplianceTool = sig
```

Description This is the signature for the commands which a user requires to operate the compliance tool.

SML

```
|val print_exception_log : string -> bool;  
|val output_exception_log : {script : string, out_file : string} -> bool;  
|val delete_exception_log : string -> unit;
```

Description *print_exception_log* causes the exception log for the named script to be printed to standard output. *output_exception_log* prints the exception log to a file. Both of these functions return true if some exceptions have been logged for the script. If no exceptions have been logged a message to that effect is output and the functions return false. Both functions will raise an exception if the named script has not been processed.

delete_exception_log deletes the exception log for the named script.

SML

```

val new_script : {name : string, unit_type : string} -> unit
val new_script1 :
  {name : string, unit_type : string,
   library_theories : string list} -> unit
val new_continuation_script : {name : string, unit_type : string} -> unit
val new_continuation_script1 :
  {name : string, unit_type : string,
   library_theories : string list} -> unit
val delete_script : {name : string, unit_type : string} -> unit;
val delete_deferred_subprogram : string -> unit;

```

Description *new_script* takes as an argument the name of an Ada compilation unit and a string indicating the type of compilation unit, which must be one of "spec" (package specification), "body" (package body), "proc" (procedure) or "func" (function). It creates a new **ProofPower** theory, into which the Z document generated by the subsequent literate script is placed. The name of the theory is derived from the compilation unit name and type. The data structure containing the SPARK program is set to its initial empty value.

new_script1 acts the same as *new_script* except that its list of library theories will be made the parents of the script theory, and any theory produced during processing the script.

new_continuation_script and *new_continuation_script1* are just like *new_script* and *new_script1*, except that the data structure containing the SPARK program is retained.

delete_script removes a script from the Compliance Tool state together with any associated theories and other items in the state dependent on the script so that the script can be reloaded. It prints a report of what has been deleted.

delete_deferred_subprogram allows the part of a script associated with a deferred subprogram to be re-entered. It deletes all theories associated with the deferred subprogram and undoes the effects of any refinement or replacement steps associated with the deferred subprogram.

SML

```

val open_scope : string -> unit

```

Description With each declarative region in an Ada program, there is a Z theory to hold definitions and verification conditions that are needed in the scope of the declarative region. The scope is initially set by *new_script* to correspond to the declarative region of the compilation unit. The *open_scope* command is then used to navigate into the scope of nested subprogram bodies. The argument of *open_scope* is the expanded name of the package or subprogram. For example, if the body of a package named *Utils* defines a subprogram named *sort*, then before expanding a k-slot or specification statement in the body of *sort*, the following call is required:

```

open_scope "Utils.sort" ;

```

open_scope may also be used to return to the scope of the compilation unit after opening the scope of a package or subprogram contained in it.

SML

```
val print_ada_program : string -> unit
val output_ada_program : {script : string, out_file : string} -> unit
```

Description The call *print_spark_program name* generates and prints the Ada program contained in the script with the specified name. A minus sign may be used for the name to specify the current script. The Ada program is created by filling in the k-slots and specification statements in a literate script with their expansions. The result is printed on the standard output channel in Ada syntax such that it could be subsequently compiled using an Ada compiler.

output_spark_program does the same as *print_spark_program* but allows the user to specify a file to which the program will then be written.

Uses For example, if a literate script called “primes” has just been entered into the Compliance Tool then the following command will output the corresponding Ada to a file called *primes.ada*.
`output_ada_program{script = "-", out_file="primes.ada"}`

SML

```
val print_z_document : string -> unit
val output_z_document : {script : string, out_file : string} -> unit
val get_script_theories : string -> string list
```

Description *print_z_document* prints the Z document from the named script to the standard output. The listing is in a format suitable for its re-entry into a ProofPower session.

output_z_program is similar to *print_z_document*, but the the Z document is written to the named output file *out_file*.

Uses For example, if a literate script called “primes” has been entered into the Compliance Tool then the following command will output the corresponding Z document to a file called *primes.zdoc*.
`output_z_document{script="primes", out_file="primes.zdoc"}`

get_script_theories n returns a list of all the ProofPower theories associated with the Compliance Notation script *n*.

SML

```
type EVAL_REPORT
val get_eval_report :
    {title : string, theories : string list} -> EVAL_REPORT;
val print_eval_report : EVAL_REPORT -> unit;
val output_eval_report : {report : EVAL_REPORT, out_file : string} -> unit;
val output_eval_report1 : {report : EVAL_REPORT, out_file : string} -> unit;
```

Description These functions are used to create a report for assistance in evaluating a compliance argument. The type *EVAL_REPORT* is an abstract type representing the logical contents of the report. To create a report first use *get_eval_report* to give a title to the report specifying the theories to be included in the report. The report can then be output to the screen using *print_eval_report* or to a file, either in *LaTeX* format using *cn_output_eval_report* or in raw ProofPower text format using *cn_output_eval_report1*.

SML

```
|val browse_vcs : unit -> unit
```

Description On systems with support for X/Windows and Motif enabled, this command invokes an interactive browsing tool for mapping VCs onto web clauses, and vice versa.

Errors

```
|516201 The VC browser is not available on this ProofPower system
```

SML

```
|val output_hypertext_edit_script : {out_file : string} -> unit
```

Description This function produces an edit script from a processed literate script. The edit script is placed in a file with name *out_file*.

The edit script will introduce hypertext links between specification statements or K-Slots, and their corresponding expansion paragraphs.

SML

```
|(* Flag cn_use_let_in_vcs - boolean control, default false *)
```

Description This is the name of flag (see *set_flag* in *ProofPower Reference Manual* [16]). The flag controls the way substitution of expressions for variables is treated during VC generation.

If the flag is false (the default), then the substitution is carried out using the HOL *subst* function and then conversions are used to transform the result into Z: this results in a Z term in which the variables have actually been replaced by their substitutes, and declarations and other constructs have been adjusted as necessary to avoid variable capture.

If the flag is set true (using *set_flag*), then a Z *let* construct is used to give the semantics of substitution without actually replacing any variables with their substitutes. This can help to abbreviate the VC and make its structure clearer.

SML

```
|(* Flag cn_show_typing_context - boolean control, default false *)
```

Description This is the name of a flag (see *set_flag* in *ProofPower Reference Manual* [16]). It controls a diagnostic display which may be of help in understanding problems with type-checking.

When set to true the flag will cause the tool to display the typing context used when type-checking each Z expression encountered in Compliance Notation clauses. The display gives a list of the theories that are in scope (omitting *z_library* and its ancestors) and a list of the variables that are have their type fixed by the context (e.g., variables corresponding to SPARK program variables).

This flag is primarily a diagnostic aid for developers of the tool, but may be of help to a general user in some difficult cases.

SML

```
|(* cn_case_of_ada_keywords - string control, default "lower" *)
```

Description This is the name of a string control (see *set_string_control* in *ProofPower Reference Manual* [16]). It controls the case of Ada reserved words in the output from *print_ada_program* and *output_ada_program*. There are three allowed values for the string control: "upper", "lower" and "as input". The values "upper" and "lower" makes the tool generate upper-case and lower-case reserved words respectively; the value "as input" makes the tool use the same case for a reserved word as was used at its first appearance in the original Compliance Notation script.

SML

```
(* cn_automatic_line_splitting – integer control declared by new_int_control, default 80 *)
(* cn_tab_width – integer control declared by new_int_control, default 2 *)
(* cn_left_margin – integer control declared by new_int_control, default 0 *)
```

Description These controls define parameters for output from *format_web_clause*, *print_web_clause*, *print_spark_program*, *output_spark_program* and *strings_from_fmt1*.

These functions always split lines at logical break points (e.g. at semicolons at the end of statements). However, they will also split lines that are longer than the value set by the control *cn_automatic_line_splitting* (if it is non-zero), choosing to split at a syntactically allowed location. If the control is set to 0 then no automatic line-splitting is done (except in Z terms, which will be split during formatting to match the current line length setting). The default value of the control is 80.

cn_tab_width controls the number of spaces used to indent a nested structure such as the statement in the then part of an if statement. *cn_left_margin* specifies an indentation to be applied on every line and is given in the units specified by *cn_tab_width*. E.g., if *cn_tab_width* is 2 and *cn_left_margin* is 4, every line will be indented by at least 8 spaces, and each nested structure will be indented 2 spaces more than the structure immediately containing it.

SML

```
(* Flag cn_compactify_terms – boolean control, default true *)
```

Description This is the name of a flag (see *set_flag* in *ProofPower Reference Manual* [16]). It controls a feature for optimising the memory space occupied by the Z terms stored in the internal data structures of the Compliance Tool.

Since this space optimisation involves some processing overhead, it is optional, but it is enabled by default. The space optimisation can be disabled by setting the flag *cn_compactify_terms* to *false* and enabled by setting it to *true*.

SML

```
(* Flag cn_syntax_check_only – boolean control, default false *)
```

Description This is the name of a flag (see *set_flag* in *ProofPower Reference Manual* [16]). It allows you to carry out check that a script complies with the syntax of the Compliance Notation.

When the flag is set false, all stages of processing are carried out on each Compliance Notation clause entered.

When the flag is set true, all processing relating to the production of VCs and the Z paragraphs that support them is suppressed. The Ada program may still be produced and all the literate programming language features may be used.

Note that this flag only controls processing of Compliance Notation clauses. Z paragraphs will be processed regardless of the value of this flag.

SML

```
(* Flag cn_stop_on_exceptions – boolean control, default true *)
```

Description This flag controls the handling of exceptions when a Compliance Notation clause is processed. If the flag is true, then if processing a Compliance Notation clause will cause ML exceptions when errors are detected. If the flag is false, then an error message will be printed but an exception will not be raised. In either case, the error message will be recorded in a log that can be inspected using *print_exception_log*, q.v.

The processing of Z paragraphs is not affected by this flag.

SML

```
(* cn_domain_conds: integer control; 3 values allowed as follows *)
val cn_no_domain_conds : int;
val cn_standard_domain_conds : int;
val cn_all_domain_conds : int;
```

Description *cn_domain_conds* is the name of an integer control (see *set_int_control* in *ProofPower Reference Manual* [16]). It controls the generation of domain conditions, i.e., additional hypotheses that are added to VCs to help make them provable in cases where the compliance argument depends for its validity on the program not raising an exception. The control should be given one of the three integer values given by the three ML bindings above. The effect of the control is shown in the following table:

<i>cn_no_domain_conds</i>	No domain conditions are generated.
<i>cn_standard_domain_conds</i>	Only domain conditions corresponding to exceptions that are guaranteed by ALRM [4] are generated.
<i>cn_all_domain_conds</i>	Domain conditions are generated as for <i>cn_standard_domain_conds</i> together with domain conditions corresponding to real arithmetic exceptions.

SML

```
(* cn_spark_syntax_warnings – flag declared by new_flag; default false *)
```

Description This flag controls whether or not Ada comments are to be inserted in the output produced by the functions *format_web_clause*, *print_web_clause*, *print_ada_program*, and *output_ada_program* to warn about uses of Ada syntax that is not in the SPARK subset. Note that the checks are only made on the Ada concrete syntax, ignoring comments, there is no checking that required SPARK annotations are present or correct and no context-sensitive checks are made. The checks are based on the syntax given in John Barnes’ book *High Integrity Ada The SPARK Approach* ISBN 0-201-17517-7.

SML

```
(* Flag cn_ignore_spark_annotations – boolean control, default false *)
```

Description This flag controls the treatment of Ada comments having the form of SPARK annotations, i.e., comments beginning with “--#” (but see also *cn_spark_annotation_char* below). If the flag is false, the default, then such comments are treated as SPARK annotations and may only appear in syntactic positions where the SPARK syntax accepts an annotation. The annotations are remembered for inclusion in the output when the program is printed. If the flag is true, then such comments are treated as ordinary Ada comments and ignored.

SML

```
(* Flag cn_spark_annotation_char – string control, default "#" *)
```

Description By default SPARK annotations are Ada comments beginning with “--#” This string control may be used to specify an alternative to “#” for the character that distinguishes SPARK annotation from other Ada comments.

SML

```
| val array_agg_def : int -> unit
```

Description This function is used to generate the definitions that support multidimensional array aggregates. These definitions are built-in for arrays of up to 20 dimensions, so you do not need to use this function unless you have array aggregates of more than 20 dimensions. The function should be called after first opening either the theory “cn” or the cache theory for your database.

Errors

```
| 508059 the argument to array_agg_def must be at least 2
```

7.2 Custom Proof Facilities

SML

```
|signature CNToolkitExtensions = sig
```

Description This is the signature for the toolkit extensions required by the Compliance Tool. It is specified in DRA/CIS/CSE3/TR/94/27/3.0.

SML

```
|val cn_boolean_cases_thm : THM;
|val cn_boolean_clauses : THM;
|val cn_boolean_clauses1 : THM;
|val cn_boolean_clauses2 : THM;
|val cn_and_then_or_else_clauses : THM;
|val cn_boolean_pos_thm : THM;
|val cn_boolean_pred_thm : THM;
|val cn_boolean_succ_thm : THM;
|val cn_boolean_thm : THM;
|val cn_boolean_∈_boolean_thm : THM;
|val cn_boolean_val_thm : THM;
|val cn_relational_clauses : THM;
|val cn_relational_clauses1 : THM;
|val cn_¬_true_eq_false_thm : THM;
|val cn_intdiv_0_thm : THM;
|val cn_intdiv_thm : THM;
|val cn_rem_thm : THM;
|val cn_intmod_thm : THM;
|val z_succn_o_thm : THM;
|val cn_integer_to_real_thm : THM;
```

Description These are the ML names for the theorems in the theory “cn”, which contains extensions to the Z toolkit required to support the Compliance Notation.

SML

```

val mk_cn_intdiv : TERM * TERM -> TERM;
val mk_cn_rem : TERM * TERM -> TERM;
val mk_cn_intmod : TERM * TERM -> TERM;
val mk_cn_star_star : TERM * TERM -> TERM;
val dest_cn_intdiv : TERM -> TERM * TERM;
val dest_cn_rem : TERM -> TERM * TERM;
val dest_cn_intmod : TERM -> TERM * TERM;
val dest_cn_star_star : TERM -> TERM * TERM;
val is_cn_intdiv : TERM -> bool;
val is_cn_rem : TERM -> bool;
val is_cn_intmod : TERM -> bool;
val is_cn_star_star : TERM -> bool;

```

Description These are constructor, destructor and discriminator functions for the operators which support the numeric operations of the Compliance Notation.

Errors

```

509001?0 does not have type  $\mathbb{Z}$ 
509002?0 is not of the form  $\lfloor i \text{ intdiv } j \rfloor$ 
509003?0 is not of the form  $\lfloor i \text{ rem } j \rfloor$ 
509004?0 is not of the form  $\lfloor i \text{ intmod } j \rfloor$ 
509005?0 is not of the form  $\lfloor i ** j \rfloor$ 

```

SML

```

val cn_intdiv_conv : CONV;
val cn_rem_conv : CONV;
val cn_intmod_conv : CONV;
val cn_star_star_conv : CONV;

```

Description These conversions perform evaluation of expressions with constant operands formed using the operators which support the numeric operations of the Compliance Notation.

Each conversion expects an expression of the form $i \text{ op } j$ where op is one of *intdiv*, *rem*, *intmod*, or ****, and where i and j are signed integer literals (i.e., either numeric literals or of the form $\sim k$, where k is a numeric literal). The resulting theorem has conclusion $i \text{ op } j = r$, where r is the signed literal resulting from evaluating the expression.

Errors

```

509011?0 is not of the form  $\lfloor i \text{ intdiv } j \rfloor$  where  $\lfloor i \rfloor$  and  $\lfloor j \rfloor$  are numeric literals
509012?0 is not of the form  $\lfloor i \text{ rem } j \rfloor$  where  $\lfloor i \rfloor$  and  $\lfloor j \rfloor$  are numeric literals
509013?0 is not of the form  $\lfloor i \text{ intmod } j \rfloor$  where  $\lfloor i \rfloor$  and  $\lfloor j \rfloor$  are numeric literals
509014?0 is not of the form  $\lfloor i ** j \rfloor$  where  $\lfloor i \rfloor$  and  $\lfloor j \rfloor$  are numeric literals

```

SML

```

|val cn_mod_and_conv : CONV;
|val cn_mod_or_conv : CONV;
|val cn_mod_xor_conv : CONV;
|val cn_mod_not_conv : CONV;

```

Description These conversions perform evaluation of expressions with constant operands formed using the operators which support the numeric operations of the Compliance Notation.

Each conversion expects an expression of the form $i \text{ op } j$ or $\text{mod_not } i$, where op is one of mod_and , mod_or , mod_xor and where i and j are signed integer literals (i.e., either numeric literals or of the form $\sim k$, where k is a numeric literal). The resulting theorem has conclusion $i \text{ op } j = r$, where r is the signed literal resulting from evaluating the expression.

Errors

```

|509021?0 is not of the form  $\lfloor \frac{z}{2} i \text{ mod\_and } j \rfloor$  where  $\lfloor \frac{z}{2} i \rfloor$  and  $\lfloor \frac{z}{2} j \rfloor$  are numeric literals
|509022?0 is not of the form  $\lfloor \frac{z}{2} i \text{ mod\_or } j \rfloor$  where  $\lfloor \frac{z}{2} i \rfloor$  and  $\lfloor \frac{z}{2} j \rfloor$  are numeric literals
|509023?0 is not of the form  $\lfloor \frac{z}{2} i \text{ mod\_xor } j \rfloor$  where  $\lfloor \frac{z}{2} i \rfloor$  and  $\lfloor \frac{z}{2} j \rfloor$  are numeric literals
|509024?0 is not of the form  $\lfloor \frac{z}{2} \text{ mod\_not } i \rfloor$  where  $\lfloor \frac{z}{2} i \rfloor$  is a numeric literal

```

SML

```

|(* Proof Context: 'cn *)
|(* Proof Context: 'cn1 *)

```

Description Component proof context for the theory cn which supports the Compliance Notation. $'cn1$ is a slightly improved version of the original $'cn$.

The main purpose of these proof contexts is to automate the elimination of the vocabulary of the theory cn in favour of plain Z toolkit constructs wherever this is possible without introducing excessive complexity.

Expressions and predicates treated by the proof contexts are constructs formed from:

```

|not -, - and -, - or -, - xor -, , - and_then -, - or_else -,
|_ mem -, - notmem -, - eq -, - noteq -,
|_ less -, - less_eq -, - greater -, - greater_eq -,
|_ intdiv -, - rem -, - ** -, - mod_and -, - mod_or -,
|_ mod_xor -, - mod_not

```

Contents

Rewriting:

```

|cn_boolean_thm (cn only), cn_boolean_succ_thm, cn_boolean_pred_thm,
|cn_boolean_pos_thm, cn_boolean_val_thm,
|cn_boolean_clauses, cn_relational_clauses,
|cn_intdiv_conv, cn_rem_conv, cn_intmod_conv, cn_star_star_conv
|cn_boolean_∈_boolean_thm (cn1 only)
|z_size_dot_dot_conv (cn1 only)
|cn_and_then_or_else_clauses

```

Stripping theorems: (none)

See Also cn , cn_ext

SML

```
|(* Proof Context: 'cn_reals *)
```

Description Component proof context for the theory *cn* which supports the Compliance Notation treatment of Ada fixed and floating point types.

The purpose of the proof context is to automate the elimination of the vocabulary of the theory *cn* concerned with real numbers in favour of plain Z toolkit constructs wherever this is possible without introducing excessive complexity.

Expressions and predicates treated by the proof contexts are constructs formed from the Z real arithmetic operators and the Compliance Notation operators *_e_*, *integer_to_real* and *integer_to_real*.

This proof context will typically be used in conjunction with one of the other Compliance Notation proof contexts and the proof context for the Z real numbers. E.g.,

```
|set_merge_pcs["'cn_reals", "'z_reals", "cn"];
```

Contents

Rewriting:

```
|cn_e_0_thm
```

```
|cn_relational_clauses1
```

```
|cn_integer_to_real_thm
```

Stripping theorems:

```
|cn_relational_clauses1
```

See Also *'cn*, *'cn1*

SML

```
|(* Proof Context: cn *)
```

```
|(* Proof Context: cn_ext *)
```

```
|(* Proof Context: cn1 *)
```

```
|(* Proof Context: cn1_ext *)
```

Description Complete proofs context for the theory *cn* which supports the Compliance Notation. *cn1* is the recommended proof context for normal use while reasoning about VCs generated by the compliance tool. *cn* is still provided for backwards compatibility.

cn1 is the merge of the component proof contexts *'cn1* and *z_library1*. *cn1_ext* is the merge of the component proof contexts *'cn* and *z_library1_ext*.

cn is the merge of the component proof contexts *'cn* and *z_library*. *cn_ext* is the merge of the component proof contexts *'cn* and *z_library_ext*.

See Also *'cn1*, *'cn*, *z_library1*, *z_library1_ext*

SML

```
|signature CNTactics = sig
```

Description This is the name of a metalanguage structure containing tactics etc. supporting proof of the VCs generated by the Compliance Tool.

SML

```
|val cn_vc_simp_tac : THM list -> TACTIC;
```

Description This tactic performs simplifications which are often appropriate at the beginning of the proof of a VC goal generated by the Compliance Tool. It should generally be used in the proof context *cn* or *cn1* or in some proof context incorporating one of these. It attempts to carry out the following steps (and fails if none of them succeeds in changing the goal):

1. Rewrite the conclusion of the goal with: the theorems supplied in the parameter (if any); the rewriting rules built into the current proof context; and, *z_plus_assoc_thm* and *z_times_assoc_thm*.
2. Remove any outer universal quantifiers (using *z_∀_tac*), typically leaving an implication whose operands are conjunctions.
3. Remove any redundant conjuncts from the result of step 2. At this stage, the goal will be proved automatically if the antecedents of the implication subsume the succedents.

For example, using the proof context *cn1*, *cn_vc_simp_tac*, will transform the goal:

```
?|-   ∀      x : INTEGER; y : INTEGER; z : INTEGER
      |      (x + y) + 1 eq z = TRUE ∧ (x ≥ 0 ∧ y ≥ 0) ∧ x ≥ 0
      |      •      x ≥ 0 ∧ z greater_eq 0 = TRUE
```

into:

```
?|-      x ∈ INTEGER ∧ y ∈ INTEGER ∧ z ∈ INTEGER
      ∧      x + y + 1 = z ∧ 0 ≤ x ∧ 0 ≤ y
      ⇒      0 ≤ z
```

Errors

```
|518003 Could not simplify the goal ?0
```

SML

```
val current_pc_z_∈_net : unit -> (TERM -> THM) NET
val set_pc_z_∈_rules : (TERM * (TERM -> THM)) list ->
                        string -> unit;
val get_pc_z_∈_rules : string ->
                        ((TERM * (TERM -> THM)) list * string) list;
val pc_z_∈_rules_of_thms : string list ->
                        THM list -> (TERM * (TERM -> THM)) list;
```

Description These are tools used to construct proof contexts for use in conjunction with the tactic *cn_hc_tac*.

SML

```
| val cn_∈_type_tac : THM list -> TACTIC
```

Description This is a tactic for proving assertions of the form $e \in T$ where T is the representation in Z of a SPARK type and e is the translation into Z of a SPARK expression.

The heuristic approach used by the tactic works best with the proof contexts that are generated by *cn_make_script_support*. It will also work with the proof context *cn1* if only predefined SPARK types are involved.

The tactic uses the assumptions of the goal, together with any theorems supplied as an argument as ground facts in an attempt to reduce assertions of the form $e \in T$ according to the structure of e . For example, an assertion of the form $f(x) \in T$ is reduced to the two assertions $f \in X \rightarrow T$ and $x \in X$ and, if the ground facts provide a suitable candidate for the variable X , then the original assertion is proved.

For example, given the SPARK declarations:

```
| type ABC is (A, B, C);
| type BYTE is range 0 .. 255;
| type MARKED_BYTE is record B : BYTE; F : BOOLEAN; end record;
| type ARR is array (ABC) of MARKED_BYTE;
```

cn_∈_type_tac will prove goals such as:

```
| W ∈ ARR ; I ∈ ABC ?⊢ (W I).B ∈ BYTE
```

SML

```
| signature CNTheoryProofSupport = sig
```

Description This is the signature for the Theory Proof Support Tools.

SML

```
|val z_norm_sig_h_schema_conv : CONV;
```

Description Given an arbitrary horizontal schema, $\overline{z}[sig|body]^{\top}$, this conversion will return the theorem that states that the original horizontal schema is equal to one with the signature variables placed in *z_sig_order* in a list of declarations (SDECLs) each with only a single variable. Items in the signature that are not simple declarations will be placed as a list after the sorted simple declarations, in their original order of occurrence. E.g.

```
|[x, z: X; y : Y; T; v, w :Z; S | (v, w, x, z, y, w) ∈ p ]
|will become
|[ v : Z; w : Z; x : X; y : Y; z : X; T; S | (v, w, x, z, y, w) ∈ p ]
```

Conversion

$$\frac{}{\vdash [sig | pred] = [sig' | pred]} \quad z_norm_sig_h_schema_conv \quad \overline{z}[sig | pred]^{\top}$$

and

Conversion

$$\frac{}{\vdash [sig] = [sig']} \quad z_norm_sig_h_schema_conv \quad \overline{z}[sig]^{\top}$$

where *sig'* is the normalised form of sig. It is possible for this conversion to cause no change.

Errors

```
|47940 ?0 is not a Z schema
```

SML

```
|val cn_simplify_canon : THM -> THM list;
```

Description This does some Compliance Notation-specific simplifications, if the appropriate definitions are available. In the following examples the likely constant name suffixes are used, but are not required by the canonicalisation function.

```
|<math>\vdash namevSUCC = (name \setminus \{namevLAST\}) \triangleleft succ</math>
```

```
  becomes  $\vdash \forall i : name_1 .. name_2 + \sim 1 \bullet namevSUCC i = i + 1$ 
```

```
  and  $\vdash namevSUCC \in name_1 .. (name_2 + \sim 1) \rightarrow name_1 + 1 .. name_2$ 
```

```
  where  $name_1$  is the bottom, and  $name_2$  is the top expression of the  
  range that is translated from  $name$ 
```

```
|<math>\vdash namevPRED = namevSUCC \sim</math>
```

```
  becomes  $\vdash \forall i : name_1 + 1 .. name_2 \bullet namevPRED i = i + \sim 1$ 
```

```
  and  $\vdash namevPRED \in name_1 + 1 .. name_2 \rightarrow name_1 .. (name_2 + \sim 1)$ 
```

```
  where  $name_1$  is the bottom, and  $name_2$  is the top expression of the  
  range that is translated from  $name$ 
```

```
|<math>\vdash namevPOS = id\ name</math>
```

```
  becomes  $\vdash \forall i : name \bullet namevPOS i = i$ 
```

```
  and  $\vdash namevPOS \in name \rightarrow name$ 
```

```
|<math>\vdash namevVAL = namevPOS \sim</math>
```

```
  becomes  $\vdash \forall i : name \bullet namevVAL i = i$ 
```

```
  and  $\vdash namevVAL \in name \rightarrow name$ 
```

```
|<math>\vdash name = \{u : master \mid dom\ u = index\}</math>
```

```
  becomes  $\vdash name = (index \rightarrow comp) \cap master$ 
```

```
|<math>\vdash name = [sig]</math>
```

```
  becomes  $\vdash name = [sig']$ 
```

```
  where  $sig'$  is the normalised form of  $sig$ , via  $z\_norm\_sig\_z\_schema\_conv$ 
```

```
|<math>\vdash name = [sig \mid pred]</math>
```

```
  becomes  $\vdash name = [sig' \mid pred]$ 
```

```
  where  $sig'$  is the normalised form of  $sig$ , via  $z\_norm\_sig\_z\_schema\_conv$ 
```

The canonicalisation may also do some limited simplification to theorems that partially match the patterns given above. If none of simplifications apply then the canon returns the empty list.

Errors

```
|517006 Theory cn is not an ancestor of the current theory
```

SML

```
val cn_script_support_thms : string -> THM list;
val list_cn_script_support_thms : string list -> THM list;
```

Description *cn_script_support_thms* takes a theory name as argument. Assuming the theory is in scope it will examine each of the definitions and axioms in the theory that declare generic variables, and perhaps save some theorems for each in the current theory, as below.

As a default Z paragraphs saved as axioms or definitions initially will be processed as if retrieved by *z_get_spec*, and rewritten according to the rules of proof context *z_library1*. Some of the paragraph forms can define more than one value, and the function will save theorems for each of the values defined. The description below assumes that a single item is defined, called “name”, with th obvious extensions for multiple declarations. Non-alphanumeric names may be normalised to fit in with ML variable name rules.

Abbreviation Definitions If *cn_simplify_canon* applies, then its results will be saved. The first theorem, if any, returned by *cn_simplify_canon* will be saved with name *cn_name_thm*; the second theorem returned will be saved with name *cn_name_sig_thm*; and if it returns no theorems then the generic form of the theorem will be saved with name *cn_name_thm*.

Free Type Definitions The signature part will be saved with name *cn_name_sig_thm* (for each constructor); the body, if non-trivial, with name *cn_name_thm* (for each constructor). A theorem for free type name, of the form *name = U*, will be saved with name *cn_name_sig_thm*.

Axiomatic Box The signature part will be saved with name *cn_name_sig_thm*, the body, if non-trivial, with name *cn_name_thm*. No theorem will be saved if there is no signature.

Given Sets A theorem stating that the given set is equal to U will be saved with name *cn_name_thm*.

Constraint No theorem will be generated, as constraints do not define a generic variable (though they may well constrain an existing variable).

Saved theorems will also be bound to an ML variable of the same name. If a theorem is already saved with the chosen theorem name then it is assumed to be for an earlier use of the support tool, and will be replaced, and a warning issued. Finally, a list of the theorems generated will be returned by the function.

list_cn_script_support_thms acts like *cn_script_support_thms* mapped over a list of theory names, except that duplicate names in the list will be ignored.

See Also *cn_make_script_support*, *list_cn_spec_rule*, *cn_spec_rule*,

Errors

```
517001 Theorem ?0 in theory ?1 has been replaced by a new Compliance Notation
      support theorem
517002 Cannot force the saving of theorem ?0 in theory ?1
517006 Theory cn is not an ancestor of the current theory
517007 LHS of Abbreviation Definition, ?0, not a generic variable
```


SML

```
|val list_cn_spec_rule : THM list -> THM list;
```

Description *list_cn_spec_rule* takes a list of theorems and analyses those that are Z paragraphs as if by *cn_script_support_thms*, saving and returning the list of created theorems.

Note that in particular this means that the function can be applied to the results of *get_spec*, but not those of *z_get_spec*.

Errors

```
|517001 Theorem ?0 in theory ?1 has been replaced by a new Compliance Notation
      support theorem
|517002 Cannot force the saving of theorem ?0 in theory ?1
|517006 Theory cn is not an ancestor of the current theory
```

SML

```
|val cn_spec_rule : THM -> (string * THM) list;
```

Description *cn_spec_rule* analyses its single theorem as if by *cn_script_support_thms*, except that it does not save the resulting theorem, or bind it to an ML variable. The function instead returns the name or names that *cn_script_support_thms* would have used, and the theorem or theorems it would have saved, except that unchanged theorems will result in an empty list being returned.

Errors

```
|517004 Theorem ?0 cannot be processed by Compliance Notation simplification tools
|517006 Theory cn is not an ancestor of the current theory
```

SML

```
|val cn_make_script_support : string -> string -> THM list;  
|val list_cn_make_script_support : string list -> string -> THM list;
```

Description *cn_make_script_support thyname pname* will first try to act as *cn_script_support_thms thyname*. It will then create a complete proof context called *pname* (any previous version will be overwritten). As with *cn_make_script_support_thms*, it returns the list of theorems generated.

list_cn_make_script_support thynames pname will first try to act as *list_cn_script_support_thms thynames*. It will then create a complete proof context called *pname* (any previous version will be overwritten).

The proof context created will be “cn1” extended by:

Contents Rewriting: all saved support theorems (except equations with U);

Stripping theorems: bi-implications from support theorems, plus support theorem equations of sets promoted to bi-implications of memberships (except where U elimination will apply).

Stripping conclusions: as stripping conclusions.

Rewriting canonicalisation: no change;

Automatic proof procedures: appropriate support theorems will be noted as to be used in type inference tools, otherwise no change.

Existence prover: no change.

The U simplification material will be extended by appending to the equation context the one formed from the equations of the form $nm = U$, via *set_u_simp_eqn_cxt*.

Usage Notes The new proof context requires theory “cn”, its creation theory and theory *thyname* to be in scope. It is not intended to be mixed with HOL proof contexts.

The new proof context is built using proof context “cn1”.

Errors

```
|517001 Theorem ?0 in theory ?1 has been replaced by a new Compliance Notation  
|      support theorem  
|517002 Cannot force the saving of theorem ?0 in theory ?1  
|517006 Theory cn is not an ancestor of the current theory
```

SML

```
|val all_cn_make_script_support : string -> THM list;
```

Description *all_cn_make_script_support pname* will determine all the theories that are parents of the current one, but that are not also ancestors of “cn” (i.e. this does not include “cn”), and will execute *list_cn_make_script_support* on this list.

That is, this will create supporting theorems and a proof context based on all the in scope Compliance Notation packages.

Usage Notes The new proof context requires theory “cn”, its creation theory and theory *thyname* to be in scope. It is not intended to be mixed with HOL proof contexts.

The new proof context is built using proof context “cn1”.

Errors

```
|517006 Theory cn is not an ancestor of the current theory
```

7.3 THE Z THEORY cn

For brevity, the global variables *array_agg6*, *array_agg7*, ..., *array_agg20* have been suppressed from the theory listing.

7.3.1 Parents

z_reals z_library

7.3.2 Global Variables

FALSE	\mathbb{Z}
TRUE	\mathbb{Z}
BOOLEAN	$\mathbb{P} \mathbb{Z}$
BOOLEAN_vFIRST	\mathbb{Z}
BOOLEAN_vLAST	\mathbb{Z}
BOOLEAN_vSUCC	$\mathbb{Z} \leftrightarrow \mathbb{Z}$
BOOLEAN_vPRED	$\mathbb{Z} \leftrightarrow \mathbb{Z}$
BOOLEAN_vPOS	$\mathbb{Z} \leftrightarrow \mathbb{Z}$
BOOLEAN_vVAL	$\mathbb{Z} \leftrightarrow \mathbb{Z}$
(- and -)	$\mathbb{Z} \times \mathbb{Z} \leftrightarrow \mathbb{Z}$
(- or -)	$\mathbb{Z} \times \mathbb{Z} \leftrightarrow \mathbb{Z}$
(- xor -)	$\mathbb{Z} \times \mathbb{Z} \leftrightarrow \mathbb{Z}$
(not -)	$\mathbb{Z} \leftrightarrow \mathbb{Z}$
(- and_then -)	$\mathbb{Z} \times \mathbb{Z} \leftrightarrow \mathbb{Z}$
(- or_else -)	$\mathbb{Z} \times \mathbb{Z} \leftrightarrow \mathbb{Z}$
(- eq -)[X]	$X \times X \leftrightarrow \mathbb{Z}$
(- noteq -)[X]	$X \times X \leftrightarrow \mathbb{Z}$
(- mem -)[X]	$X \times \mathbb{P} X \leftrightarrow \mathbb{Z}$
(- notmem -)[X]	$X \times \mathbb{P} X \leftrightarrow \mathbb{Z}$
(- array_and -)[X]	$(X \leftrightarrow \mathbb{Z}) \times (X \leftrightarrow \mathbb{Z}) \leftrightarrow X \leftrightarrow \mathbb{Z}$
(- array_or -)[X]	$(X \leftrightarrow \mathbb{Z}) \times (X \leftrightarrow \mathbb{Z}) \leftrightarrow X \leftrightarrow \mathbb{Z}$
(- array_xor -)[X]	$(X \leftrightarrow \mathbb{Z}) \times (X \leftrightarrow \mathbb{Z}) \leftrightarrow X \leftrightarrow \mathbb{Z}$
(array_not -)[X]	$(X \leftrightarrow \mathbb{Z}) \leftrightarrow X \leftrightarrow \mathbb{Z}$
(- less -)	$\mathbb{Z} \times \mathbb{Z} \leftrightarrow \mathbb{Z}$
(- less_eq -)	$\mathbb{Z} \times \mathbb{Z} \leftrightarrow \mathbb{Z}$
(- greater -)	$\mathbb{Z} \times \mathbb{Z} \leftrightarrow \mathbb{Z}$
(- greater_eq -)	$\mathbb{Z} \times \mathbb{Z} \leftrightarrow \mathbb{Z}$

(- real_less -) $\mathbb{R} \times \mathbb{R} \leftrightarrow \mathbb{Z}$
 (- real_less_eq -) $\mathbb{R} \times \mathbb{R} \leftrightarrow \mathbb{Z}$
 (- real_greater -) $\mathbb{R} \times \mathbb{R} \leftrightarrow \mathbb{Z}$
 (- real_greater_eq -) $\mathbb{R} \times \mathbb{R} \leftrightarrow \mathbb{Z}$
 (- array_less -) $(\mathbb{Z} \leftrightarrow \mathbb{Z}) \times (\mathbb{Z} \leftrightarrow \mathbb{Z}) \leftrightarrow \mathbb{Z}$
 (- array_less_eq -) $(\mathbb{Z} \leftrightarrow \mathbb{Z}) \times (\mathbb{Z} \leftrightarrow \mathbb{Z}) \leftrightarrow \mathbb{Z}$
 (- array_greater -) $(\mathbb{Z} \leftrightarrow \mathbb{Z}) \times (\mathbb{Z} \leftrightarrow \mathbb{Z}) \leftrightarrow \mathbb{Z}$
 (- array_greater_eq -) $(\mathbb{Z} \leftrightarrow \mathbb{Z}) \times (\mathbb{Z} \leftrightarrow \mathbb{Z}) \leftrightarrow \mathbb{Z}$
 (- intdiv -) $\mathbb{Z} \times \mathbb{Z} \leftrightarrow \mathbb{Z}$
 (- rem -) $\mathbb{Z} \times \mathbb{Z} \leftrightarrow \mathbb{Z}$
 (- intmod -) $\mathbb{Z} \times \mathbb{Z} \leftrightarrow \mathbb{Z}$
 (- ** -) $\mathbb{Z} \times \mathbb{Z} \leftrightarrow \mathbb{Z}$
integer_to_real $\mathbb{Z} \leftrightarrow \mathbb{R}$
real_to_integer $\mathbb{R} \leftrightarrow \mathbb{Z}$
 (- mod_and -) $\mathbb{Z} \times \mathbb{Z} \leftrightarrow \mathbb{Z}$
 (- mod_or -) $\mathbb{Z} \times \mathbb{Z} \leftrightarrow \mathbb{Z}$
 (- mod_xor -) $\mathbb{Z} \times \mathbb{Z} \leftrightarrow \mathbb{Z}$
 (mod_not -) $\mathbb{Z} \times \mathbb{Z} \leftrightarrow \mathbb{Z}$
INTEGER $\mathbb{P} \mathbb{Z}$
INTEGERvSUCC $\mathbb{Z} \leftrightarrow \mathbb{Z}$
INTEGERvPRED $\mathbb{Z} \leftrightarrow \mathbb{Z}$
INTEGERvPOS $\mathbb{Z} \leftrightarrow \mathbb{Z}$
INTEGERvVAL $\mathbb{Z} \leftrightarrow \mathbb{Z}$
INTEGERvFIRST
 \mathbb{Z}
INTEGERvLAST \mathbb{Z}
NATURAL $\mathbb{P} \mathbb{Z}$
NATURALvFIRST
 \mathbb{Z}
NATURALvLAST \mathbb{Z}
NATURALvSUCC $\mathbb{Z} \leftrightarrow \mathbb{Z}$
NATURALvPRED $\mathbb{Z} \leftrightarrow \mathbb{Z}$
NATURALvPOS $\mathbb{Z} \leftrightarrow \mathbb{Z}$
NATURALvVAL $\mathbb{Z} \leftrightarrow \mathbb{Z}$
POSITIVE $\mathbb{P} \mathbb{Z}$
POSITIVEvFIRST
 \mathbb{Z}
POSITIVEvLAST
 \mathbb{Z}

POSITIVEvSUCC
 $\mathbb{Z} \leftrightarrow \mathbb{Z}$
POSITIVEvPRED
 $\mathbb{Z} \leftrightarrow \mathbb{Z}$
POSITIVEvPOS $\mathbb{Z} \leftrightarrow \mathbb{Z}$
POSITIVEvVAL $\mathbb{Z} \leftrightarrow \mathbb{Z}$
LONG_INTEGER $\mathbb{P} \mathbb{Z}$
LONG_INTEGERvSUCC
 $\mathbb{Z} \leftrightarrow \mathbb{Z}$
LONG_INTEGERvPRED
 $\mathbb{Z} \leftrightarrow \mathbb{Z}$
LONG_INTEGERvPOS
 $\mathbb{Z} \leftrightarrow \mathbb{Z}$
LONG_INTEGERvVAL
 $\mathbb{Z} \leftrightarrow \mathbb{Z}$
LONG_INTEGERvFIRST
 \mathbb{Z}
LONG_INTEGERvLAST
 \mathbb{Z}
SHORT_INTEGER
 $\mathbb{P} \mathbb{Z}$
SHORT_INTEGERvSUCC
 $\mathbb{Z} \leftrightarrow \mathbb{Z}$
SHORT_INTEGERvPRED
 $\mathbb{Z} \leftrightarrow \mathbb{Z}$
SHORT_INTEGERvPOS
 $\mathbb{Z} \leftrightarrow \mathbb{Z}$
SHORT_INTEGERvVAL
 $\mathbb{Z} \leftrightarrow \mathbb{Z}$
SHORT_INTEGERvFIRST
 \mathbb{Z}
SHORT_INTEGERvLAST
 \mathbb{Z}
universal_discrete
 $\mathbb{P} \mathbb{Z}$
universal_discretevSUCC
 $\mathbb{Z} \leftrightarrow \mathbb{Z}$
universal_discretevPRED
 $\mathbb{Z} \leftrightarrow \mathbb{Z}$
universal_discretevPOS
 $\mathbb{Z} \leftrightarrow \mathbb{Z}$
universal_discretevVAL
 $\mathbb{Z} \leftrightarrow \mathbb{Z}$
universal_discretevFIRST
 \mathbb{Z}
universal_discretevLAST
 \mathbb{Z}
FLOAT $\mathbb{P} \mathbb{R}$
FLOATvDIGITS \mathbb{Z}
FLOATvFIRST \mathbb{R}
FLOATvLAST \mathbb{R}

SHORT_FLOAT	$\mathbb{P} \mathbb{R}$
SHORT_FLOAT_vDIGITS	\mathbb{Z}
SHORT_FLOAT_vFIRST	\mathbb{R}
SHORT_FLOAT_vLAST	\mathbb{R}
LONG_FLOAT	$\mathbb{P} \mathbb{R}$
LONG_FLOAT_vDIGITS	\mathbb{Z}
LONG_FLOAT_vFIRST	\mathbb{R}
LONG_FLOAT_vLAST	\mathbb{R}
CHARACTER_vFIRST	\mathbb{Z}
CHARACTER_vLAST	\mathbb{Z}
CHARACTER	$\mathbb{P} \mathbb{Z}$
CHARACTER_vSUCC	$\mathbb{Z} \leftrightarrow \mathbb{Z}$
CHARACTER_vPRED	$\mathbb{Z} \leftrightarrow \mathbb{Z}$
CHARACTER_vPOS	$\mathbb{Z} \leftrightarrow \mathbb{Z}$
CHARACTER_vVAL	$\mathbb{Z} \leftrightarrow \mathbb{Z}$
STRING	$\mathbb{P} (\mathbb{Z} \leftrightarrow \mathbb{Z})$
Z_CHAR	$\mathbb{P} (\mathbb{Z} \leftrightarrow \mathbb{S})$
Z_STRING	$\mathbb{P} (\mathbb{Z} \leftrightarrow \mathbb{S})$
dest_char	$\mathbb{S} \leftrightarrow \mathbb{Z}$
(string_lit _)	$(\mathbb{Z} \leftrightarrow \mathbb{S}) \leftrightarrow \mathbb{Z} \leftrightarrow \mathbb{Z}$
(char_lit _)	$(\mathbb{Z} \leftrightarrow \mathbb{S}) \leftrightarrow \mathbb{Z}$
Informal_Function	\mathbb{P} <i>Informal_Function</i>
(- &_2 -)[X]	$X \times (\mathbb{Z} \leftrightarrow X) \leftrightarrow \mathbb{Z} \leftrightarrow X$
(- &_1 -)[X]	$(\mathbb{Z} \leftrightarrow X) \times X \leftrightarrow \mathbb{Z} \leftrightarrow X$
(- &_0 -)[X]	$(\mathbb{Z} \leftrightarrow X) \times (\mathbb{Z} \leftrightarrow X) \leftrightarrow \mathbb{Z} \leftrightarrow X$
slide[X, Y]	$(X \leftrightarrow Y) \times \mathbb{P} X \leftrightarrow X \leftrightarrow Y$
Boolean	$\mathbb{B} \leftrightarrow \mathbb{Z}$
VRElsfInd	\mathbb{P} <i>VRElsfInd</i>
VRElsfTrue	<i>VRElsfInd</i>
VRElsfFalse	<i>VRElsfInd</i>
VC_Route	\mathbb{P} <i>VC_Route</i>
(VRAny _)	<i>VC_Route</i> \leftrightarrow <i>VC_Route</i>
(VRNull _)	<i>VC_Route</i> \leftrightarrow <i>VC_Route</i>
(VRAssign _)	<i>VC_Route</i> \leftrightarrow <i>VC_Route</i>
(VRSpecVia _)	<i>VC_Route</i> \leftrightarrow <i>VC_Route</i>
(VRSpecToSide _)	<i>VC_Route</i> \leftrightarrow <i>VC_Route</i>

$VC_Route \leftrightarrow VC_Route$

VRSpecPreIntro
 VC_Route
(VRSemi _) $VC_Route \leftrightarrow VC_Route$
(VREndSemi _)
 $VC_Route \leftrightarrow VC_Route$
(VRIfThen _) $VRElfsInd \times VC_Route \leftrightarrow VC_Route$
(VRIfElse _) $VRElfsInd \times VC_Route \leftrightarrow VC_Route$
(VREndIf _) $VC_Route \leftrightarrow VC_Route$
(VRCaseBranch _)
 $\mathbb{Z} \times VC_Route \leftrightarrow VC_Route$
(VRCaseOthers _)
 $VC_Route \leftrightarrow VC_Route$
(VREndCase _)
 $VC_Route \leftrightarrow VC_Route$
(VRLoopUndecVia _)
 $VC_Route \leftrightarrow VC_Route$

VRLoopUndecPreIntro
 VC_Route

VRLoopUndecPreToSide
 VC_Route
(VRLoopUndecToSide _)
 $VC_Route \leftrightarrow VC_Route$

(VRWhileVia _)
 $VC_Route \leftrightarrow VC_Route$

VRWhilePreIntro
 VC_Route
(VRWhileWPToSide _)
 $VC_Route \leftrightarrow VC_Route$
(VRWhileToSide _)
 $VC_Route \leftrightarrow VC_Route$
(VRForVia _) $VC_Route \leftrightarrow VC_Route$

VRForPreIntro
 VC_Route

VRForPreToSide
 VC_Route
(VRForToSide _)
 $VC_Route \leftrightarrow VC_Route$
(VRForExitToSide _)
 $VC_Route \leftrightarrow VC_Route$

VRExitTillIntro
 $(\mathbb{Z} \leftrightarrow \mathbb{S}) \leftrightarrow VC_Route$
(VRExitVia _)
 $VC_Route \leftrightarrow VC_Route$

VRReturnIntro
 VC_Route
(VRProcCall _)
 $VC_Route \leftrightarrow VC_Route$
(VRProcCallEnd _)
 $VC_Route \leftrightarrow VC_Route$

VRProcCallRngIntro

```

          VC_Route
(VRLogConToSide _)
          VC_Route ↔ VC_Route
VRLogConPreIntro
          VC_Route
VRLogConTypeMemIntro
          VC_Route
(VRRefinementBegin _)
          VC_Route ↔ VC_Route
VRRefinementIntro
          VC_Route
array_agg2[g1, g2, g]
          (g1 ↔ g2 ↔ g) ↔ g1 × g2 ↔ g
array_agg3[g1, g2, g3, g]
          (g1 ↔ g2 ↔ g3 ↔ g) ↔ g1 × g2 × g3 ↔ g
array_agg4[g1, g2, g3, g4, g]
          (g1 ↔ g2 ↔ g3 ↔ g4 ↔ g) ↔ g1 × g2 × g3 × g4 ↔ g
array_agg5[g1, g2, g3, g4, g5, g]
          (g1 ↔ g2 ↔ g3 ↔ g4 ↔ g5 ↔ g)
          ↔ g1 × g2 × g3 × g4 × g5 ↔ g

```

7.3.3 Fixity

fun 0 rightassoc

```

(char_lit _) (VRIfElse _) (VRWhileVia _)
(string_lit _) (VRIfThen _) (VRWhileWPToSide _)
(VRAny _) (VRLogConToSide _) (_ and _)
(VRAssign _) (VRLoopUndecToSide _) (_ and_then _)
(VRCaseBranch _) (VRLoopUndecVia _) (_ array_and _)
(VRCaseOthers _) (VRNull _) (_ array_or _)
(VREndCase _) (VRProcCallEnd _) (_ array_xor _)
(VREndIf _) (VRProcCall _) (_ mod_and _)
(VREndSemi _) (VRRefinementBegin _) (_ mod_or _)
(VRExitVia _)(VRSemi _) (_ mod_xor _)
(VRForExitToSide _) (VRSpecToSide _) (_ or _)
(VRForToSide _) (VRSpecVia _) (_ or_else _)
(VRForVia _) (VRWhileToSide _) (_ xor _)

```

fun 10 rightassoc

```

(- eq _) (- mem _) (- noteq _) (- notmem _)

```

fun 20 rightassoc

```

(- array_greater _) (- less _)
(- array_greater_eq _) (- less_eq _)
(- array_less _) (- real_greater _)
(- array_less_eq _) (- real_greater_eq _)
(- greater _) (- real_less _)
(- greater_eq _) (- real_less_eq _)

```


fun 30 rightassoc

$$(- \&_0 -) \quad (- \&_1 -) \quad (- \&_2 -)$$

fun 40 rightassoc

$$(- \text{intdiv } -) \quad (- \text{intmod } -) \quad (- \text{rem } -)$$

fun 50 rightassoc

$$(\text{array_not } -) \quad (\text{mod_not } -) \quad (\text{not } -)(- ** -)$$

7.3.4 Axioms

- **and** -

- **or** -

- **xor** -

not -

$$\begin{aligned} &\vdash ((\text{not } -) \in \text{BOOLEAN} \rightarrow \text{BOOLEAN}) \\ &\quad \wedge \{(- \text{and } -), (- \text{or } -), (- \text{xor } -)\} \\ &\quad \subseteq \text{BOOLEAN} \times \text{BOOLEAN} \rightarrow \text{BOOLEAN}) \\ &\quad \wedge (\forall b : \text{BOOLEAN} \\ &\quad \bullet \text{not } \text{FALSE} = \text{TRUE} \\ &\quad \quad \wedge \text{not } \text{TRUE} = \text{FALSE} \\ &\quad \quad \wedge (b \text{ and } \text{FALSE} = \text{FALSE}) \\ &\quad \quad \wedge (b \text{ and } \text{TRUE} = b) \\ &\quad \quad \wedge (b \text{ or } \text{FALSE} = b) \\ &\quad \quad \wedge (b \text{ or } \text{TRUE} = \text{TRUE}) \\ &\quad \quad \wedge (b \text{ xor } \text{FALSE} = b) \\ &\quad \quad \wedge (b \text{ xor } \text{TRUE} = \text{not } b) \end{aligned}$$

- **and_then** -

- **or_else** -

$$\begin{aligned} &\vdash \{(- \text{and_then } -), (- \text{or_else } -)\} \\ &\quad \subseteq \text{BOOLEAN} \times \text{BOOLEAN} \rightarrow \text{BOOLEAN} \\ &\quad \wedge (- \text{and_then } -) = (- \text{and } -) \\ &\quad \wedge (- \text{or_else } -) = (- \text{or } -) \end{aligned}$$

- **eq** -

- **noteq** -

- **mem** -

- **notmem** -

$$\begin{aligned} &\vdash [X]((\{(- \text{mem } -)[X], (- \text{notmem } -)[X]\} \\ &\quad \subseteq X \times \mathbb{P} X \rightarrow \text{BOOLEAN}) \\ &\quad \wedge \{(- \text{eq } -)[X], (- \text{noteq } -)[X]\} \subseteq X \times X \rightarrow \text{BOOLEAN}) \\ &\quad \wedge (\forall x, y : X; S : \mathbb{P} X; b : \text{BOOLEAN} \\ &\quad \bullet (b = (- \text{mem } -)[X] (x, S) \Leftrightarrow b = \text{TRUE} \Leftrightarrow x \in S) \\ &\quad \quad \wedge (b = (- \text{notmem } -)[X] (x, S) \\ &\quad \quad \quad \Leftrightarrow b = \text{TRUE} \\ &\quad \quad \quad \Leftrightarrow x \notin S) \\ &\quad \quad \wedge (b = (- \text{eq } -)[X] (x, y) \Leftrightarrow b = \text{TRUE} \Leftrightarrow x = y) \\ &\quad \quad \wedge (b = (- \text{noteq } -)[X] (x, y) \\ &\quad \quad \quad \Leftrightarrow b = \text{TRUE} \\ &\quad \quad \quad \Leftrightarrow x \neq y))) \end{aligned}$$

- **array_and** -

- **array_or** -

- **array_xor** -

array_not -

$$\vdash [X](((\text{array_not } -)[X] \in (X \leftrightarrow \text{BOOLEAN}) \rightarrow X \leftrightarrow \text{BOOLEAN}$$

$$\begin{aligned}
& \wedge \{(- \text{array_and } -)[X], \\
& \quad (- \text{array_or } -)[X], \\
& \quad (- \text{array_xor } -)[X]\} \\
& \subseteq (X \leftrightarrow \text{BOOLEAN}) \times (X \leftrightarrow \text{BOOLEAN}) \rightarrow X \leftrightarrow \text{BOOLEAN} \\
& \wedge (\forall a, b : X \leftrightarrow \text{BOOLEAN} \\
& \quad \bullet (\text{array_not } -)[X] a = (\lambda i : \text{dom } a \bullet \text{not } a i) \\
& \quad \wedge (- \text{array_and } -)[X] (a, b) \\
& \quad = (\lambda i : \text{dom } a \cap \text{dom } b \bullet a i \text{ and } b i) \\
& \quad \wedge (- \text{array_or } -)[X] (a, b) \\
& \quad = (\lambda i : \text{dom } a \cap \text{dom } b \bullet a i \text{ or } b i) \\
& \quad \wedge (- \text{array_xor } -)[X] (a, b) \\
& \quad = (\lambda i : \text{dom } a \cap \text{dom } b \bullet a i \text{ xor } b i))) \\
- \text{less } - \\
- \text{less_eq } - \\
- \text{greater } - \\
- \text{greater_eq } - \\
\vdash \{(- \text{less } -), \\
\quad (- \text{less_eq } -), \\
\quad (- \text{greater } -), \\
\quad (- \text{greater_eq } -)\} \\
\subseteq \mathbb{Z} \times \mathbb{Z} \rightarrow \text{BOOLEAN} \\
\wedge (\forall x, y : \mathbb{Z}; b : \text{BOOLEAN} \\
\quad \bullet (b = x \text{less } y \Leftrightarrow b = \text{TRUE} \Leftrightarrow x < y) \\
\quad \wedge (b = x \text{less_eq } y \Leftrightarrow b = \text{TRUE} \Leftrightarrow x \leq y) \\
\quad \wedge (b = x \text{greater } y \Leftrightarrow b = \text{TRUE} \Leftrightarrow x > y) \\
\quad \wedge (b = x \text{greater_eq } y \Leftrightarrow b = \text{TRUE} \Leftrightarrow x \geq y)) \\
- \text{real_less } - \\
- \text{real_less_eq } - \\
- \text{real_greater } - \\
- \text{real_greater_eq } - \\
\vdash \{(- \text{real_less } -), \\
\quad (- \text{real_less_eq } -), \\
\quad (- \text{real_greater } -), \\
\quad (- \text{real_greater_eq } -)\} \\
\subseteq \mathbb{R} \times \mathbb{R} \rightarrow \text{BOOLEAN} \\
\wedge (\forall x, y : \mathbb{R}; b : \text{BOOLEAN} \\
\quad \bullet (b = x \text{real_less } y \Leftrightarrow b = \text{TRUE} \Leftrightarrow x <_R y) \\
\quad \wedge (b = x \text{real_less_eq } y \Leftrightarrow b = \text{TRUE} \Leftrightarrow x \leq_R y) \\
\quad \wedge (b = x \text{real_greater } y \Leftrightarrow b = \text{TRUE} \Leftrightarrow x >_R y) \\
\quad \wedge (b = x \text{real_greater_eq } y \\
\quad \Leftrightarrow b = \text{TRUE} \\
\quad \Leftrightarrow x \geq_R y)) \\
- \text{array_less } - \\
- \text{array_less_eq } - \\
- \text{array_greater } - \\
- \text{array_greater_eq } - \\
\vdash \{(- \text{array_less } -), \\
\quad (- \text{array_less_eq } -), \\
\quad (- \text{array_greater } -), \\
\quad (- \text{array_greater_eq } -)\} \\
\subseteq (\mathbb{Z} \leftrightarrow \mathbb{Z}) \times (\mathbb{Z} \leftrightarrow \mathbb{Z}) \rightarrow \text{BOOLEAN}
\end{aligned}$$

$$\wedge (\forall a, b : \mathbb{Z} \leftrightarrow \mathbb{Z}$$

- $(a \text{ array_less } b = \text{TRUE}$
 - $\Leftrightarrow (\exists i, j, k : \mathbb{Z}$
 - $\{i, j\} \subseteq \text{dom } b$
 - $\wedge i - 1 \notin \text{dom } b$
 - $\wedge i + k - 1 \notin \text{dom } a$
 - $\wedge (\forall t : i .. j - 1$
 - $t + k \in \text{dom } a \wedge b \ t = a \ (t + k))$
 - $\wedge j + k \in \text{dom } a$
 - $\Rightarrow a \ (j + k) < b \ j))$
- $\wedge a \text{ array_less_eq } b = a \text{ array_less } b \text{ or } a \text{ eq } b$
- $\wedge a \text{ array_greater } b = b \text{ array_less } a$
- $\wedge a \text{ array_greater_eq } b = b \text{ array_less_eq } a)$

- intdiv -

- rem -

- intmod - $\vdash \{(- \text{ intdiv } -), (- \text{ rem } -), (- \text{ intmod } -)\}$

$$\subseteq \mathbb{Z} \times \mathbb{Z} \setminus \{0\} \rightarrow \mathbb{Z}$$

$$\wedge (\forall x, y : \mathbb{Z}$$

- | $y \neq 0$
- $(x * y \geq 0 \Rightarrow x \text{ intdiv } y = \text{abs } x \text{ div } \text{abs } y)$
 - $\wedge (x * y < 0$
 - $\Rightarrow x \text{ intdiv } y = \sim (\text{abs } x \text{ div } \text{abs } y))$
- $\wedge x \text{ rem } y = x - (x \text{ intdiv } y) * y$
- $\wedge (x * y \geq 0 \vee x \text{ rem } y = 0$
 - $\Rightarrow x \text{ intmod } y = x \text{ rem } y)$
- $\wedge (x * y < 0 \wedge x \text{ rem } y \neq 0$
 - $\Rightarrow x \text{ intmod } y = x \text{ rem } y + y))$

- ** - $\vdash (- ** -) \in \mathbb{Z} \times \mathbb{N} \rightarrow \mathbb{Z}$

$$\wedge (\forall x : \mathbb{Z}; y : \mathbb{N}$$

- $x ** 0 = 1 \wedge x ** (y + 1) = x * x ** y)$

integer_to_real

$$\vdash \text{integer_to_real} \in \mathbb{Z} \rightarrow \mathbb{R}$$

$$\wedge (\forall i : \mathbb{Z} \bullet \text{integer_to_real } i = \text{real } i)$$

real_to_integer

$$\vdash \text{real_to_integer} \in \mathbb{R} \rightarrow \mathbb{Z}$$

$$\wedge (\forall x : \mathbb{R}$$

- $\sim_R 0.5 \leq_R x -_R \text{real } (\text{real_to_integer } x)$
- $\wedge x -_R \text{real } (\text{real_to_integer } x) \leq_R 0.5)$

- mod_and -

- mod_or -

- mod_xor - $\vdash \{(- \text{ mod_and } -), (- \text{ mod_or } -), (- \text{ mod_xor } -)\}$

$$\subseteq \mathbb{N} \times \mathbb{N} \rightarrow \mathbb{N}$$

$$\wedge (\forall i : \mathbb{N} \bullet i \text{ mod_and } 0 = 0)$$

$$\wedge (\forall i : \mathbb{N}; j : \mathbb{N}_1$$

- $i \text{ mod_and } j$
 - $= 2 * (i \text{ div } 2 \text{ mod_and } j \text{ div } 2)$
 - $+ i \text{ mod } 2 * (j \text{ mod } 2))$

$$\wedge (\forall i : \mathbb{N} \bullet i \text{ mod_or } 0 = i)$$

$$\wedge (\forall i : \mathbb{N}; j : \mathbb{N}_1$$

- $i \text{ mod_or } j$
 - $= 2 * (i \text{ div } 2 \text{ mod_or } j \text{ div } 2)$

$$\begin{aligned}
& + \max \{i \bmod 2, j \bmod 2\}) \\
& \wedge (\forall i : \mathbb{N} \bullet i \bmod_{\text{xor}} 0 = i) \\
& \wedge (\forall i : \mathbb{N}; j : \mathbb{N}_1 \\
& \quad \bullet i \bmod_{\text{xor}} j \\
& \quad = 2 * (i \text{ div } 2 \bmod_{\text{xor}} j \text{ div } 2) \\
& \quad + (i + j) \bmod 2) \\
\text{mod_not } _ & \vdash (\text{mod_not } _) \in \mathbb{Z} \times \mathbb{Z} \rightarrow \mathbb{Z} \\
& \wedge (\forall i, \text{modulus} : \mathbb{Z} \\
& \quad \bullet \text{mod_not } (i, \text{modulus}) = \text{modulus} - (i + 1)) \\
\text{INTEGER} & \vdash \text{INTEGER} \in \mathbb{P} \mathbb{Z} \wedge \text{true} \\
\text{INTEGERvSUCC} & \\
\text{INTEGERvPRED} & \\
\text{INTEGERvPOS} & \\
\text{INTEGERvVAL} & \\
\text{INTEGERvFIRST} & \\
\text{INTEGERvLAST} & \vdash (\{\text{INTEGERvFIRST}, \text{INTEGERvLAST}\} \subseteq \mathbb{Z} \\
& \wedge \{\text{INTEGERvSUCC}, \\
& \quad \text{INTEGERvPRED}, \\
& \quad \text{INTEGERvPOS}, \\
& \quad \text{INTEGERvVAL}\} \\
& \subseteq \mathbb{Z} \leftrightarrow \mathbb{Z}) \\
& \wedge \text{true} \\
\text{LONG_INTEGER} & \vdash \text{LONG_INTEGER} \in \mathbb{P} \mathbb{Z} \wedge \text{true} \\
\text{LONG_INTEGERvSUCC} & \\
\text{LONG_INTEGERvPRED} & \\
\text{LONG_INTEGERvPOS} & \\
\text{LONG_INTEGERvVAL} & \\
\text{LONG_INTEGERvFIRST} & \\
\text{LONG_INTEGERvLAST} & \vdash (\{\text{LONG_INTEGERvFIRST}, \text{LONG_INTEGERvLAST}\} \subseteq \mathbb{Z} \\
& \wedge \{\text{LONG_INTEGERvSUCC}, \\
& \quad \text{LONG_INTEGERvPRED}, \\
& \quad \text{LONG_INTEGERvPOS}, \\
& \quad \text{LONG_INTEGERvVAL}\} \\
& \subseteq \mathbb{Z} \leftrightarrow \mathbb{Z}) \\
& \wedge \text{true} \\
\text{SHORT_INTEGER} & \vdash \text{SHORT_INTEGER} \in \mathbb{P} \mathbb{Z} \wedge \text{true} \\
\text{SHORT_INTEGERvSUCC} & \\
\text{SHORT_INTEGERvPRED} & \\
\text{SHORT_INTEGERvPOS} & \\
\text{SHORT_INTEGERvVAL} & \\
\text{SHORT_INTEGERvFIRST} & \\
\text{SHORT_INTEGERvLAST} & \vdash (\{\text{SHORT_INTEGERvFIRST}, \text{SHORT_INTEGERvLAST}\} \subseteq \mathbb{Z} \\
& \wedge \{\text{SHORT_INTEGERvSUCC}, \\
& \quad \text{SHORT_INTEGERvPRED}, \\
& \quad \text{SHORT_INTEGERvPOS}, \\
& \quad \text{SHORT_INTEGERvVAL}\} \\
& \subseteq \mathbb{Z} \leftrightarrow \mathbb{Z}) \\
& \wedge \text{true}
\end{aligned}$$

universal_discrete

$\vdash \text{universal_discrete} \in \mathbb{P} \mathbb{Z} \wedge \text{true}$

universal_discretevSUCC

universal_discretevPRED

universal_discretevPOS

universal_discretevVAL

universal_discretevFIRST

universal_discretevLAST

$\vdash (\{\text{universal_discretevFIRST}, \text{universal_discretevLAST}\}$
 $\subseteq \mathbb{Z}$
 $\wedge \{\text{universal_discretevSUCC},$
 $\text{universal_discretevPRED},$
 $\text{universal_discretevPOS},$
 $\text{universal_discretevVAL}\}$
 $\subseteq \mathbb{Z} \leftrightarrow \mathbb{Z})$
 $\wedge \text{true}$

FLOAT $\vdash \text{FLOAT} \in \mathbb{P} \mathbb{R} \wedge \text{true}$

FLOATvDIGITS

FLOATvFIRST

FLOATvLAST $\vdash (\{\text{FLOATvFIRST}, \text{FLOATvLAST}\} \subseteq \mathbb{R}$
 $\wedge \text{FLOATvDIGITS} \in \mathbb{Z})$
 $\wedge \text{true}$

SHORT_FLOAT $\vdash \text{SHORT_FLOAT} \in \mathbb{P} \mathbb{R} \wedge \text{true}$

SHORT_FLOATvDIGITS

SHORT_FLOATvFIRST

SHORT_FLOATvLAST

$\vdash (\{\text{SHORT_FLOATvFIRST}, \text{SHORT_FLOATvLAST}\} \subseteq \mathbb{R}$
 $\wedge \text{SHORT_FLOATvDIGITS} \in \mathbb{Z})$
 $\wedge \text{true}$

LONG_FLOAT $\vdash \text{LONG_FLOAT} \in \mathbb{P} \mathbb{R} \wedge \text{true}$

LONG_FLOATvDIGITS

LONG_FLOATvFIRST

LONG_FLOATvLAST

$\vdash (\{\text{LONG_FLOATvFIRST}, \text{LONG_FLOATvLAST}\} \subseteq \mathbb{R}$
 $\wedge \text{LONG_FLOATvDIGITS} \in \mathbb{Z})$
 $\wedge \text{true}$

CHARACTERvLAST

$\vdash \text{CHARACTERvLAST} \in \mathbb{Z} \wedge \text{CHARACTERvLAST} \geq 127$

STRING $\vdash \text{STRING} \in \mathbb{P} (\text{POSITIVE} \leftrightarrow \text{CHARACTER}) \wedge \text{true}$

dest_char $\vdash \text{dest_char} \in \mathbb{S} \rightarrow \mathbb{Z}$

$\wedge (\forall ch : \mathbb{S} \bullet \text{dest_char } ch = \lceil \text{NZ } (\text{RepChar } ch) \rceil)$

string_lit - $\vdash (\text{string_lit } -) \in \mathbb{Z_STRING} \rightarrow \text{seq CHARACTER}$

$\wedge (\forall str : \mathbb{Z_STRING}$

$\bullet \text{string_lit } str = \text{dest_char} \circ str)$

char_lit - $\vdash (\text{char_lit } -) \in \mathbb{Z_CHAR} \rightarrow \text{CHARACTER}$

$\wedge (\text{char_lit } -) = \text{head} \circ (\text{string_lit } -)$

- &2 -

- &1 -

- &0 -

$\vdash [X](\text{(- \&0 -)}[X] \in (\mathbb{Z} \leftrightarrow X) \times (\mathbb{Z} \leftrightarrow X) \rightarrow \mathbb{Z} \leftrightarrow X$
 $\wedge (\text{- \&1 -})[X] \in (\mathbb{Z} \leftrightarrow X) \times X \rightarrow \mathbb{Z} \leftrightarrow X$
 $\wedge (\text{- \&2 -})[X] \in X \times (\mathbb{Z} \leftrightarrow X) \rightarrow \mathbb{Z} \leftrightarrow X)$

	$\wedge (\forall a, b : \mathbb{Z} \leftrightarrow X; m, n : \mathbb{Z}$ $ \text{dom } a \mapsto m \in \text{max} \wedge \text{dom } b \mapsto n \in \text{min}$ <ul style="list-style-type: none"> • $(- \&_0 -)[X] (a, b)$ $= a \oplus \{i : \text{dom } b \bullet i + m + 1 - n \mapsto b \ i\}$ $\wedge (\forall a : \mathbb{Z} \leftrightarrow X \bullet (- \&_0 -)[X] (a, \emptyset) = a)$ $\wedge (\forall a : \mathbb{Z} \leftrightarrow X; x : X$ <ul style="list-style-type: none"> • $(- \&_1 -)[X] (a, x) = (- \&_0 -)[X] (a, \langle x \rangle)$ $\wedge (\forall a : \mathbb{Z} \leftrightarrow X; x : X$ <ul style="list-style-type: none"> • $(- \&_2 -)[X] (x, a) = (- \&_0 -)[X] (\langle x \rangle, a))$
slide	$\vdash [X,$ $Y](\text{slide}[X, Y] \in (X \leftrightarrow Y) \times \mathbb{P} X \rightarrow X \leftrightarrow Y$ $\wedge (\forall f : X \leftrightarrow Y; r : \mathbb{P} X$ $ \text{dom } f = r$ <ul style="list-style-type: none"> • $\text{slide}[X, Y] (f, r) = f)$
Boolean	$\vdash \text{Boolean} \in \mathbb{B} \rightarrow \text{BOOLEAN}$ $\wedge \text{Boolean } \text{true} = \text{TRUE}$ $\wedge \text{Boolean } \text{false} = \text{FALSE}$
VRElsfTrue	
VRElsfFalse	$\vdash \{ \text{VRElsfTrue}, \text{VRElsfFalse} \} \subseteq \text{VRElsfInd}$ $\wedge \text{disjoint } \langle \{ \text{VRElsfTrue} \}, \{ \text{VRElsfFalse} \} \rangle$ $\wedge (\forall W : \mathbb{P} \text{VRElsfInd}$ $ \{ \text{VRElsfTrue}, \text{VRElsfFalse} \} \subseteq W$ <ul style="list-style-type: none"> • $\text{VRElsfInd} \subseteq W)$
VRAny	-
VRNull	-
VRAssign	-
VRSpecVia	-
VRSpecToSide	-
VRSpecPreIntro	-
VRSemi	-
VREndSemi	-
VRIfThen	-
VRIfElse	-
VREndIf	-
VRCaseBranch	-
VRCaseOthers	-
VREndCase	-
VRLoopUndecVia	-
VRLoopUndecPreIntro	-
VRLoopUndecPreToSide	-
VRLoopUndecToSide	-
VRWhileVia	-
VRWhilePreIntro	-
VRWhileWPToSide	-
VRWhileToSide	-
VRForVia	-
VRForPreIntro	-
VRForPreToSide	-
VRForToSide	-
VRForExitToSide	-
VRExitTillIntro	-

VRExitVia $_$
VRReturnIntro
VRProcCall $_$
VRProcCallEnd $_$
VRProcCallRngIntro
VRLogConToSide $_$
VRLogConPreIntro
VRLogConTypeMemIntro
VRRefinementBegin $_$
VRRefinementIntro

$$\vdash (\{$$

- $VRSpecPreIntro,$
- $VRLoopUndecPreIntro,$
- $VRLoopUndecPreToSide,$
- $VRWhilePreIntro,$
- $VRForPreIntro,$
- $VRForPreToSide,$
- $VRReturnIntro,$
- $VRProcCallRngIntro,$
- $VRLogConPreIntro,$
- $VRLogConTypeMemIntro,$
- $VRRefinementIntro\}$

$$\subseteq VC_Route$$

- $\wedge (VRAny _) \in VC_Route \mapsto VC_Route$
- $\wedge (VRNull _) \in VC_Route \mapsto VC_Route$
- $\wedge (VRAssign _) \in VC_Route \mapsto VC_Route$
- $\wedge (VRSpecVia _) \in VC_Route \mapsto VC_Route$
- $\wedge (VRSpecToSide _) \in VC_Route \mapsto VC_Route$
- $\wedge (VRSemi _) \in VC_Route \mapsto VC_Route$
- $\wedge (VREndSemi _) \in VC_Route \mapsto VC_Route$
- $\wedge (VRIfThen _) \in VRElfsfInd \times VC_Route \mapsto VC_Route$
- $\wedge (VRIfElse _) \in VRElfsfInd \times VC_Route \mapsto VC_Route$
- $\wedge (VREndIf _) \in VC_Route \mapsto VC_Route$
- $\wedge (VRCaseBranch _) \in \mathbb{N}_1 \times VC_Route \mapsto VC_Route$
- $\wedge (VRCaseOthers _) \in VC_Route \mapsto VC_Route$
- $\wedge (VREndCase _) \in VC_Route \mapsto VC_Route$
- $\wedge (VRLoopUndecVia _) \in VC_Route \mapsto VC_Route$
- $\wedge (VRLoopUndecToSide _) \in VC_Route \mapsto VC_Route$
- $\wedge (VRWhileVia _) \in VC_Route \mapsto VC_Route$
- $\wedge (VRWhileWPToSide _) \in VC_Route \mapsto VC_Route$
- $\wedge (VRWhileToSide _) \in VC_Route \mapsto VC_Route$
- $\wedge (VRForVia _) \in VC_Route \mapsto VC_Route$
- $\wedge (VRForToSide _) \in VC_Route \mapsto VC_Route$
- $\wedge (VRForExitToSide _) \in VC_Route \mapsto VC_Route$
- $\wedge VRExitTillIntro \in Z_STRING \mapsto VC_Route$
- $\wedge (VRExitVia _) \in VC_Route \mapsto VC_Route$
- $\wedge (VRProcCall _) \in VC_Route \mapsto VC_Route$
- $\wedge (VRProcCallEnd _) \in VC_Route \mapsto VC_Route$
- $\wedge (VRLogConToSide _) \in VC_Route \mapsto VC_Route$
- $\wedge (VRRefinementBegin _) \in VC_Route \mapsto VC_Route)$
- $\wedge disjoint \langle ran (VRAny _),$
- $ran (VRNull _),$

$$\begin{aligned}
& \text{ran } (VRAssign \ -), \\
& \text{ran } (VRSpecVia \ -), \\
& \text{ran } (VRSpecToSide \ -), \\
& \{VRSpecPreIntro\}, \\
& \text{ran } (VRSemi \ -), \\
& \text{ran } (VREndSemi \ -), \\
& \text{ran } (VRIfThen \ -), \\
& \text{ran } (VRIfElse \ -), \\
& \text{ran } (VREndIf \ -), \\
& \text{ran } (VRCaseBranch \ -), \\
& \text{ran } (VRCaseOthers \ -), \\
& \text{ran } (VREndCase \ -), \\
& \text{ran } (VRLoopUndecVia \ -), \\
& \{VRLoopUndecPreIntro\}, \\
& \{VRLoopUndecPreToSide\}, \\
& \text{ran } (VRLoopUndecToSide \ -), \\
& \text{ran } (VRWhileVia \ -), \\
& \{VRWhilePreIntro\}, \\
& \text{ran } (VRWhileWPToSide \ -), \\
& \text{ran } (VRWhileToSide \ -), \\
& \text{ran } (VRForVia \ -), \\
& \{VRForPreIntro\}, \\
& \{VRForPreToSide\}, \\
& \text{ran } (VRForToSide \ -), \\
& \text{ran } (VRForExitToSide \ -), \\
& \text{ran } VRExitFillIntro, \\
& \text{ran } (VRExitVia \ -), \\
& \{VRReturnIntro\}, \\
& \text{ran } (VRProcCall \ -), \\
& \text{ran } (VRProcCallEnd \ -), \\
& \{VRProcCallRngIntro\}, \\
& \text{ran } (VRLogConToSide \ -), \\
& \{VRLogConPreIntro\}, \\
& \{VRLogConTypeMemIntro\}, \\
& \text{ran } (VRRefinementBegin \ -), \\
& \{VRRefinementIntro\} \\
\wedge (\forall W : \mathbb{P} \ VC_Route \\
& | \{VRSpecPreIntro, \\
& \quad VRLoopUndecPreIntro, \\
& \quad VRLoopUndecPreToSide, \\
& \quad VRWhilePreIntro, \\
& \quad VRForPreIntro, \\
& \quad VRForPreToSide, \\
& \quad VRReturnIntro, \\
& \quad VRProcCallRngIntro, \\
& \quad VRLogConPreIntro, \\
& \quad VRLogConTypeMemIntro, \\
& \quad VRRefinementIntro\} \\
& \cup ((VRAny \ -) (W)) \\
& \quad \cup ((VRNull \ -) (W)) \\
& \quad \cup ((VRAssign \ -) (W))
\end{aligned}$$

$$\begin{aligned}
& \cup ((VRSpecVia _) (W)) \\
& \quad \cup ((VRSpecToSide _) (W)) \\
& \quad \quad \cup ((VRSemi _) (W)) \\
& \quad \quad \quad \cup ((VREndSemi _) (W)) \\
& \quad \quad \quad \quad \cup ((VRIfThen _) (\\
& VRElfsfInd \times W)) \\
& \quad \quad \quad \quad \quad \cup ((VRIfElse _) (\\
& VRElfsfInd \times W)) \\
& \cup ((VREndIf _) (W)) \\
& \quad \cup ((VRCaseBranch _) (\mathbb{N}_1 \times W)) \\
& \quad \quad \cup ((VRCaseOthers _) (W)) \\
& \quad \quad \quad \cup ((VREndCase _) (W)) \\
& \quad \quad \quad \quad \cup ((VRLoopUndecVia _) (W)) \\
& \quad \quad \quad \quad \quad \cup ((VRLoopUndecToSide _) (W)) \\
& \quad \quad \quad \quad \quad \quad \cup ((VRWhileVia _) (W)) \\
& \quad \quad \quad \quad \quad \quad \quad \cup ((VRWhileWPToSide _) (\\
& \quad \quad \quad \quad \quad \quad \quad \quad W)) \\
& \quad \quad \quad \quad \quad \quad \quad \quad \cup ((VRWhileToSide _) (\\
& \quad \quad \quad \quad \quad \quad \quad \quad \quad W)) \\
& \quad \quad \quad \quad \quad \quad \quad \quad \quad \cup ((VRForVia _) (\\
& \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad W)) \\
& \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \cup ((VRForToSide _) (\\
& W)) \\
& \cup ((VRForExitToSide _) (W)) \\
& \quad \cup (VRExitTillIntro (Z_STRING)) \\
& \quad \quad \cup ((VRExitVia _) (W)) \\
& \quad \quad \quad \cup ((VRProcCall _) (W)) \\
& \quad \quad \quad \quad \cup ((VRProcCallEnd _) (W)) \\
& \quad \quad \quad \quad \quad \cup ((VRLogConToSide _) (W)) \\
& \quad \quad \quad \quad \quad \quad \cup (VRRefinementBegin _) (\\
& \quad \quad \quad \quad \quad \quad \quad W)))))))))))))))))))) \\
& \subseteq W \\
& \quad \bullet VC_Route \subseteq W)
\end{aligned}$$

array_agg2 $\vdash [g1,$
 $g2,$
 $g](array_agg2[g1, g2, g]$
 $\in (g1 \rightarrow g2 \rightarrow g) \rightarrow g1 \times g2 \rightarrow g$
 $\wedge (\forall f : g1 \rightarrow g2 \rightarrow g; x1 : g1; x2 : g2$
 $\bullet array_agg2[g1, g2, g] f (x1, x2) = f x1 x2))$

array_agg3 $\vdash [g1,$
 $g2,$
 $g3,$
 $g](array_agg3[g1, g2, g3, g]$
 $\in (g1 \rightarrow g2 \rightarrow g3 \rightarrow g) \rightarrow g1 \times g2 \times g3 \rightarrow g$
 $\wedge (\forall f : g1 \rightarrow g2 \rightarrow g3 \rightarrow g;$
 $x1 : g1;$
 $x2 : g2;$
 $x3 : g3$
 $\bullet array_agg3[g1, g2, g3, g] f (x1, x2, x3)$
 $= f x1 x2 x3))$

array_agg4 $\vdash [g1,$
 $g2,$
 $g3,$
 $g4,$
 $g](array_agg4[g1, g2, g3, g4, g]$
 $\in (g1 \rightarrow g2 \rightarrow g3 \rightarrow g4 \rightarrow g) \rightarrow g1 \times g2 \times g3 \times g4 \rightarrow g$
 $\wedge (\forall f : g1 \rightarrow g2 \rightarrow g3 \rightarrow g4 \rightarrow g;$
 $x1 : g1;$
 $x2 : g2;$
 $x3 : g3;$
 $x4 : g4$
 $\bullet array_agg4[g1, g2, g3, g4, g] f$
 $(x1, x2, x3, x4)$
 $= f x1 x2 x3 x4))$

array_agg5 $\vdash [g1,$
 $g2,$
 $g3,$
 $g4,$
 $g5,$
 $g](array_agg5[g1, g2, g3, g4, g5, g]$
 $\in (g1 \rightarrow g2 \rightarrow g3 \rightarrow g4 \rightarrow g5 \rightarrow g)$
 $\rightarrow g1 \times g2 \times g3 \times g4 \times g5 \rightarrow g$
 $\wedge (\forall f : g1 \rightarrow g2 \rightarrow g3 \rightarrow g4 \rightarrow g5 \rightarrow g;$
 $x1 : g1;$
 $x2 : g2;$
 $x3 : g3;$
 $x4 : g4;$
 $x5 : g5$
 $\bullet array_agg5[g1, g2, g3, g4, g5, g] f$
 $(x1, x2, x3, x4, x5)$
 $= f x1 x2 x3 x4 x5))$

7.3.5 Definitions

FALSE $\vdash FALSE = 0$
TRUE $\vdash TRUE = 1$
BOOLEAN $\vdash BOOLEAN = FALSE .. TRUE$
BOOLEANvFIRST
 $\vdash BOOLEANvFIRST = FALSE$
BOOLEANvLAST $\vdash BOOLEANvLAST = TRUE$
BOOLEANvSUCC $\vdash BOOLEANvSUCC = (BOOLEAN \setminus \{BOOLEANvLAST\}) \triangleleft succ$
BOOLEANvPRED $\vdash BOOLEANvPRED = BOOLEANvSUCC \sim$
BOOLEANvPOS $\vdash BOOLEANvPOS = id \text{ BOOLEAN}$
BOOLEANvVAL $\vdash BOOLEANvVAL = BOOLEANvPOS \sim$
NATURAL $\vdash NATURAL = 0 .. INTEGERvLAST$
NATURALvFIRST
 $\vdash NATURALvFIRST = 0$
NATURALvLAST $\vdash NATURALvLAST = INTEGERvLAST$
NATURALvSUCC $\vdash NATURALvSUCC = INTEGERvSUCC$
NATURALvPRED $\vdash NATURALvPRED = INTEGERvPRED$
NATURALvPOS $\vdash NATURALvPOS = INTEGERvPOS$

NATURAL_vVAL $\vdash \text{NATURAL}_{v\text{VAL}} = \text{INTEGER}_{v\text{VAL}}$
POSITIVE $\vdash \text{POSITIVE} = 1 .. \text{INTEGER}_{v\text{LAST}}$
POSITIVE_vFIRST
 $\vdash \text{POSITIVE}_{v\text{FIRST}} = 1$
POSITIVE_vLAST
 $\vdash \text{POSITIVE}_{v\text{LAST}} = \text{INTEGER}_{v\text{LAST}}$
POSITIVE_vSUCC
 $\vdash \text{POSITIVE}_{v\text{SUCC}} = \text{INTEGER}_{v\text{SUCC}}$
POSITIVE_vPRED
 $\vdash \text{POSITIVE}_{v\text{PRED}} = \text{INTEGER}_{v\text{PRED}}$
POSITIVE_vPOS $\vdash \text{POSITIVE}_{v\text{POS}} = \text{INTEGER}_{v\text{POS}}$
POSITIVE_vVAL $\vdash \text{POSITIVE}_{v\text{VAL}} = \text{INTEGER}_{v\text{VAL}}$
CHARACTER_vFIRST
 $\vdash \text{CHARACTER}_{v\text{FIRST}} = 0$
CHARACTER $\vdash \text{CHARACTER} = \text{CHARACTER}_{v\text{FIRST}} .. \text{CHARACTER}_{v\text{LAST}}$
CHARACTER_vSUCC
 $\vdash \text{CHARACTER}_{v\text{SUCC}}$
 $= (\text{CHARACTER} \setminus \{\text{CHARACTER}_{v\text{LAST}}\}) \triangleleft \text{succ}$
CHARACTER_vPRED
 $\vdash \text{CHARACTER}_{v\text{PRED}} = \text{CHARACTER}_{v\text{SUCC}} \sim$
CHARACTER_vPOS
 $\vdash \text{CHARACTER}_{v\text{POS}} = \text{id } \text{CHARACTER}$
CHARACTER_vVAL
 $\vdash \text{CHARACTER}_{v\text{VAL}} = \text{CHARACTER}_{v\text{POS}} \sim$
Z_CHAR $\vdash \text{Z_CHAR} = \text{seq } \mathbb{S}$
Z_STRING $\vdash \text{Z_STRING} = \text{seq } \mathbb{S}$
Informal_Function
 $\vdash \text{Informal_Function} = \mathbb{U}$
VRElsfInd $\vdash \text{VRElsfInd} = \mathbb{U}$
VC_Route $\vdash \text{VC_Route} = \mathbb{U}$

7.3.6 Theorems

cn_boolean_thm
 $\vdash \text{BOOLEAN} = \{\text{FALSE}, \text{TRUE}\}$
cn_boolean_succ_thm
 $\vdash \text{BOOLEAN}_{v\text{SUCC}} = \{\text{FALSE} \mapsto \text{TRUE}\}$
cn_boolean_pred_thm
 $\vdash \text{BOOLEAN}_{v\text{PRED}} = \{\text{TRUE} \mapsto \text{FALSE}\}$
cn_boolean_pos_thm
 $\vdash \text{BOOLEAN}_{v\text{POS}} = \text{id } \text{BOOLEAN}$
cn_boolean_val_thm
 $\vdash \text{BOOLEAN}_{v\text{VAL}} = \text{id } \text{BOOLEAN}$
cn_¬_true_eq_false_thm
 $\vdash \neg \text{TRUE} = \text{FALSE}$
cn_boolean_cases_thm
 $\vdash \forall x : \text{BOOLEAN} \bullet x = \text{TRUE} \vee x = \text{FALSE}$
cn_boolean_clauses
 $\vdash \text{TRUE} = \text{Boolean true}$
 $\wedge \text{FALSE} = \text{Boolean false}$
 $\wedge (\forall p : \mathbb{U} \bullet \text{not Boolean } p = \text{Boolean } (\neg p))$

$$\begin{aligned} & \wedge (\forall p, q : \mathbb{U} \\ & \quad \bullet \textit{ Boolean } p \textit{ and Boolean } q = \textit{ Boolean } (p \wedge q)) \\ & \wedge (\forall p, q : \mathbb{U} \\ & \quad \bullet \textit{ Boolean } p \textit{ or Boolean } q = \textit{ Boolean } (p \vee q)) \\ & \wedge (\forall p, q : \mathbb{U} \\ & \quad \bullet \textit{ Boolean } p \textit{ xor Boolean } q = \textit{ Boolean } (\neg (p \Leftrightarrow q))) \\ & \wedge (\forall p, q : \mathbb{U} \bullet \textit{ Boolean } p = \textit{ Boolean } q \Leftrightarrow p \Leftrightarrow q) \end{aligned}$$
cn_and_then_or_else_clauses

$$\begin{aligned} & \vdash \forall x, y : \mathbb{U} \\ & \quad \bullet \textit{ x and_then } y = \textit{ x and } y \wedge \textit{ x or_else } y = \textit{ x or } y \end{aligned}$$
cn_boolean_in_boolean_thm

$$\vdash \forall x : \mathbb{U} \bullet \textit{ Boolean } x \in \textit{ BOOLEAN}$$
cn_relational_clauses

$$\begin{aligned} & \vdash (\forall x : \mathbb{U}; S : \mathbb{U} \bullet \textit{ x mem } S = \textit{ Boolean } (x \in S)) \\ & \wedge (\forall x : \mathbb{U}; S : \mathbb{U} \bullet \textit{ x notmem } S = \textit{ Boolean } (\neg x \in S)) \\ & \wedge (\forall x, y : \mathbb{U} \bullet \textit{ x eq } y = \textit{ Boolean } (x = y)) \\ & \wedge (\forall x, y : \mathbb{U} \bullet \textit{ x noteq } y = \textit{ Boolean } (\neg x = y)) \\ & \wedge (\forall x, y : \mathbb{U} \bullet \textit{ x less } y = \textit{ Boolean } (x < y)) \\ & \wedge (\forall x, y : \mathbb{U} \bullet \textit{ x less_eq } y = \textit{ Boolean } (x \leq y)) \\ & \wedge (\forall x, y : \mathbb{U} \bullet \textit{ x greater } y = \textit{ Boolean } (x > y)) \\ & \wedge (\forall x, y : \mathbb{U} \bullet \textit{ x greater_eq } y = \textit{ Boolean } (x \geq y)) \end{aligned}$$
cn_relational_clauses1

$$\begin{aligned} & \vdash (\forall x, y : \mathbb{U} \bullet \textit{ x real_less } y = \textit{ Boolean } (x <_R y)) \\ & \wedge (\forall x, y : \mathbb{U} \\ & \quad \bullet \textit{ x real_less_eq } y = \textit{ Boolean } (x \leq_R y)) \\ & \wedge (\forall x, y : \mathbb{U} \\ & \quad \bullet \textit{ x real_greater } y = \textit{ Boolean } (x >_R y)) \\ & \wedge (\forall x, y : \mathbb{U} \\ & \quad \bullet \textit{ x real_greater_eq } y = \textit{ Boolean } (x \geq_R y)) \end{aligned}$$
cn_integer_to_real_thm

$$\vdash \forall x : \mathbb{Z} \bullet \textit{ integer_to_real } x = \textit{ real } x$$
cn_boolean_clauses1

$$\begin{aligned} & \vdash (\forall x : \textit{ BOOLEAN} \\ & \quad \bullet \textit{ not } x = \textit{ Boolean } (\neg x = \textit{ Boolean true})) \\ & \wedge (\forall x, y : \textit{ BOOLEAN} \\ & \quad \bullet \textit{ x and } y \\ & \quad = \textit{ Boolean } \\ & \quad \quad (x = \textit{ Boolean true} \wedge y = \textit{ Boolean true})) \\ & \wedge (\forall x, y : \textit{ BOOLEAN} \\ & \quad \bullet \textit{ x or } y \\ & \quad = \textit{ Boolean } \\ & \quad \quad (x = \textit{ Boolean true} \vee y = \textit{ Boolean true})) \\ & \wedge (\forall x, y : \textit{ BOOLEAN} \\ & \quad \bullet \textit{ x xor } y \\ & \quad = \textit{ Boolean } \\ & \quad \quad (\neg x = \textit{ Boolean true} \Leftrightarrow y = \textit{ Boolean true})) \end{aligned}$$
cn_boolean_clauses2

$$\begin{aligned} & \vdash (\forall x : \textit{ BOOLEAN}; p : \mathbb{U} \\ & \quad \bullet \textit{ x and Boolean } p \\ & \quad = \textit{ Boolean } (x = \textit{ Boolean true} \wedge p)) \\ & \wedge (\forall x : \textit{ BOOLEAN}; p : \mathbb{U} \end{aligned}$$

- *Boolean p and x*
= *Boolean (p ∧ x = Boolean true)*)
- ∧ (∀ *x* : *BOOLEAN*; *p* : \mathbb{U})
- *x or Boolean p*
= *Boolean (x = Boolean true ∨ p)*)
- ∧ (∀ *x* : *BOOLEAN*; *p* : \mathbb{U})
- *Boolean p or x*
= *Boolean (p ∨ x = Boolean true)*)
- ∧ (∀ *x* : *BOOLEAN*; *p* : \mathbb{U})
- *x xor Boolean p*
= *Boolean (¬ x = Boolean true ⇔ p)*)
- ∧ (∀ *x* : *BOOLEAN*; *p* : \mathbb{U})
- *Boolean p xor x*
= *Boolean (¬ p ⇔ x = Boolean true)*)

cn_intdiv_0_thm

- $$\vdash \forall j : \mathbb{Z}$$
- $$| \neg$$
- $$j = 0$$
- $0 \text{ intdiv } j = 0 \wedge 0 \text{ rem } j = 0 \wedge 0 \text{ intmod } j = 0$

cn_intdiv_thm

- $$\vdash \forall i, j, k : \mathbb{Z}$$
- $$| \neg$$
- $$j = 0$$
- $i \text{ intdiv } j = k$
⇔ (∃ *m* : \mathbb{Z}
 - $i = k * j + m$
∧ $\text{abs } m < \text{abs } j$
∧ ($0 \leq i \wedge 0 \leq m \vee i < 0 \wedge m \leq 0$))

cn_rem_thm

- $$\vdash \forall i, j, k : \mathbb{Z}$$
- $$| \neg$$
- $$j = 0$$
- $i \text{ rem } j = k$
⇔ (∃ *d* : \mathbb{Z}
 - $i = d * j + k$
∧ $\text{abs } k < \text{abs } j$
∧ ($0 \leq i \wedge 0 \leq k \vee i < 0 \wedge k \leq 0$))

cn_intmod_thm

- $$\vdash \forall i, j, k : \mathbb{Z}$$
- $$| \neg$$
- $$j = 0$$
- $i \text{ intmod } j = k$
⇔ (∃ *d* : \mathbb{Z}
 - $i = d * j + k$
∧ $\text{abs } k < \text{abs } j$
∧ ($0 \leq j \wedge 0 \leq k \vee j < 0 \wedge k \leq 0$))

z_succⁿ_g_thm

- $$\vdash \forall x : \mathbb{U}; y : \mathbb{U}$$
- $\text{succ }^x \text{ }_g y$
= {*a* : \mathbb{U} ; *b* : \mathbb{U}
| ($1 \leq x \wedge 0 \leq a \vee \neg 0 \leq x \wedge 0 - x \leq a \vee x = 0$)
∧ ($a + x, b$) ∈ *y*}

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INDEX

'cn1	48	(_ less_eq _)	57
'cn_reals	49	(_ less _)	57
'cn	48	(_ mem _)[X]	57
(array_not _)[X]	57	(_ mod_and _)	58
(char_lit _)	60	(_ mod_or _)	58
(mod_not _)	58	(_ mod_xor _)	58
(not _)	57	(_ noteq _)[X]	57
(string_lit _)	60	(_ notmem _)[X]	57
(VRAny _)	60	(_ or_else _)	57
(VRAssign _)	60	(_ or _)	57
(VRCaseBranch _)	61	(_ real_greater_eq _)	58
(VRCaseOthers _)	61	(_ real_greater _)	58
(VREndCase _)	61	(_ real_less_eq _)	58
(VREndIf _)	61	(_ real_less _)	58
(VREndSemi _)	61	(_ rem _)	58
(VRExitVia _)	61	(_ xor _)	57
(VRForExitToSide _)	61	(_ &_0 _)[X]	60
(VRForToSide _)	61	(_ &_1 _)[X]	60
(VRForVia _)	61	(_ &_2 _)[X]	60
(VRIfElse _)	61	all_cn_make_script_support	56
(VRIfThen _)	61	array_agg2[g1, g2, g]	62
(VRLogConToSide _)	62	array_agg2	71
(VRLoopUndecToSide _)	61	array_agg3[g1, g2, g3, g]	62
(VRLoopUndecVia _)	61	array_agg3	71
(VRNull _)	60	array_agg4[g1, g2, g3, g4, g]	62
(VRProcCallEnd _)	61	array_agg4	72
(VRProcCall _)	61	array_agg5[g1, g2, g3, g4, g5, g]	62
(VRRefinementBegin _)	62	array_agg5	72
(VRSemi _)	61	array_agg_def	45
(VRSpecToSide _)	60	array_not _	63
(VRSpecVia _)	60	BOOLEANvFIRST	57
(VRWhileToSide _)	61	BOOLEANvFIRST	72
(VRWhileVia _)	61	BOOLEANvLAST	57
(VRWhileWPTToSide _)	61	BOOLEANvLAST	72
(_ ** _)	58	BOOLEANvPOS	57
(_ and_then _)	57	BOOLEANvPOS	72
(_ and _)	57	BOOLEANvPRED	57
(_ array_and _)[X]	57	BOOLEANvPRED	72
(_ array_greater_eq _)	58	BOOLEANvSUCC	57
(_ array_greater _)	58	BOOLEANvSUCC	72
(_ array_less_eq _)	58	BOOLEANvVAL	57
(_ array_less _)	58	BOOLEANvVAL	72
(_ array_or _)[X]	57	BOOLEAN	57
(_ array_xor _)[X]	57	Boolean	60
(_ eq _)[X]	57	Boolean	68
(_ greater_eq _)	57	BOOLEAN	72
(_ greater _)	57	browse_vcs	42
(_ intdiv _)	58	CHARACTERvFIRST	60
(_ intmod _)	58	CHARACTERvFIRST	73

<i>CHARACTERvLAST</i>	60	<i>cn_mod_and_conv</i>	48
<i>CHARACTERvLAST</i>	67	<i>cn_mod_not_conv</i>	48
<i>CHARACTERvPOS</i>	60	<i>cn_mod_or_conv</i>	48
<i>CHARACTERvPOS</i>	73	<i>cn_mod_xor_conv</i>	48
<i>CHARACTERvPRED</i>	60	<i>cn_no_domain_conds</i>	44
<i>CHARACTERvPRED</i>	73	<i>cn_relational_clauses1</i>	46
<i>CHARACTERvSUCC</i>	60	<i>cn_relational_clauses1</i>	74
<i>CHARACTERvSUCC</i>	73	<i>cn_relational_clauses</i>	46
<i>CHARACTERvVAL</i>	60	<i>cn_relational_clauses</i>	74
<i>CHARACTERvVAL</i>	73	<i>cn_rem_conv</i>	47
<i>CHARACTER</i>	60	<i>cn_rem_thm</i>	46
<i>CHARACTER</i>	73	<i>cn_rem_thm</i>	75
<i>char_lit</i>	67	<i>cn_script_support_thms</i>	54
<i>cn1_ext</i>	49	<i>cn_show_typing_context</i>	42
<i>cn1</i>	49	<i>cn_simplify_canon</i>	53
<i>CNTactics</i>	49	<i>cn_spark_annotation_char</i>	44
<i>CNTheoryProofSupport</i>	51	<i>cn_spark_syntax_warnings</i>	44
<i>CNToolkitExtensions</i>	46	<i>cn_spec_rule</i>	55
<i>cn_all_domain_conds</i>	44	<i>cn_standard_domain_conds</i>	44
<i>cn_and_then_or_else_clauses</i>	46	<i>cn_star_star_conv</i>	47
<i>cn_and_then_or_else_clauses</i>	74	<i>cn_stop_on_exceptions</i>	43
<i>cn_automatic_line_splitting</i>	43	<i>cn_syntax_check_only</i>	43
<i>cn_boolean_cases_thm</i>	46	<i>cn_tab_width</i>	43
<i>cn_boolean_cases_thm</i>	73	<i>cn_use_let_in_vcs</i>	42
<i>cn_boolean_clauses1</i>	46	<i>cn_vc_simp_tac</i>	50
<i>cn_boolean_clauses1</i>	74	<i>cn_∈_type_tac</i>	51
<i>cn_boolean_clauses2</i>	46	<i>cn_¬_true_eq_false_thm</i>	46
<i>cn_boolean_clauses2</i>	74	<i>cn_¬_true_eq_false_thm</i>	73
<i>cn_boolean_clauses</i>	46	<i>cn</i>	49
<i>cn_boolean_clauses</i>	73	<i>ComplianceTool</i>	39
<i>cn_boolean_pos_thm</i>	46	<i>current_pc_z_∈_net</i>	50
<i>cn_boolean_pos_thm</i>	73	<i>delete_deferred_subprogram</i>	40
<i>cn_boolean_pred_thm</i>	46	<i>delete_exception_log</i>	39
<i>cn_boolean_pred_thm</i>	73	<i>delete_script</i>	40
<i>cn_boolean_succ_thm</i>	46	<i>dest_char</i>	60
<i>cn_boolean_succ_thm</i>	73	<i>dest_char</i>	67
<i>cn_boolean_thm</i>	46	<i>dest_cn_intdiv</i>	47
<i>cn_boolean_thm</i>	73	<i>dest_cn_intmod</i>	47
<i>cn_boolean_val_thm</i>	46	<i>dest_cn_rem</i>	47
<i>cn_boolean_val_thm</i>	73	<i>dest_cn_star_star</i>	47
<i>cn_boolean_∈_boolean_thm</i>	46	<i>EVAL_REPORT</i>	41
<i>cn_boolean_∈_boolean_thm</i>	74	<i>FALSE</i>	57
<i>cn_case_of_ada_keywords</i>	42	<i>FALSE</i>	72
<i>cn_compactify_terms</i>	43	<i>FLOATvDIGITS</i>	59
<i>cn_ext</i>	49	<i>FLOATvDIGITS</i>	67
<i>cn_ignore_spark_annotations</i>	44	<i>FLOATvFIRST</i>	59
<i>cn_intdiv_0_thm</i>	46	<i>FLOATvFIRST</i>	67
<i>cn_intdiv_0_thm</i>	75	<i>FLOATvLAST</i>	59
<i>cn_intdiv_conv</i>	47	<i>FLOATvLAST</i>	67
<i>cn_intdiv_thm</i>	46	<i>FLOAT</i>	59
<i>cn_intdiv_thm</i>	75	<i>FLOAT</i>	67
<i>cn_integer_to_real_thm</i>	46	<i>fun 0 rightassoc</i>	62
<i>cn_integer_to_real_thm</i>	74	<i>fun 10 rightassoc</i>	62
<i>cn_intmod_conv</i>	47	<i>fun 20 rightassoc</i>	62
<i>cn_intmod_thm</i>	46	<i>fun 30 rightassoc</i>	63
<i>cn_intmod_thm</i>	75	<i>fun 40 rightassoc</i>	63
<i>cn_left_margin</i>	43	<i>fun 50 rightassoc</i>	63
<i>cn_make_script_support</i>	56	<i>get_eval_report</i>	41

<i>get_pc_z_ε_rules</i>	50	<i>NATURALvPOS</i>	58
<i>get_script_theories</i>	41	<i>NATURALvPOS</i>	72
<i>Informal_Function</i>	60	<i>NATURALvPRED</i>	58
<i>Informal_Function</i>	73	<i>NATURALvPRED</i>	72
<i>INTEGERvFIRST</i>	58	<i>NATURALvSUCC</i>	58
<i>INTEGERvFIRST</i>	66	<i>NATURALvSUCC</i>	72
<i>INTEGERvLAST</i>	58	<i>NATURALvVAL</i>	58
<i>INTEGERvLAST</i>	66	<i>NATURALvVAL</i>	73
<i>INTEGERvPOS</i>	58	<i>NATURAL</i>	58
<i>INTEGERvPOS</i>	66	<i>NATURAL</i>	72
<i>INTEGERvPRED</i>	58	<i>new_continuation_script1</i>	40
<i>INTEGERvPRED</i>	66	<i>new_continuation_script</i>	40
<i>INTEGERvSUCC</i>	58	<i>new_script1</i>	40
<i>INTEGERvSUCC</i>	66	<i>new_script</i>	40
<i>INTEGERvVAL</i>	58	<i>not</i>	63
<i>INTEGERvVAL</i>	66	<i>open_scope</i>	40
<i>integer_to_real</i>	58	<i>output_ada_program</i>	41
<i>integer_to_real</i>	65	<i>output_eval_report1</i>	41
<i>INTEGER</i>	58	<i>output_eval_report</i>	41
<i>INTEGER</i>	66	<i>output_exception_log</i>	39
<i>is_cn_intdiv</i>	47	<i>output_hypertext_edit_script</i>	42
<i>is_cn_intmod</i>	47	<i>output_z_document</i>	41
<i>is_cn_rem</i>	47	<i>pc_z_ε_rules_of_thms</i>	50
<i>is_cn_star_star</i>	47	<i>POSITIVEvFIRST</i>	58
<i>list_cn_make_script_support</i>	56	<i>POSITIVEvFIRST</i>	73
<i>list_cn_script_support_thms</i>	54	<i>POSITIVEvLAST</i>	58
<i>list_cn_spec_rule</i>	55	<i>POSITIVEvLAST</i>	73
<i>LONG_FLOATvDIGITS</i>	60	<i>POSITIVEvPOS</i>	59
<i>LONG_FLOATvDIGITS</i>	67	<i>POSITIVEvPOS</i>	73
<i>LONG_FLOATvFIRST</i>	60	<i>POSITIVEvPRED</i>	59
<i>LONG_FLOATvFIRST</i>	67	<i>POSITIVEvPRED</i>	73
<i>LONG_FLOATvLAST</i>	60	<i>POSITIVEvSUCC</i>	59
<i>LONG_FLOATvLAST</i>	67	<i>POSITIVEvSUCC</i>	73
<i>LONG_FLOAT</i>	60	<i>POSITIVEvVAL</i>	59
<i>LONG_FLOAT</i>	67	<i>POSITIVEvVAL</i>	73
<i>LONG_INTEGERvFIRST</i>	59	<i>POSITIVE</i>	58
<i>LONG_INTEGERvFIRST</i>	66	<i>POSITIVE</i>	73
<i>LONG_INTEGERvLAST</i>	59	<i>print_ada_program</i>	41
<i>LONG_INTEGERvLAST</i>	66	<i>print_eval_report</i>	41
<i>LONG_INTEGERvPOS</i>	59	<i>print_exception_log</i>	39
<i>LONG_INTEGERvPOS</i>	66	<i>print_z_document</i>	41
<i>LONG_INTEGERvPRED</i>	59	<i>real_to_integer</i>	58
<i>LONG_INTEGERvPRED</i>	66	<i>real_to_integer</i>	65
<i>LONG_INTEGERvSUCC</i>	59	<i>set_pc_z_ε_rules</i>	50
<i>LONG_INTEGERvSUCC</i>	66	<i>SHORT_FLOATvDIGITS</i>	60
<i>LONG_INTEGERvVAL</i>	59	<i>SHORT_FLOATvDIGITS</i>	67
<i>LONG_INTEGERvVAL</i>	66	<i>SHORT_FLOATvFIRST</i>	60
<i>LONG_INTEGER</i>	59	<i>SHORT_FLOATvFIRST</i>	67
<i>LONG_INTEGER</i>	66	<i>SHORT_FLOATvLAST</i>	60
<i>LONG_INTEGER</i>	66	<i>SHORT_FLOATvLAST</i>	67
<i>mk_cn_intdiv</i>	47	<i>SHORT_FLOAT</i>	60
<i>mk_cn_intmod</i>	47	<i>SHORT_FLOAT</i>	67
<i>mk_cn_rem</i>	47	<i>SHORT_INTEGERvFIRST</i>	59
<i>mk_cn_star_star</i>	47	<i>SHORT_INTEGERvFIRST</i>	66
<i>mod_not</i>	66	<i>SHORT_INTEGERvLAST</i>	59
<i>NATURALvFIRST</i>	58	<i>SHORT_INTEGERvLAST</i>	66
<i>NATURALvFIRST</i>	72	<i>SHORT_INTEGERvPOS</i>	59
<i>NATURALvLAST</i>	58	<i>SHORT_INTEGERvPOS</i>	66
<i>NATURALvLAST</i>	72	<i>SHORT_INTEGERvPOS</i>	66

<i>SHORT_INTEGERvPRED</i>	59	<i>VRLogConToSide</i> _	69
<i>SHORT_INTEGERvPRED</i>	66	<i>VRLogConTypeMemIntro</i>	62
<i>SHORT_INTEGERvSUCC</i>	59	<i>VRLogConTypeMemIntro</i>	69
<i>SHORT_INTEGERvSUCC</i>	66	<i>VRLoopUndecPreIntro</i>	61
<i>SHORT_INTEGERvVAL</i>	59	<i>VRLoopUndecPreIntro</i>	68
<i>SHORT_INTEGERvVAL</i>	66	<i>VRLoopUndecPreToSide</i>	61
<i>SHORT_INTEGER</i>	59	<i>VRLoopUndecPreToSide</i>	68
<i>SHORT_INTEGER</i>	66	<i>VRLoopUndecToSide</i> _	68
<i>slide[X, Y]</i>	60	<i>VRLoopUndecVia</i> _	68
<i>slide</i>	68	<i>VRNull</i> _	68
<i>string_lit</i> _	67	<i>VRProcCallEnd</i> _	69
<i>STRING</i>	60	<i>VRProcCallRngIntro</i>	61
<i>STRING</i>	67	<i>VRProcCallRngIntro</i>	69
<i>TRUE</i>	57	<i>VRProcCall</i> _	69
<i>TRUE</i>	72	<i>VRRefinementBegin</i> _	69
<i>universal_discretevFIRST</i>	59	<i>VRRefinementIntro</i>	62
<i>universal_discretevFIRST</i>	67	<i>VRRefinementIntro</i>	69
<i>universal_discretevLAST</i>	59	<i>VRReturnIntro</i>	61
<i>universal_discretevLAST</i>	67	<i>VRReturnIntro</i>	69
<i>universal_discretevPOS</i>	59	<i>VRSemi</i> _	68
<i>universal_discretevPOS</i>	67	<i>VRSpecPreIntro</i>	61
<i>universal_discretevPRED</i>	59	<i>VRSpecPreIntro</i>	68
<i>universal_discretevPRED</i>	67	<i>VRSpecToSide</i> _	68
<i>universal_discretevSUCC</i>	59	<i>VRSpecVia</i> _	68
<i>universal_discretevSUCC</i>	67	<i>VRWhilePreIntro</i>	61
<i>universal_discretevVAL</i>	59	<i>VRWhilePreIntro</i>	68
<i>universal_discretevVAL</i>	67	<i>VRWhileToSide</i> _	68
<i>universal_discrete</i>	59	<i>VRWhileVia</i> _	68
<i>universal_discrete</i>	67	<i>VRWhileWPToSide</i> _	68
<i>VC_Route</i>	60	<i>Z_CHAR</i>	60
<i>VC_Route</i>	73	<i>Z_CHAR</i>	73
<i>VRAny</i> _	68	<i>z_norm_sig_h_schema_conv</i>	52
<i>VRAssign</i> _	68	<i>Z_STRING</i>	60
<i>VRCaseBranch</i> _	68	<i>Z_STRING</i>	73
<i>VRCaseOthers</i> _	68	<i>z_succⁿ_g_thm</i>	46
<i>VRElseFalse</i>	60	<i>z_succⁿ_g_thm</i>	75
<i>VRElseFalse</i>	68	<i>_ ** _</i>	65
<i>VRElseInd</i>	60	<i>_ and_then _</i>	63
<i>VRElseInd</i>	73	<i>_ and _</i>	63
<i>VRElseTrue</i>	60	<i>_ array_and _</i>	63
<i>VRElseTrue</i>	68	<i>_ array_greater_eq _</i>	64
<i>VREndCase</i> _	68	<i>_ array_greater _</i>	64
<i>VREndIf</i> _	68	<i>_ array_less_eq _</i>	64
<i>VREndSemi</i> _	68	<i>_ array_less _</i>	64
<i>VRExitTillIntro</i>	61	<i>_ array_or _</i>	63
<i>VRExitTillIntro</i>	68	<i>_ array_xor _</i>	63
<i>VRExitVia</i> _	69	<i>_ eq _</i>	63
<i>VRForExitToSide</i> _	68	<i>_ greater_eq _</i>	64
<i>VRForPreIntro</i>	61	<i>_ greater _</i>	64
<i>VRForPreIntro</i>	68	<i>_ intdiv _</i>	65
<i>VRForPreToSide</i>	61	<i>_ intmod _</i>	65
<i>VRForPreToSide</i>	68	<i>_ less_eq _</i>	64
<i>VRForToSide</i> _	68	<i>_ less _</i>	64
<i>VRForVia</i> _	68	<i>_ mem _</i>	63
<i>VRIfElse</i> _	68	<i>_ mod_and _</i>	65
<i>VRIfThen</i> _	68	<i>_ mod_or _</i>	65
<i>VRLogConPreIntro</i>	62	<i>_ mod_xor _</i>	65
<i>VRLogConPreIntro</i>	69	<i>_ noteq _</i>	63

- <code>notmem</code> -	63
- <code>or_else</code> -	63
- <code>or</code> -	63
- <code>real_greater_eq</code> -	64
- <code>real_greater</code> -	64
- <code>real_less_eq</code> -	64
- <code>real_less</code> -	64
- <code>rem</code> -	65
- <code>xor</code> -	63
- <code>&_0</code> -	67
- <code>&_1</code> -	67
- <code>&_2</code> -	67